

Modal Crash Types for Intermittent Computing

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Joint work with Myra Dotzel, Milijana Surbatovich, and Limin Jia

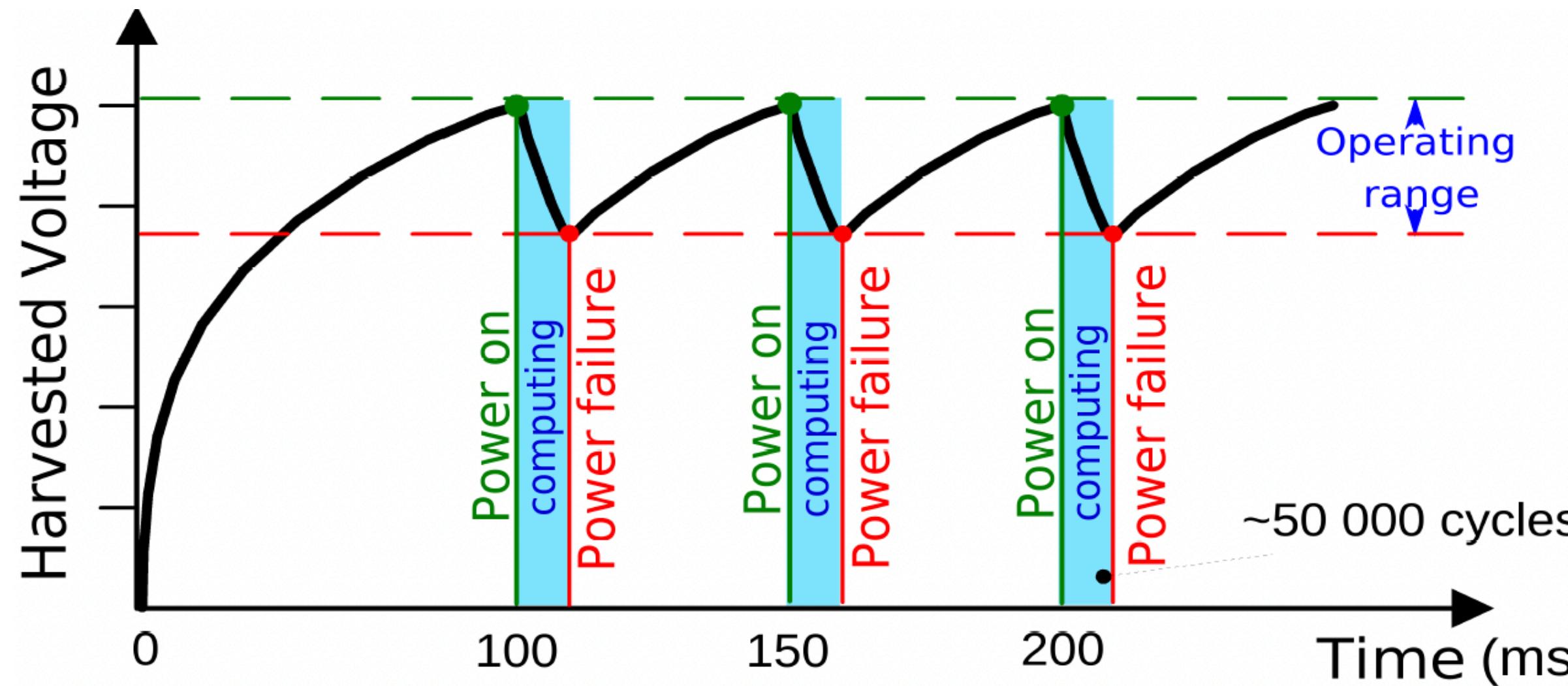
ESOP 2023

Intermittent execution in energy harvesting devices

Devices powered with energy from the environment (e.g., solar)

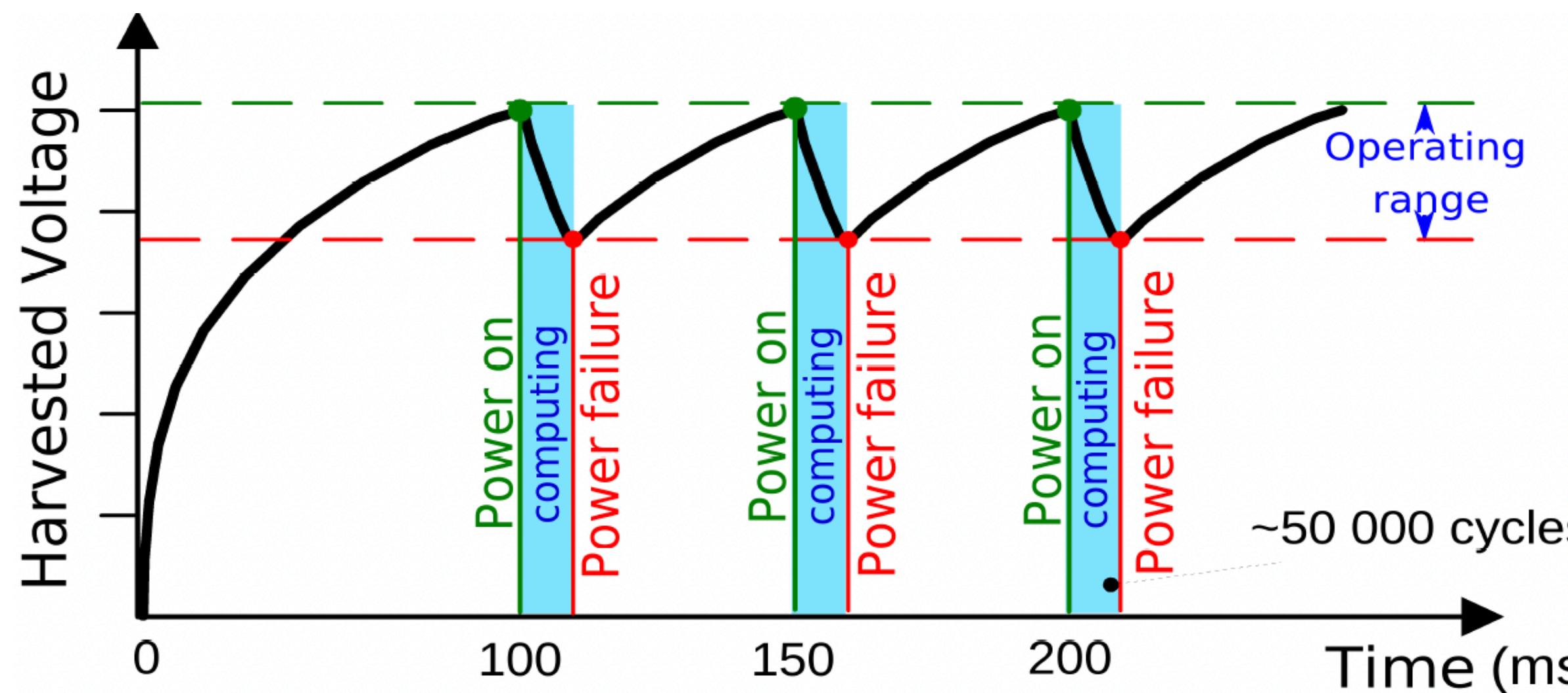
Intermittent execution in energy harvesting devices

Devices powered with energy from the environment (e.g., solar)



Intermittent execution in energy harvesting devices

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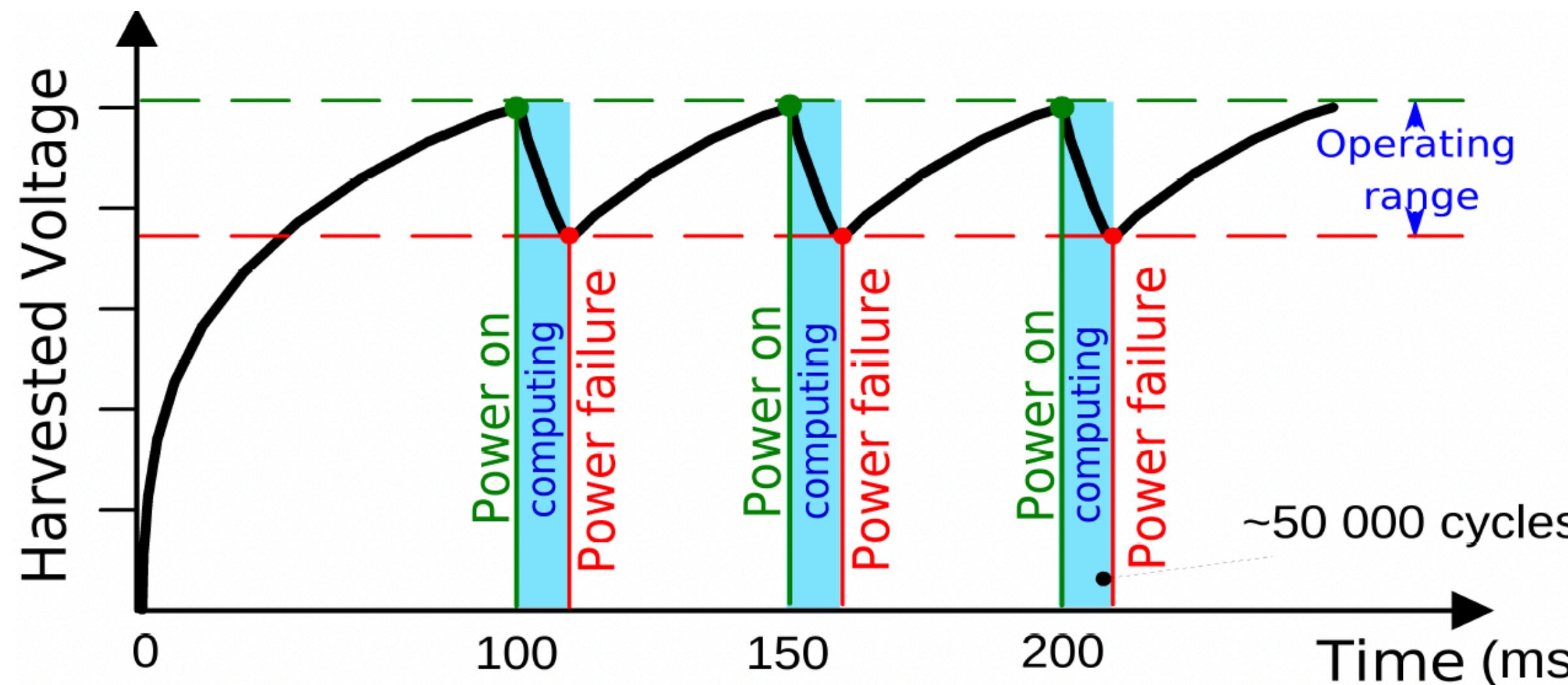
Examples



- Harsh environments
- Space, medical devices
- Tiny devices w/o battery

Intermittent execution in energy harvesting devices

Devices powered with energy from the environment (e.g., solar)



Examples



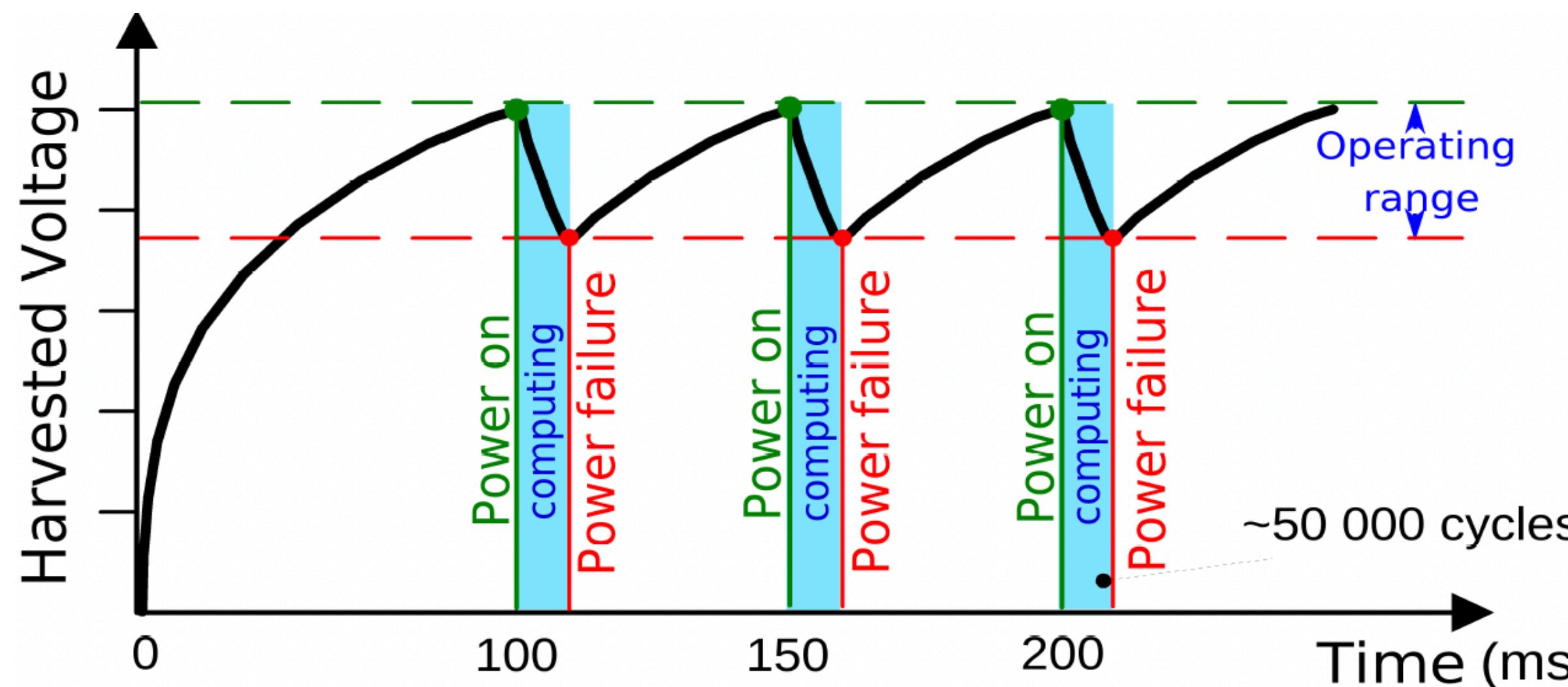
- Harsh environments
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Energy is available intermittently →

Executions are intermittent

Intermittent execution in energy harvesting devices

Devices powered with energy from the environment (e.g., solar)



Examples



- Harsh environments
- Space, medical devices
- Tiny devices w/o battery

Energy is available intermittently →

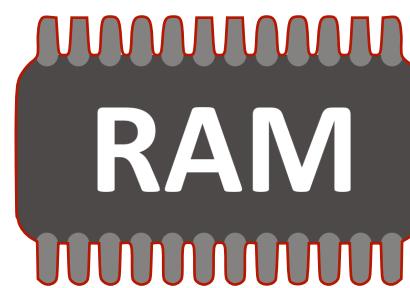
Executions are intermittent

We need to handle frequent power failures!

Volatile and nonvolatile memories with stable and unstable types



Nonvolatile memory

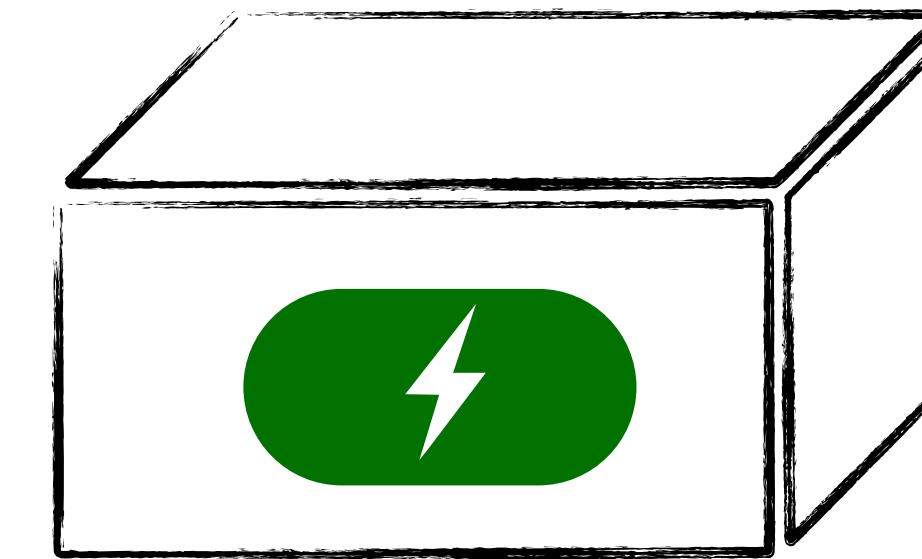


Volatile memory

ℓ_1	ℓ_2
0	0

pc →

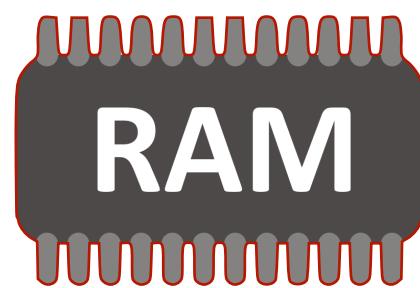
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let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```



Volatile and nonvolatile memories with stable and unstable types



Nonvolatile memory



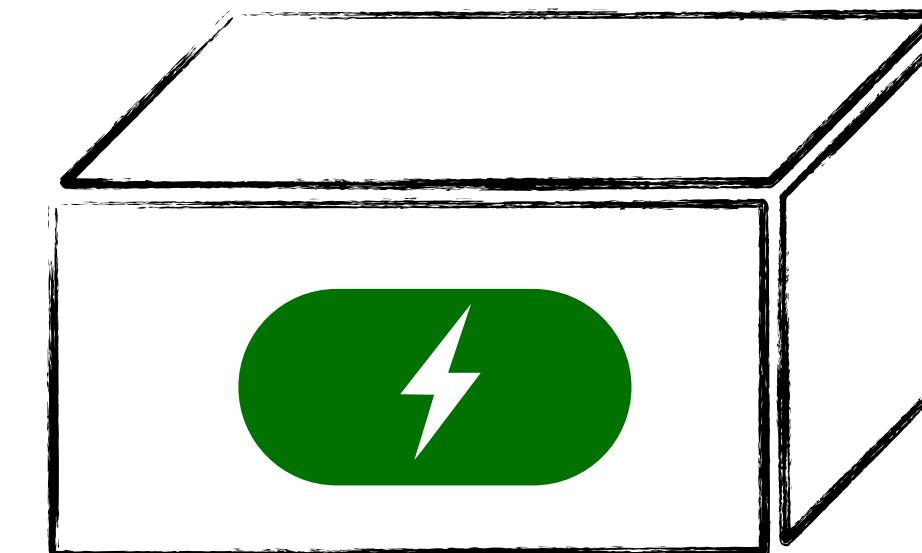
Volatile memory

Final memory
state we expect:

ℓ_1	ℓ_2
0	0

pc →

```
let w:=2 in  
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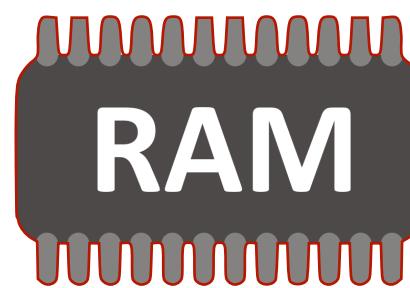


ℓ_1	ℓ_2
2	2

Volatile and nonvolatile memories with stable and unstable types



Nonvolatile memory



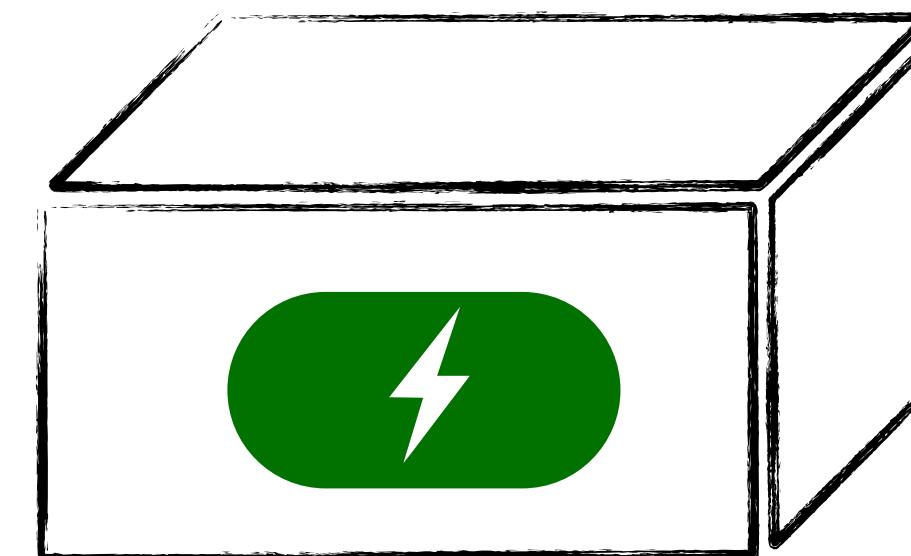
Volatile memory

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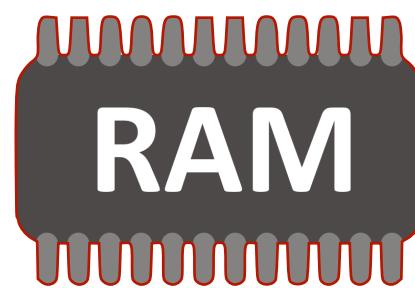


ℓ_1	ℓ_2
2	2

Volatile and nonvolatile memories with stable and unstable types



Nonvolatile memory

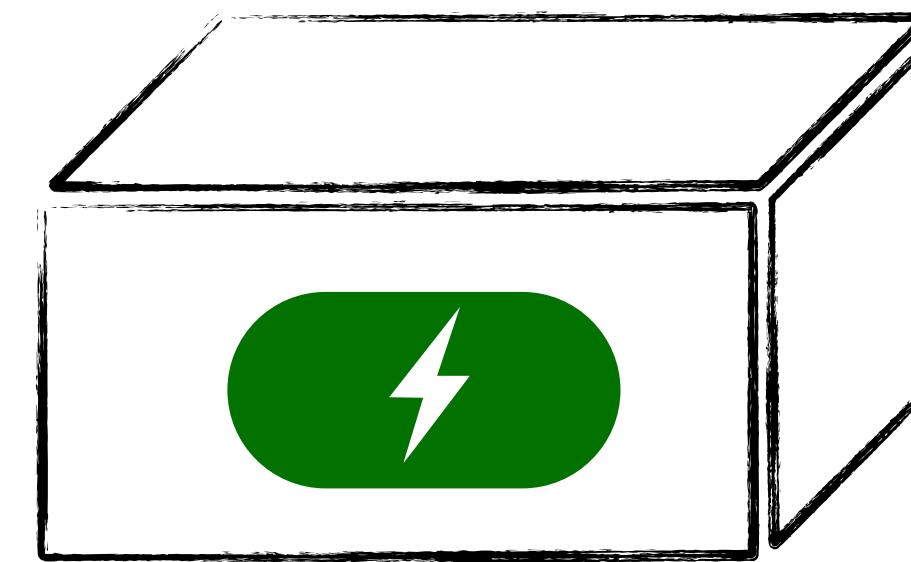


Volatile memory

ℓ_1	ℓ_2
0	0

pc →

```
let w:=2 in  
L1:= w+L1  
L2:= L2+L1
```



w	2
---	---

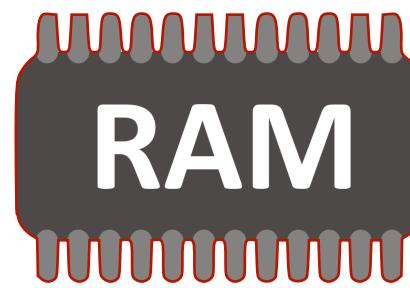
Final memory
state we expect:

ℓ_1	ℓ_2
2	2

Volatile and nonvolatile memories with stable and unstable types



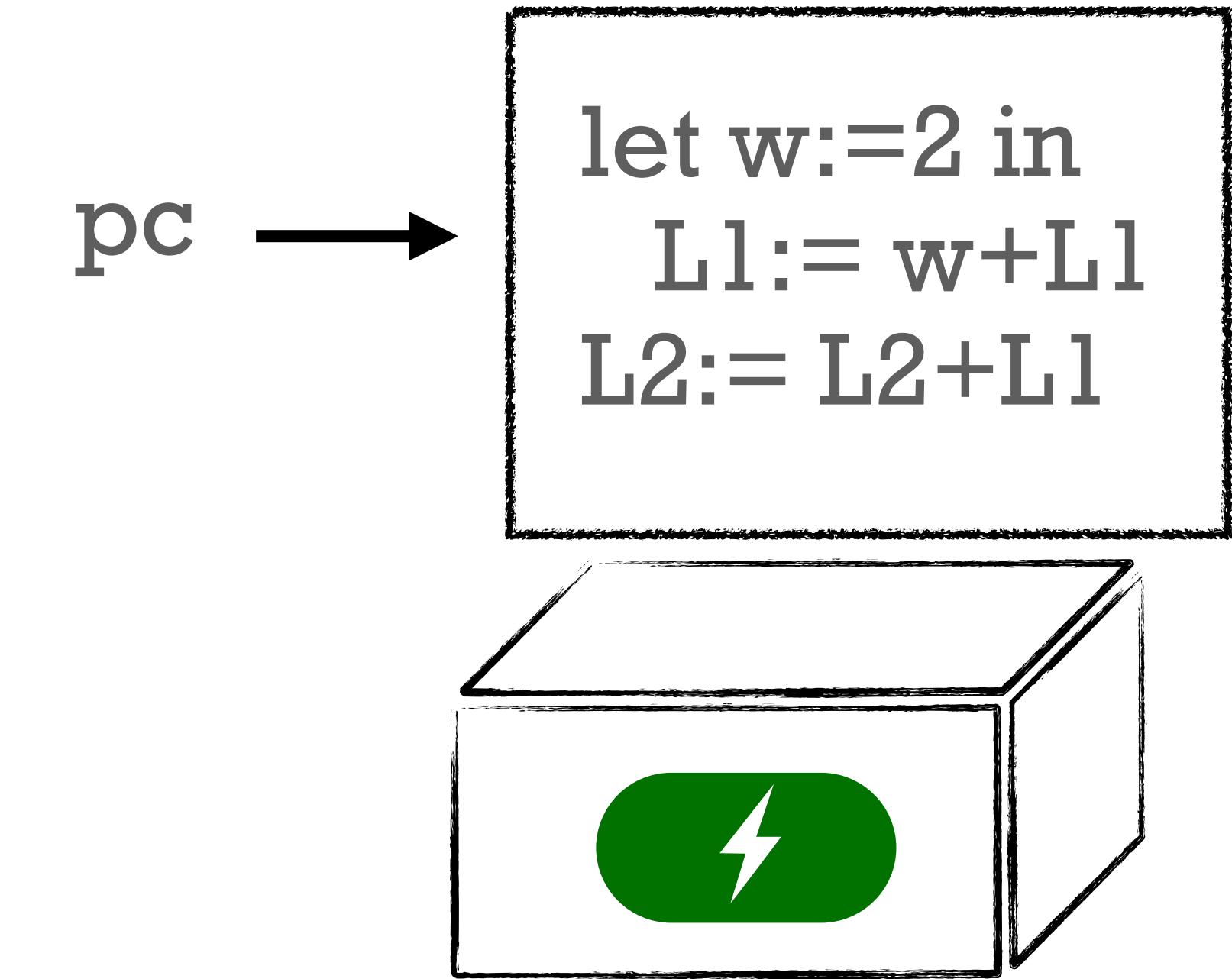
Nonvolatile memory



Volatile memory

ℓ_1	ℓ_2
0	0

w	2
---	---

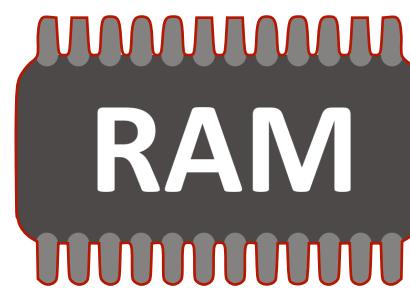


Final memory state we expect:	ℓ_1	ℓ_2
	2	2

Volatile and nonvolatile memories with stable and unstable types



Nonvolatile memory

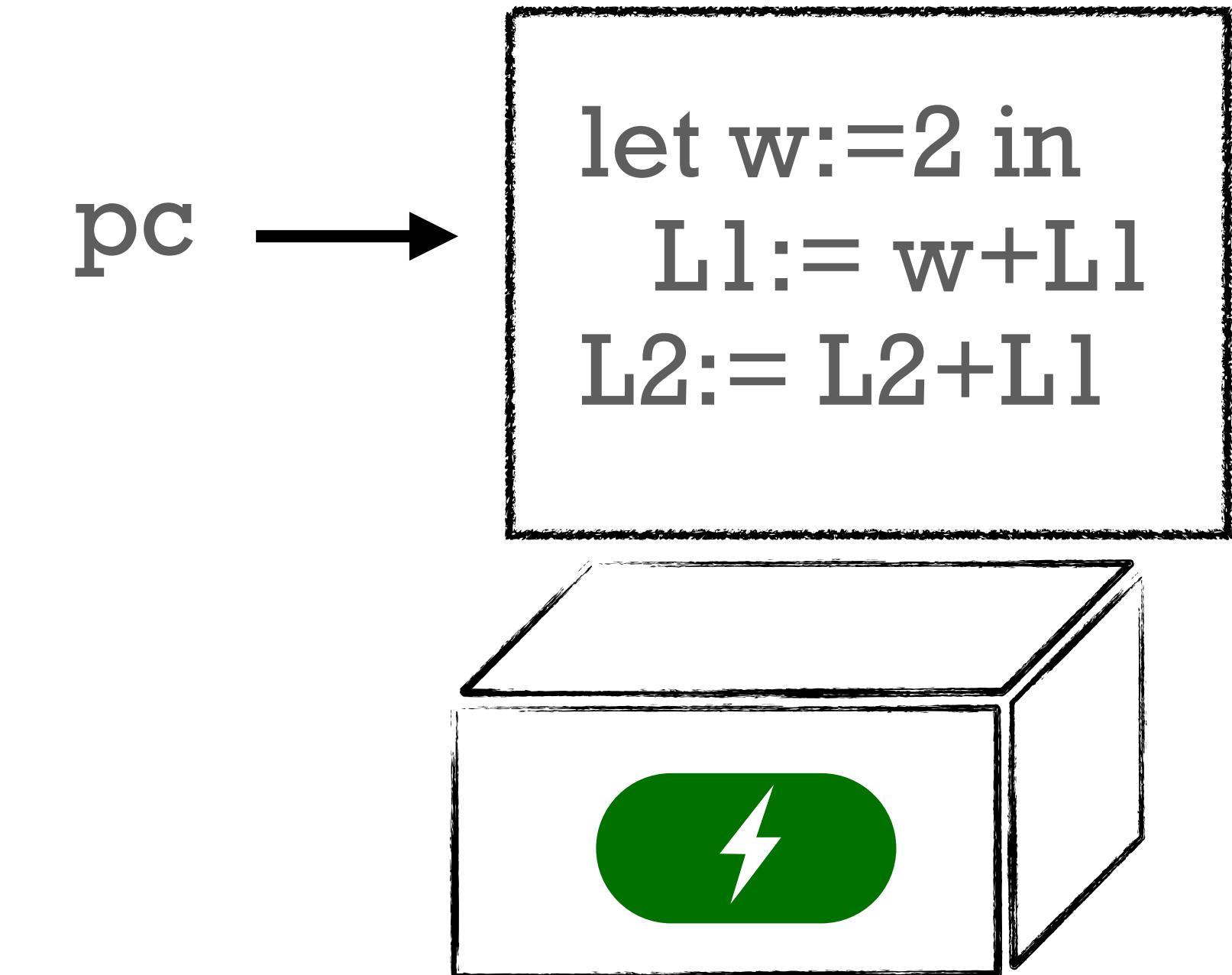


Volatile memory

Final memory
state we expect:

ℓ_1	ℓ_2
2	0

w
2

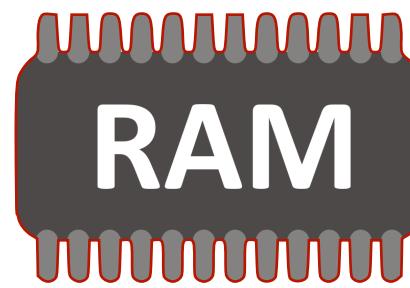


ℓ_1	ℓ_2
2	2

Volatile and nonvolatile memories with stable and unstable types



Nonvolatile memory

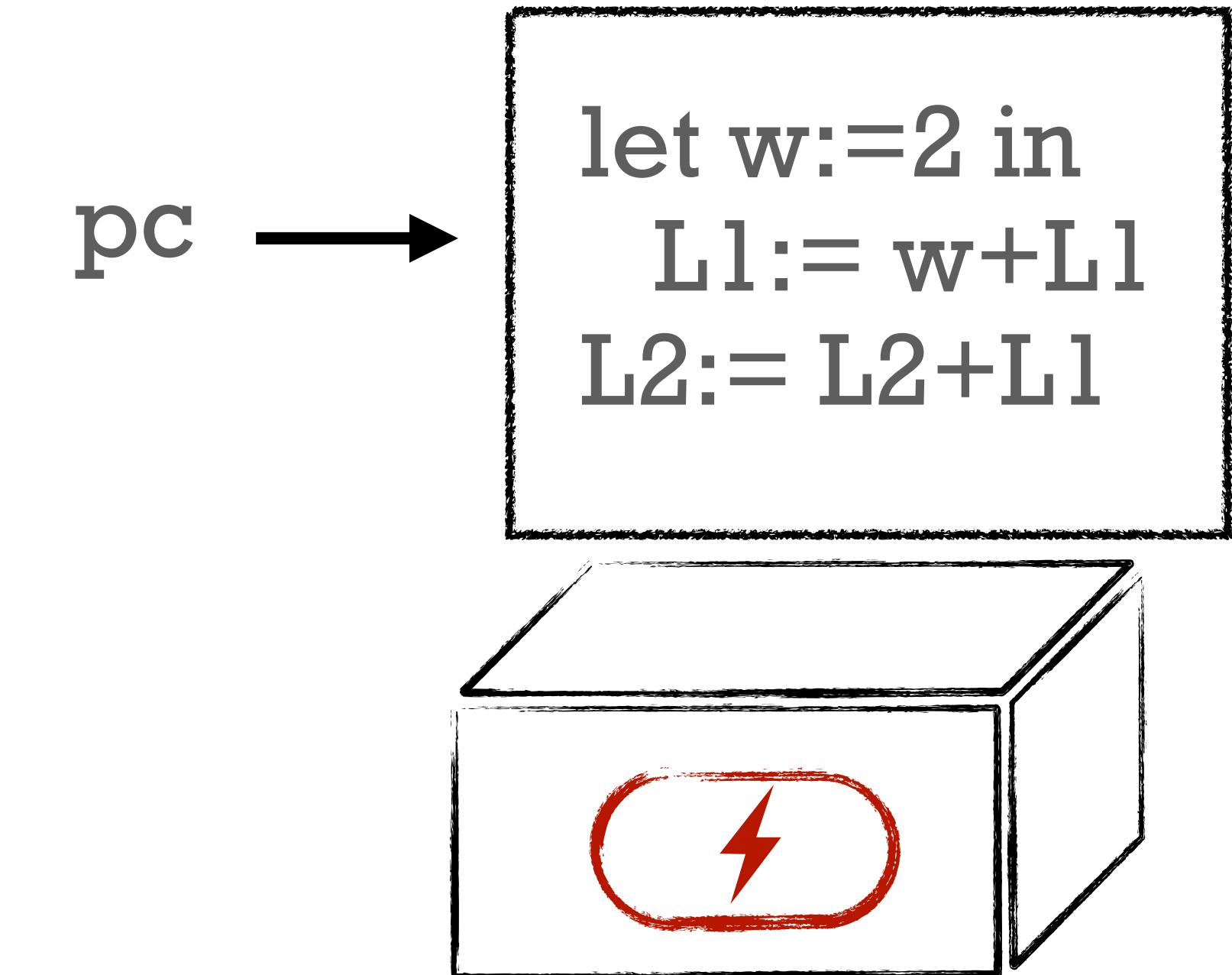


Volatile memory

Final memory
state we expect:

ℓ_1	ℓ_2
2	0

w
2

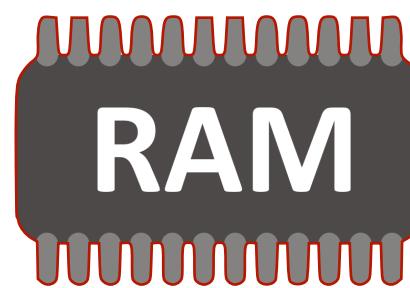


ℓ_1	ℓ_2
2	2

Volatile and nonvolatile memories with stable and unstable types



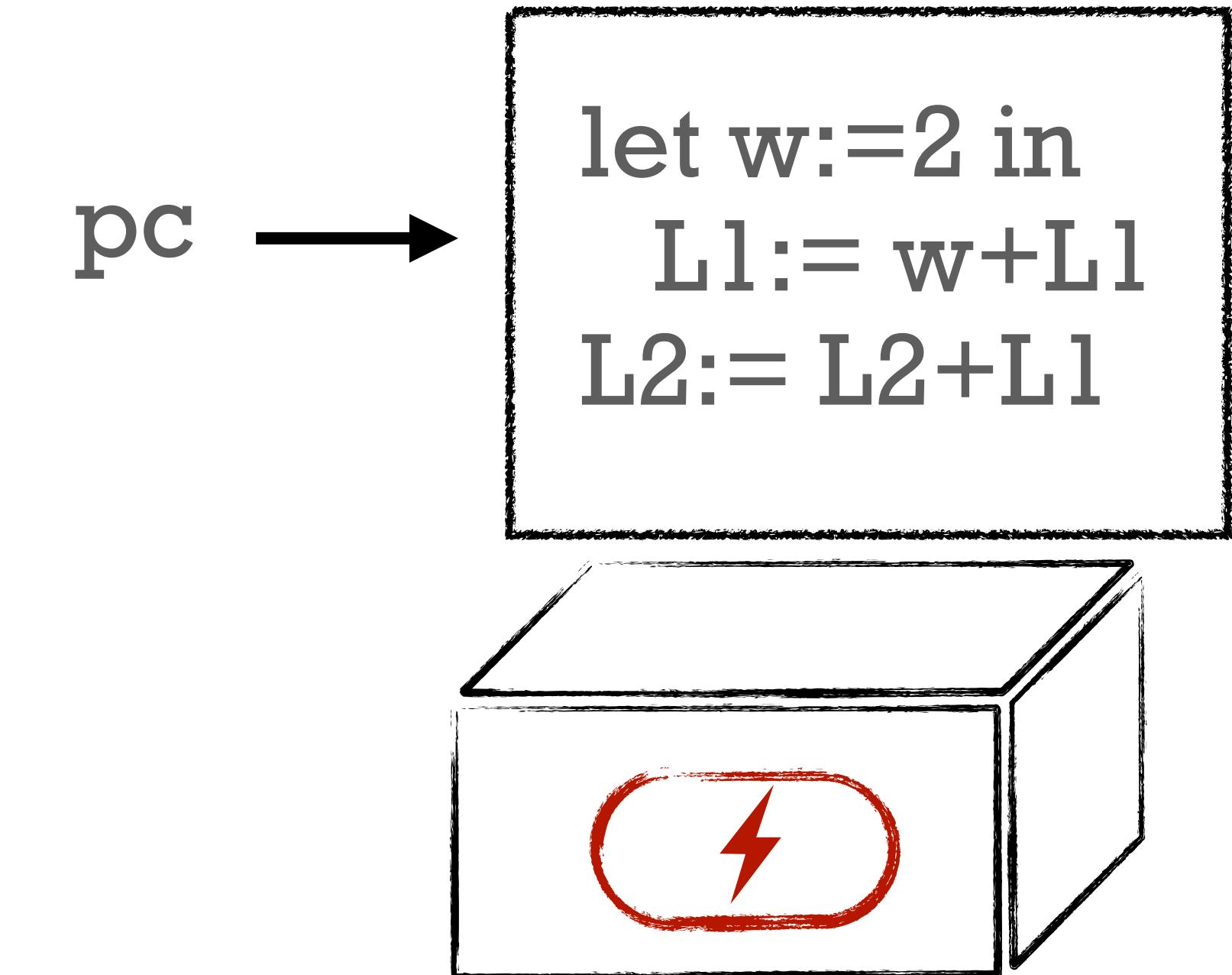
Nonvolatile memory
Stable values



Volatile memory

ℓ_1	ℓ_2
2	0

w
2



ℓ_1	ℓ_2
2	2

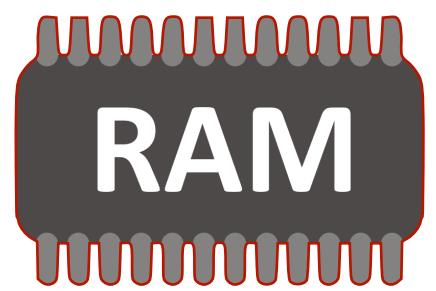
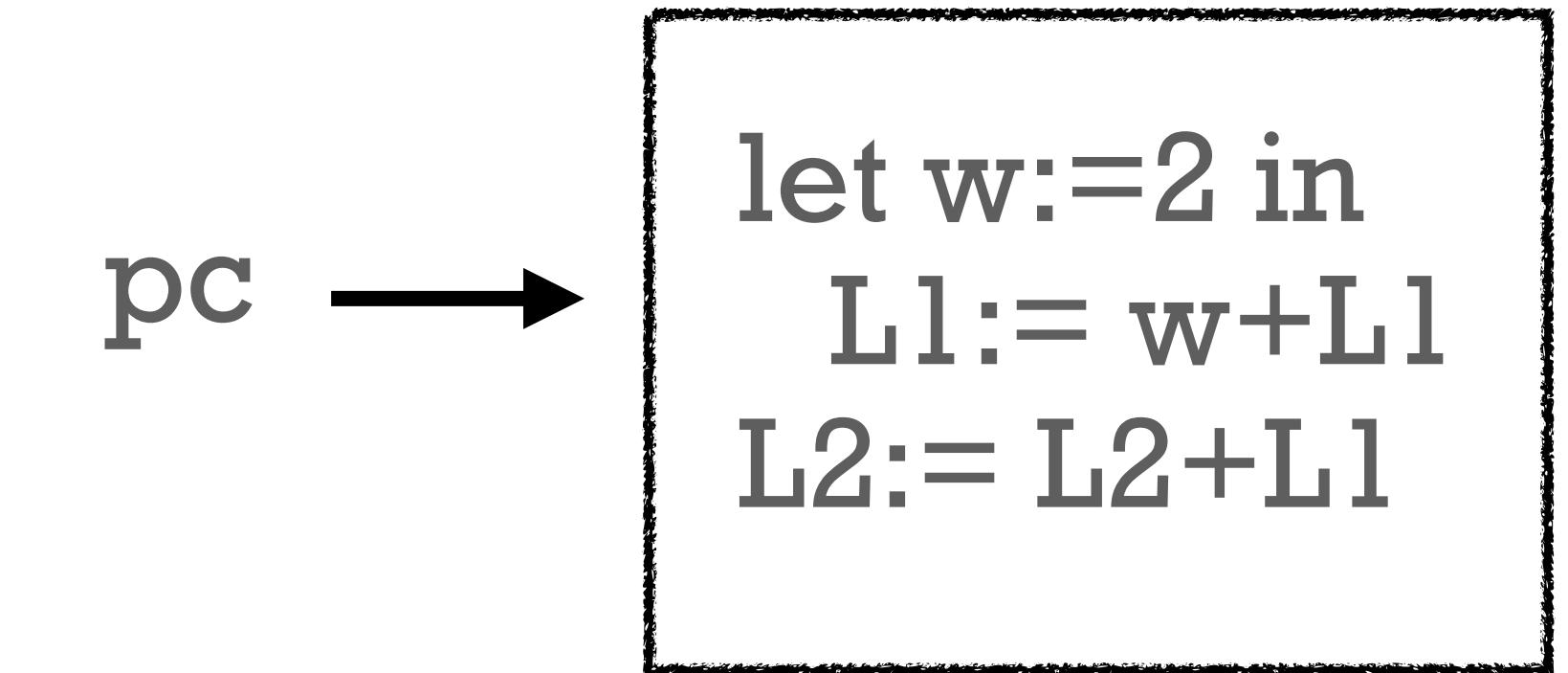
Final memory
state we expect:

Volatile and nonvolatile memories with stable and unstable types



Nonvolatile memory
Stable values

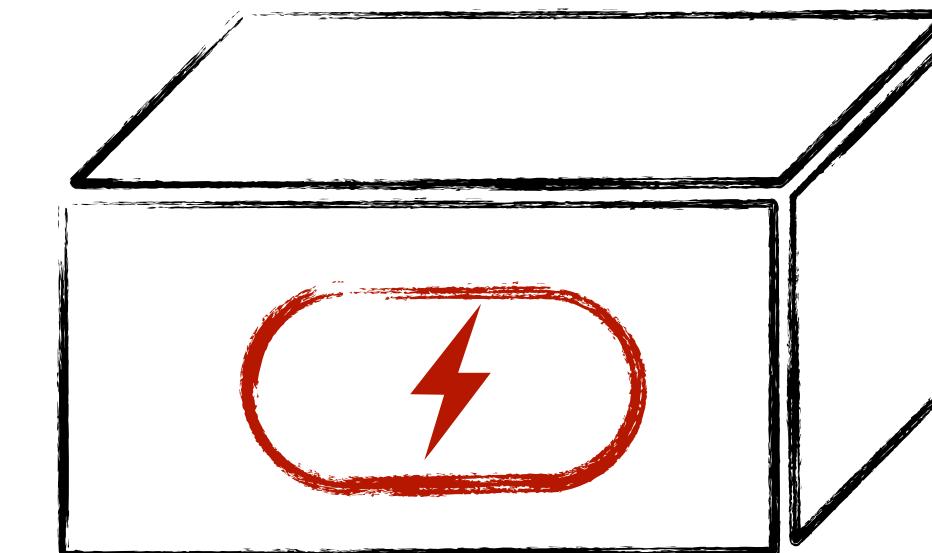
ℓ_1	ℓ_2
2	0



Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2



Volatile and nonvolatile memories with stable and unstable types

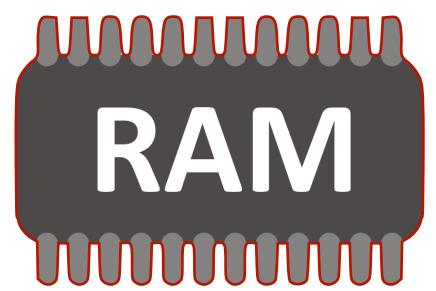


Nonvolatile memory
Stable values

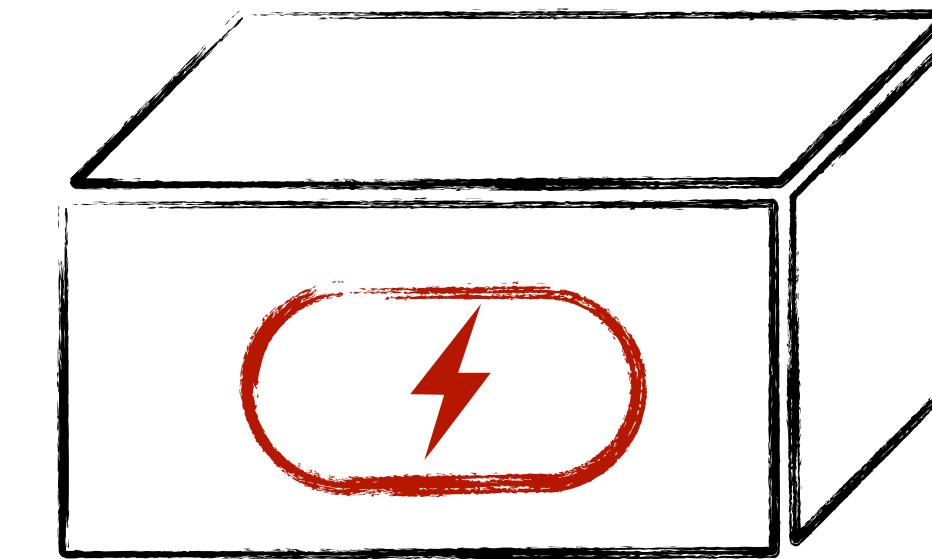
ℓ_1	ℓ_2
2	0

pc →

```
let w:=2 in  
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Volatile memory
Unstable values



Final memory
state we expect:

ℓ_1	ℓ_2
2	2

Volatile and nonvolatile memories with stable and unstable types

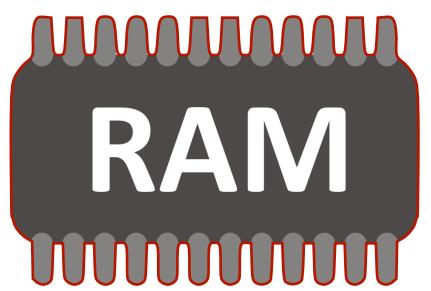


Nonvolatile memory
Stable values

ℓ_1	ℓ_2
2	0

pc →

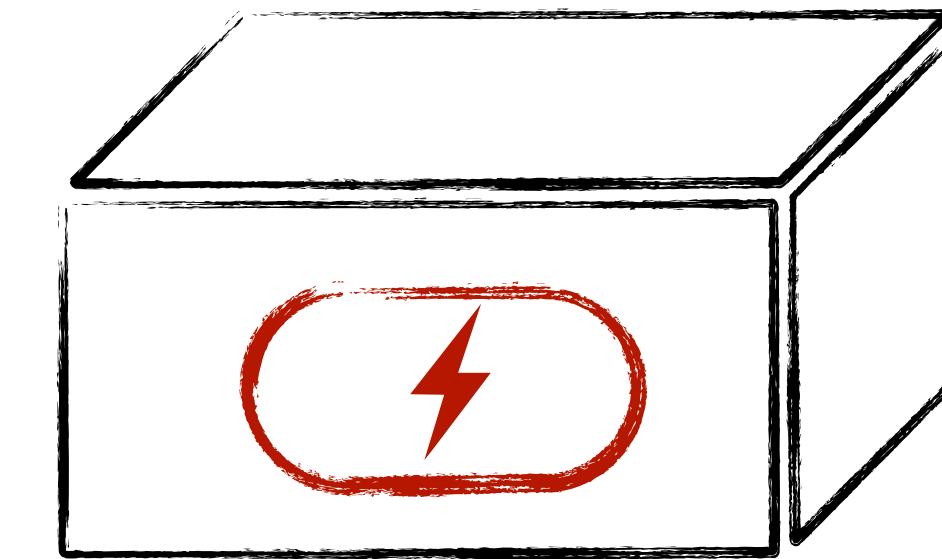
```
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```



Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2



Final memory
state we get:

ℓ_1	ℓ_2
4	4

Volatile and nonvolatile memories with stable and unstable types

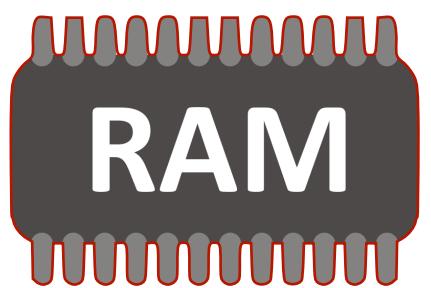


Nonvolatile memory
Stable values

ℓ_1	ℓ_2
2	0

pc →

```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```



Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2



Incorrect

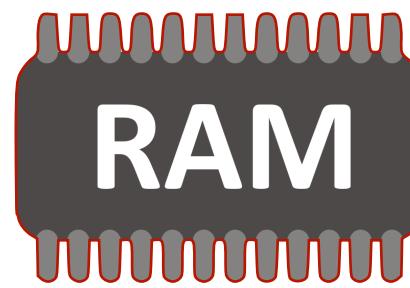
Final memory
state we get:

ℓ_1	ℓ_2
4	4

Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values



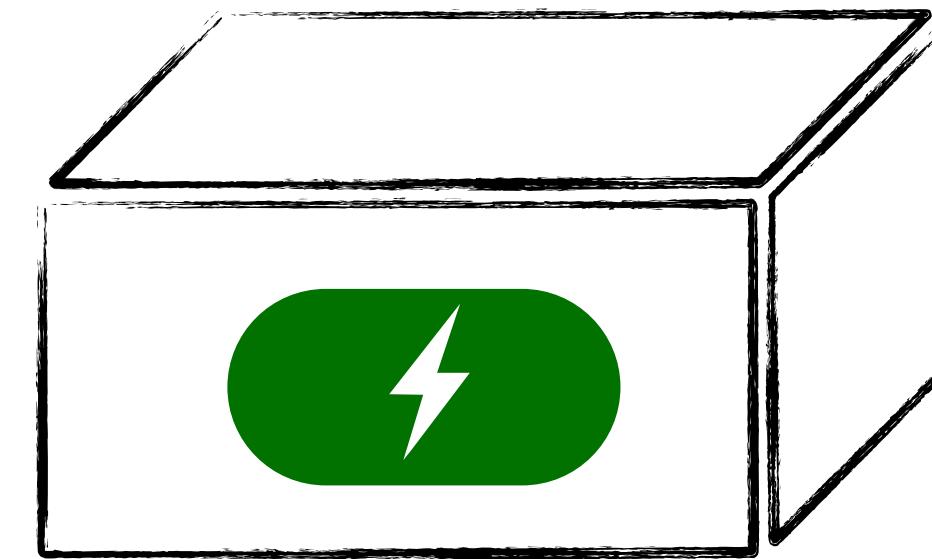
Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
0	0

pc → Checkpoint

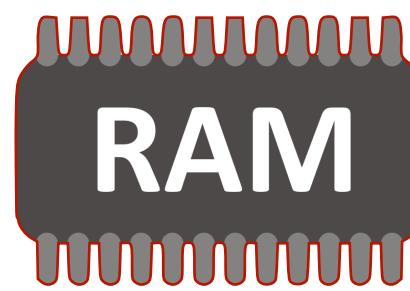
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```



Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2

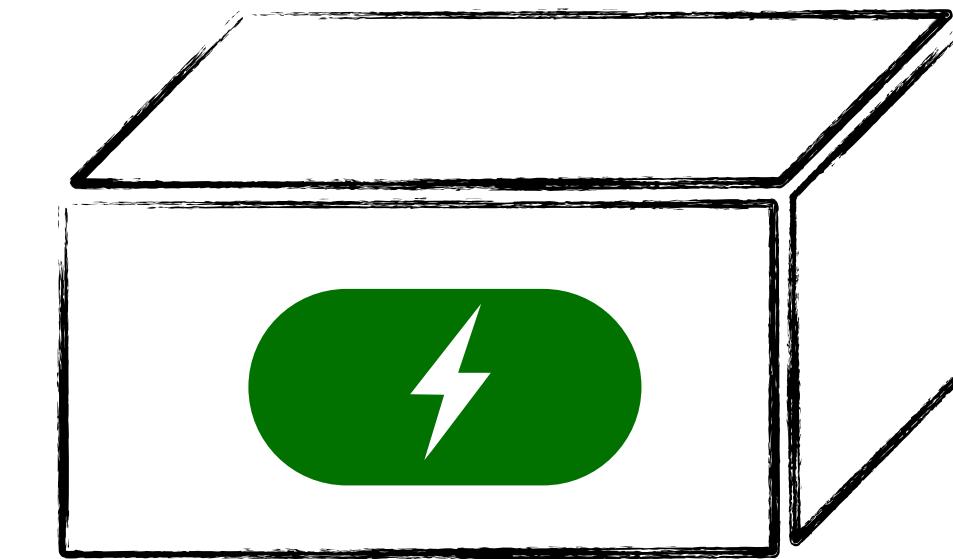
Checkpoint

ℓ_1	ℓ_2
0	0

pc →

Checkpoint

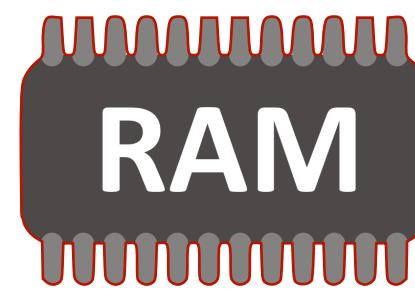
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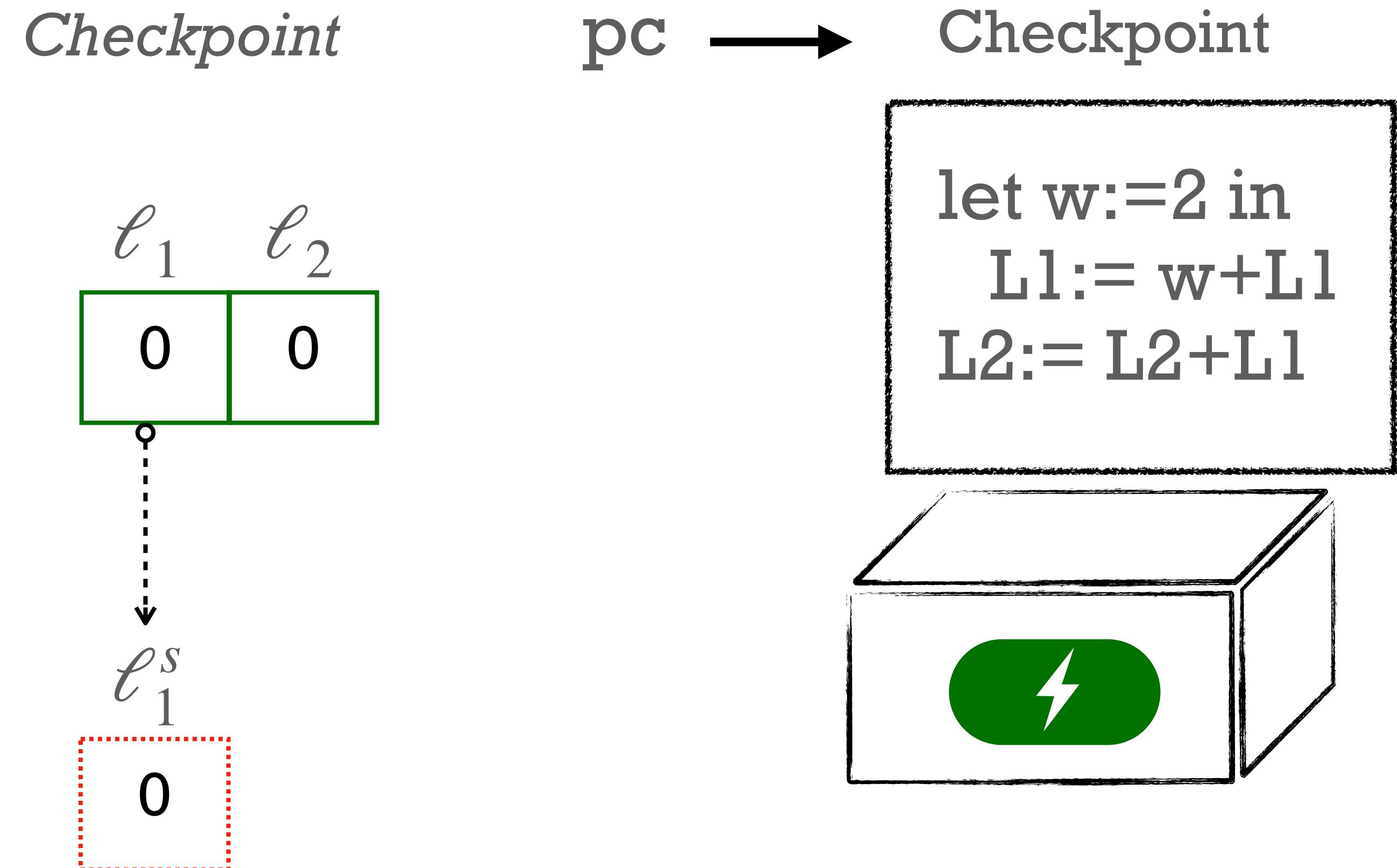
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

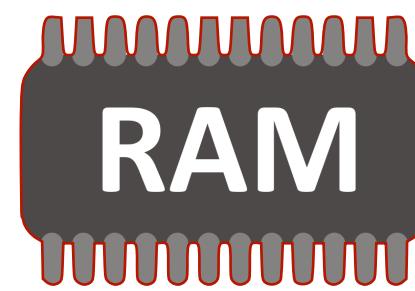
ℓ_1	ℓ_2
2	2



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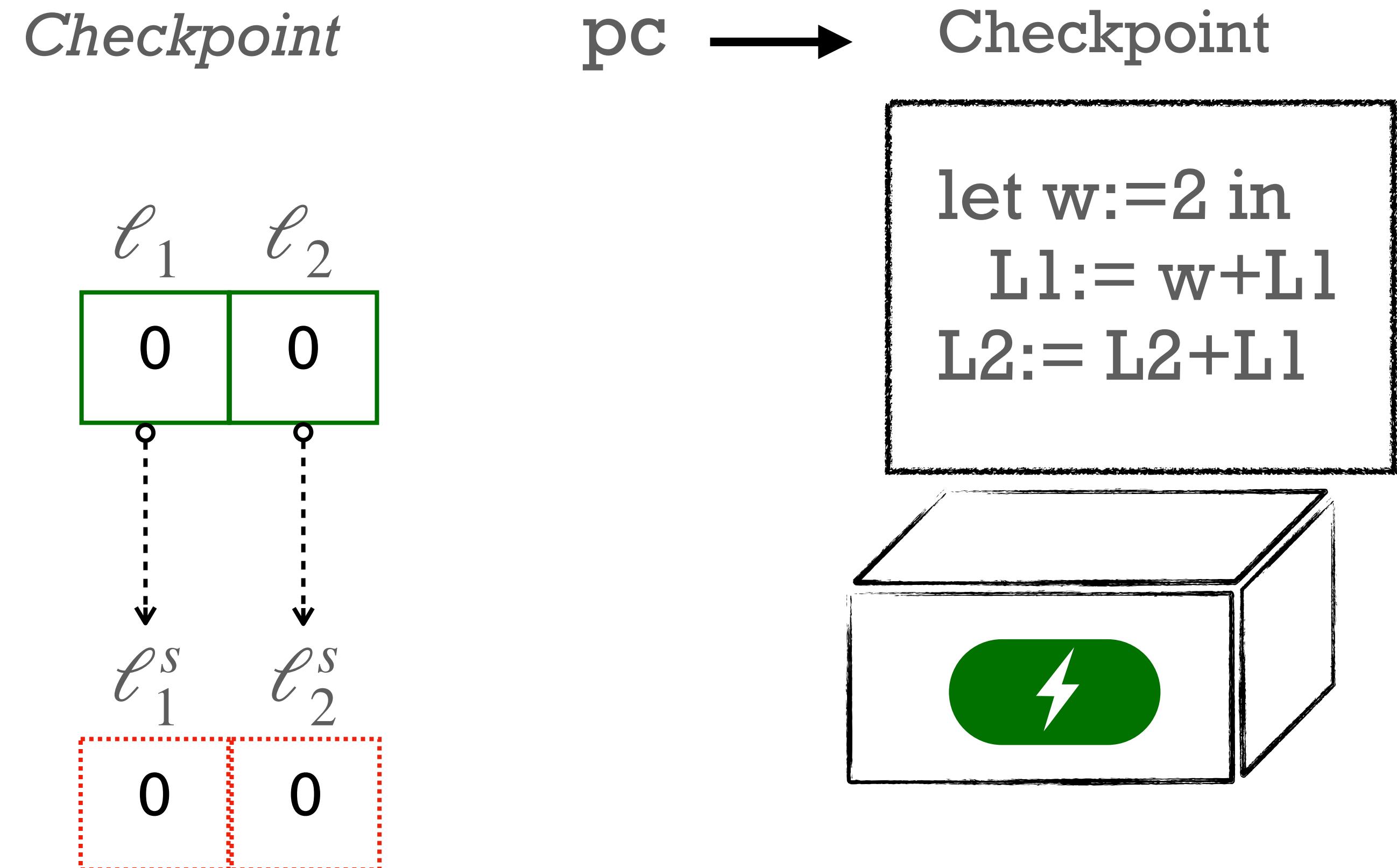
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

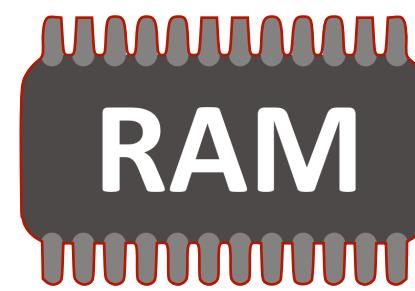
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



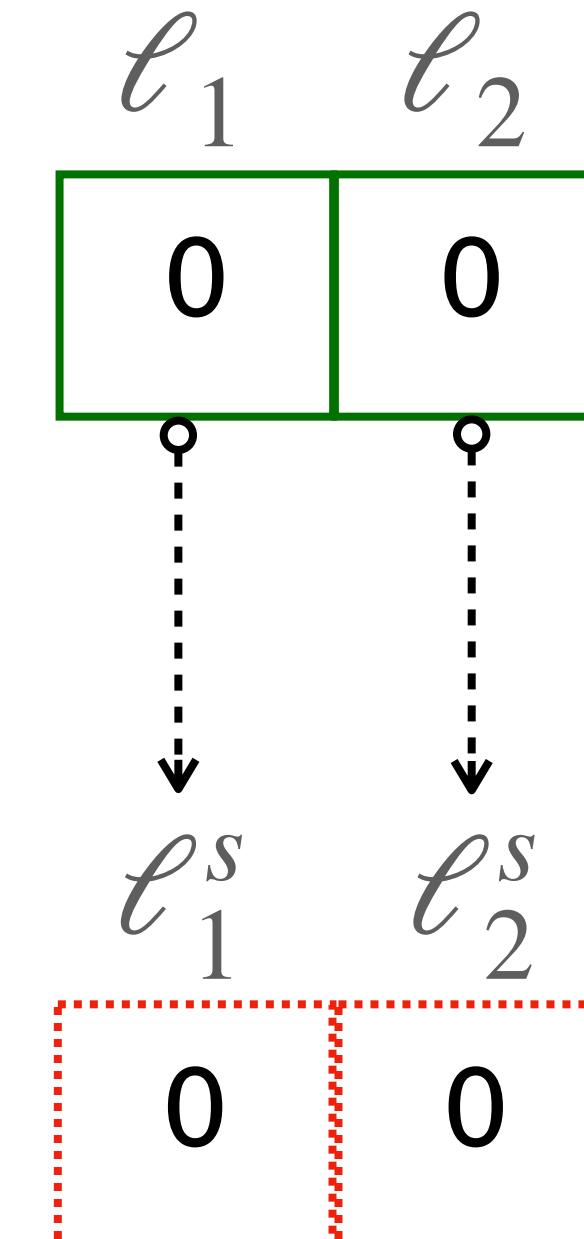
Nonvolatile memory
Stable values



Volatile memory
Unstable values

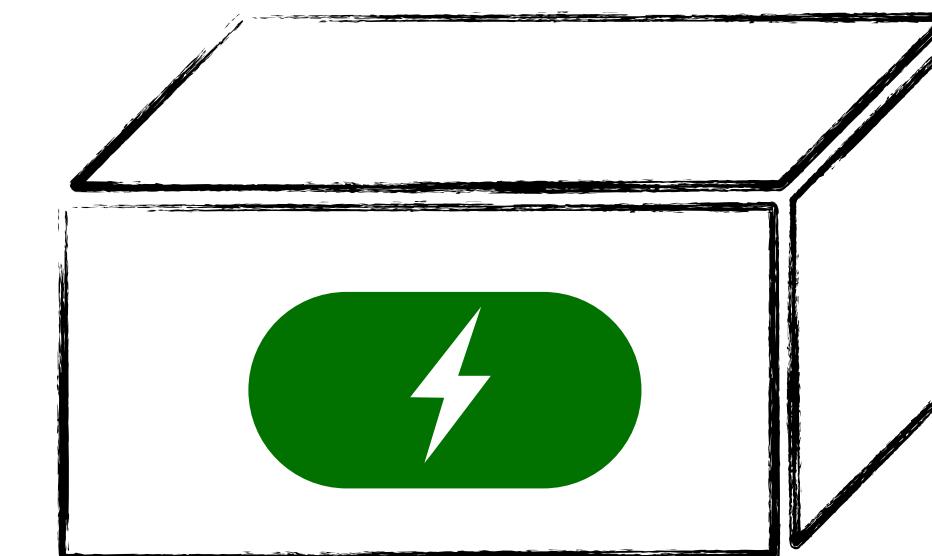
Final memory
state we expect:

ℓ_1	ℓ_2
2	2



pc → Checkpoint

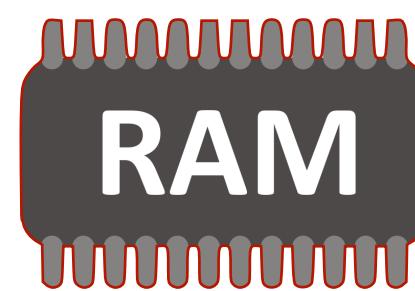
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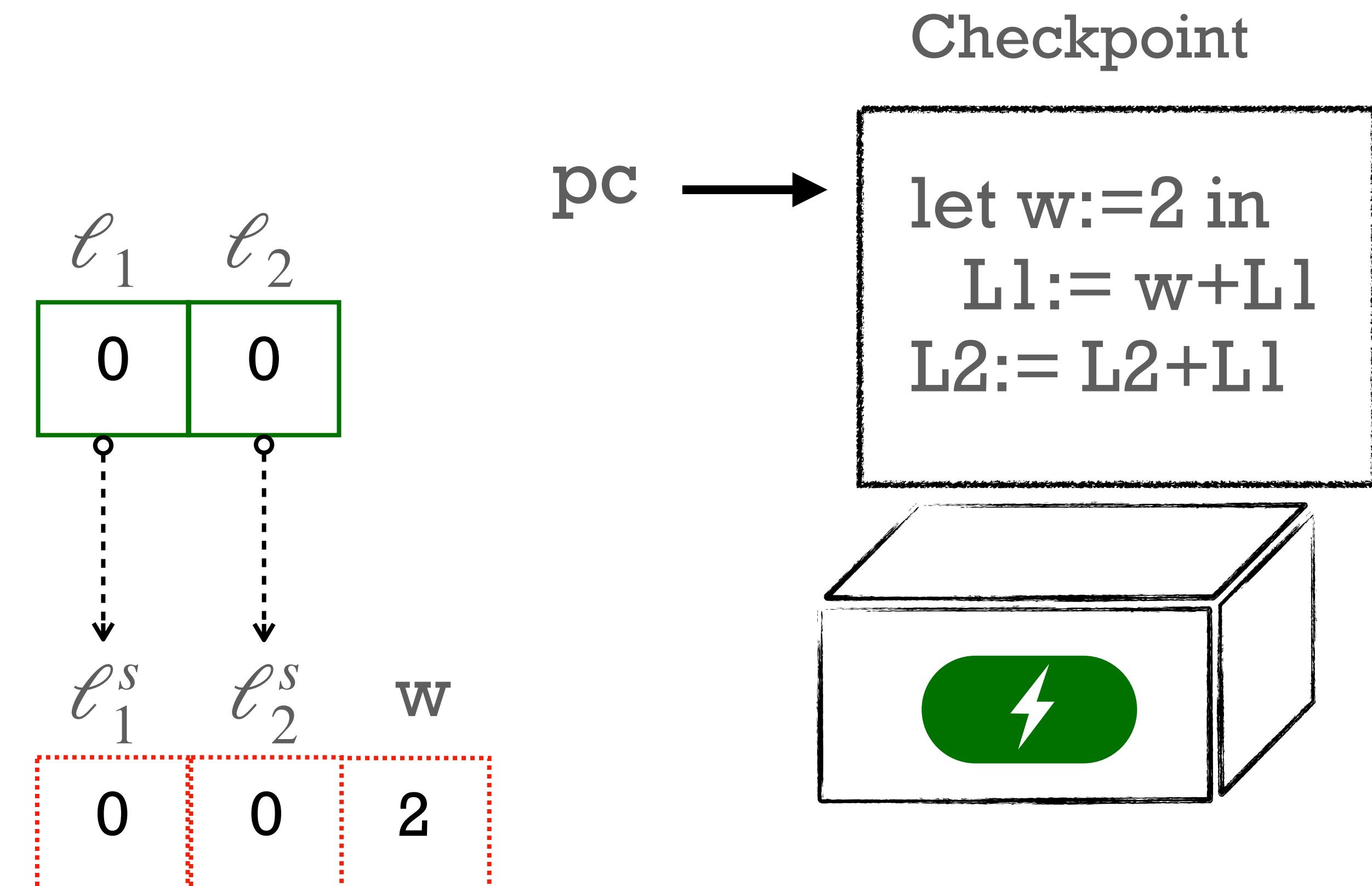
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
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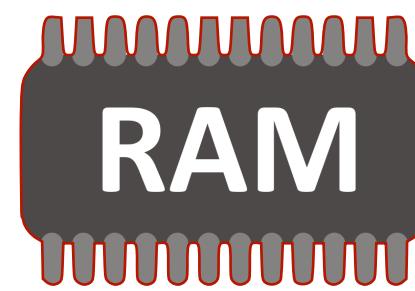
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



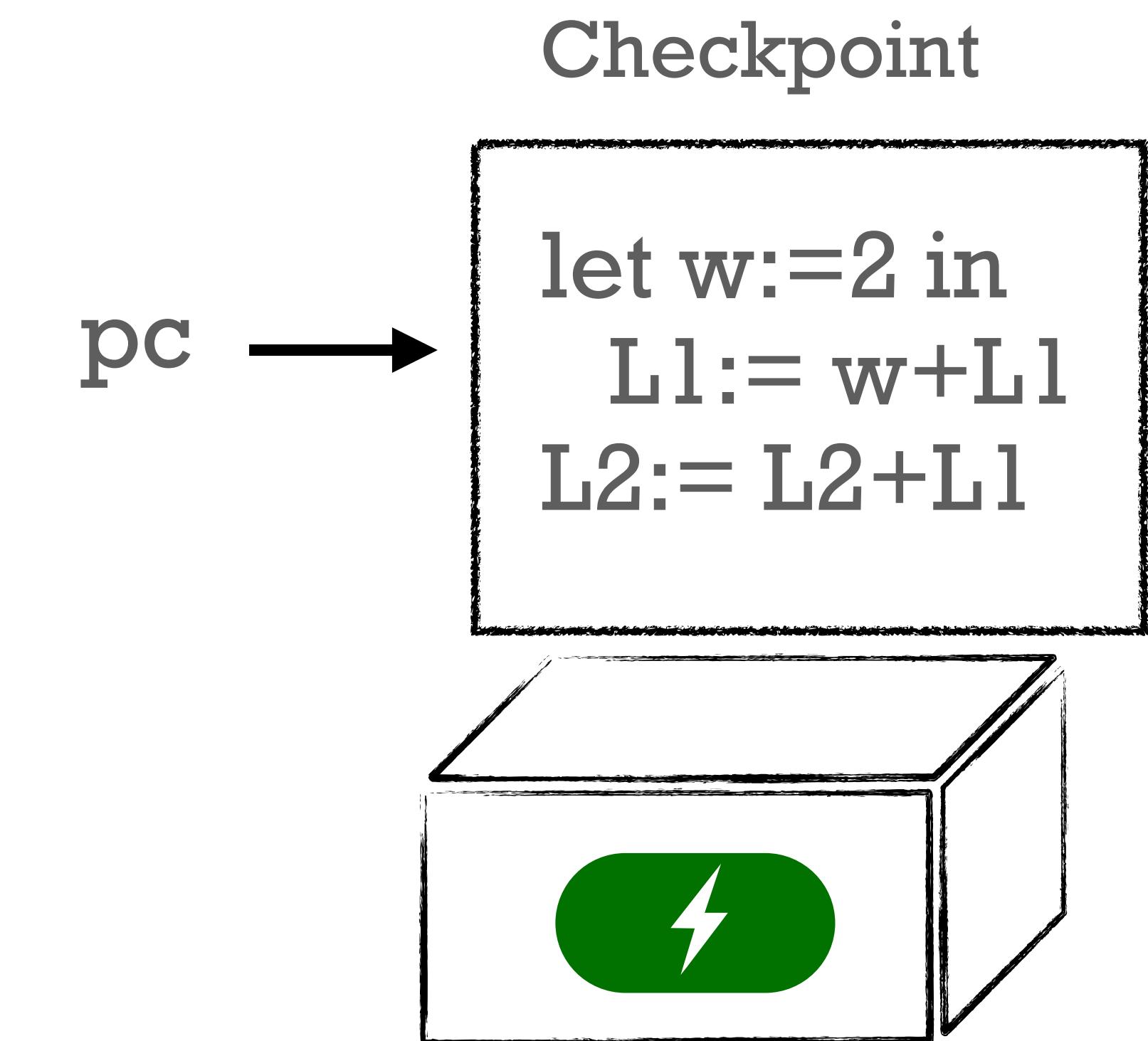
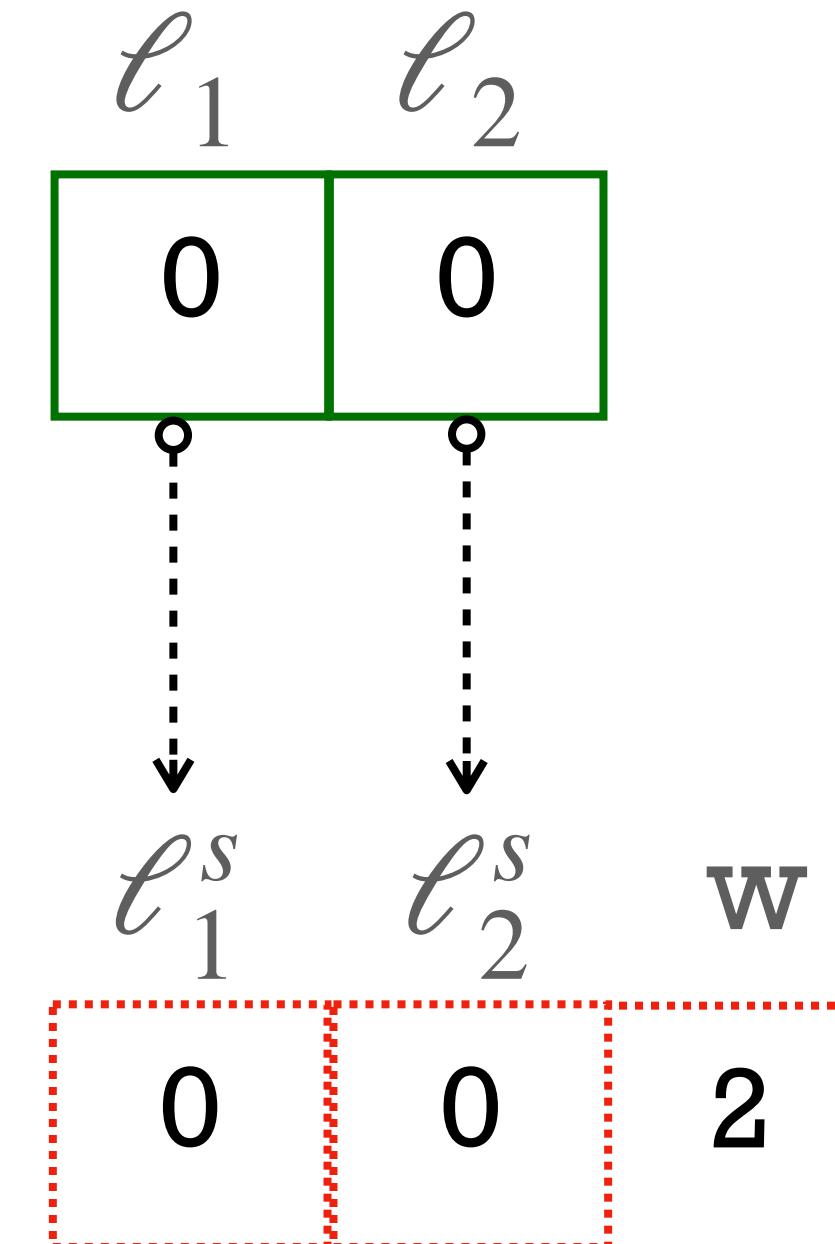
Nonvolatile memory
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Volatile memory
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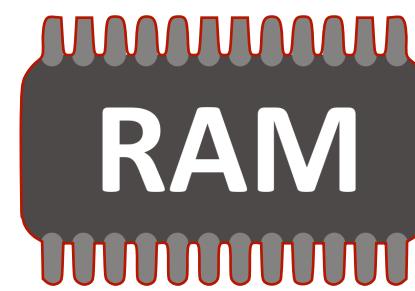
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2	2



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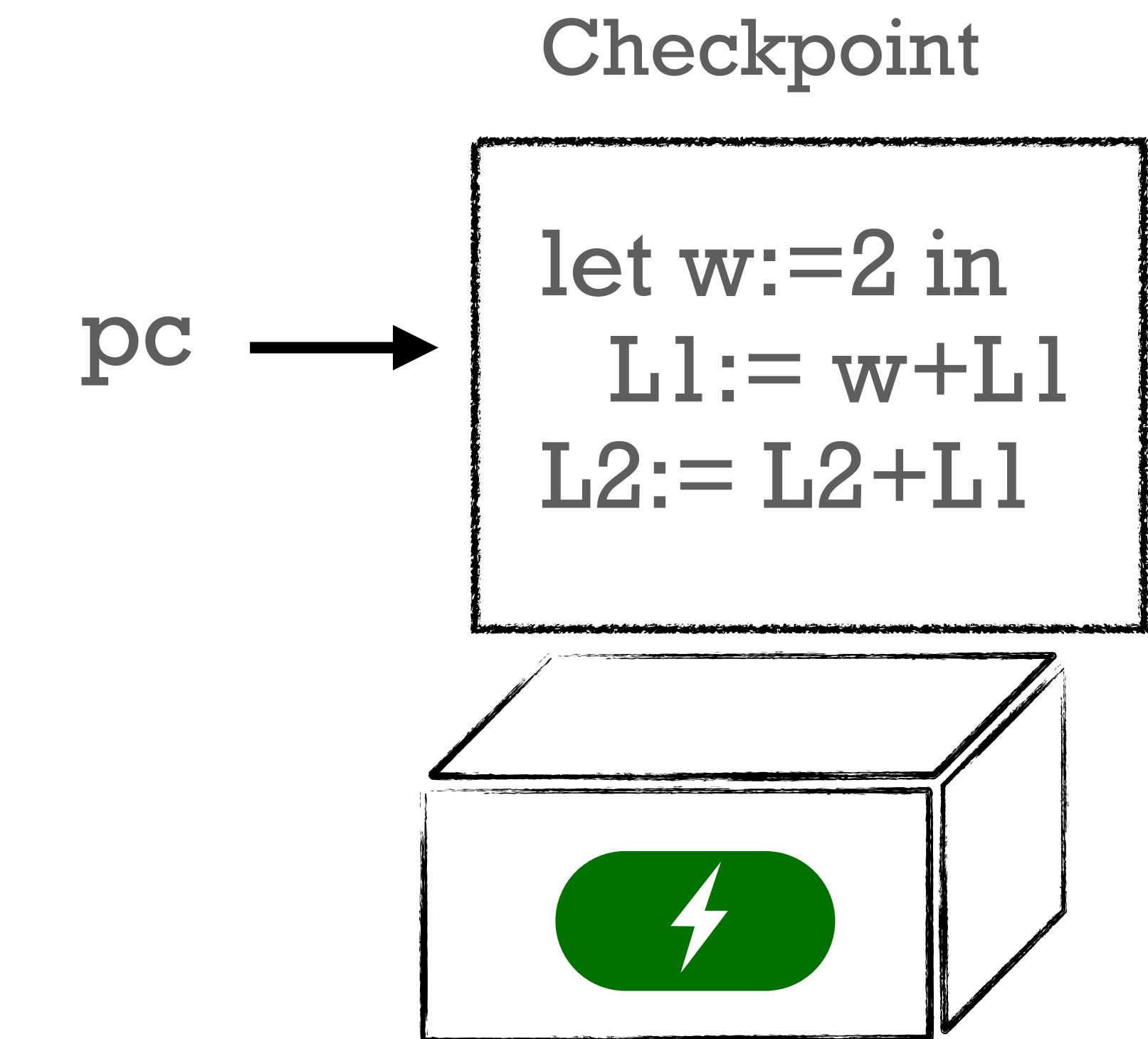
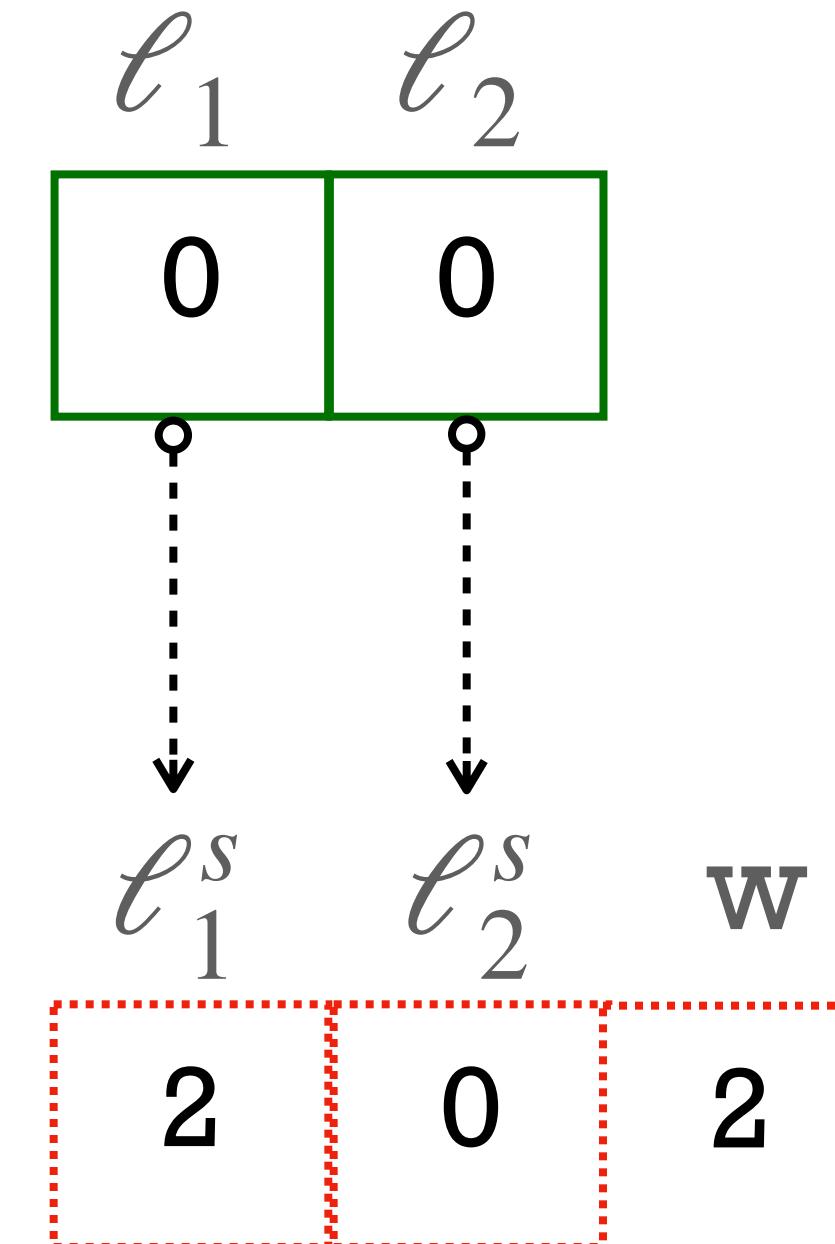
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

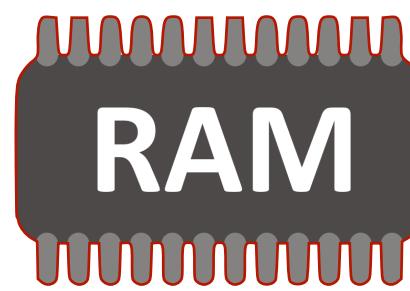
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



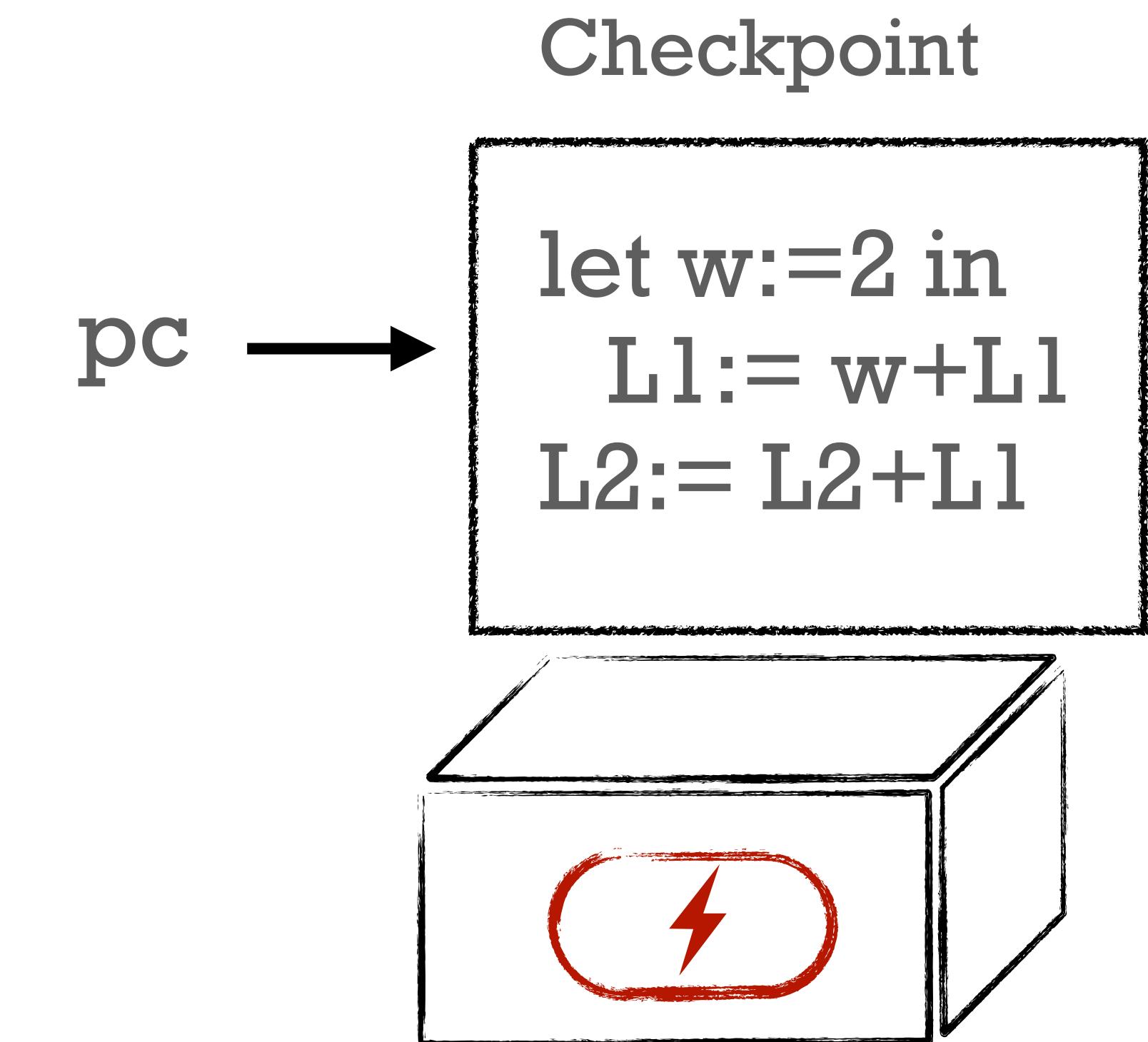
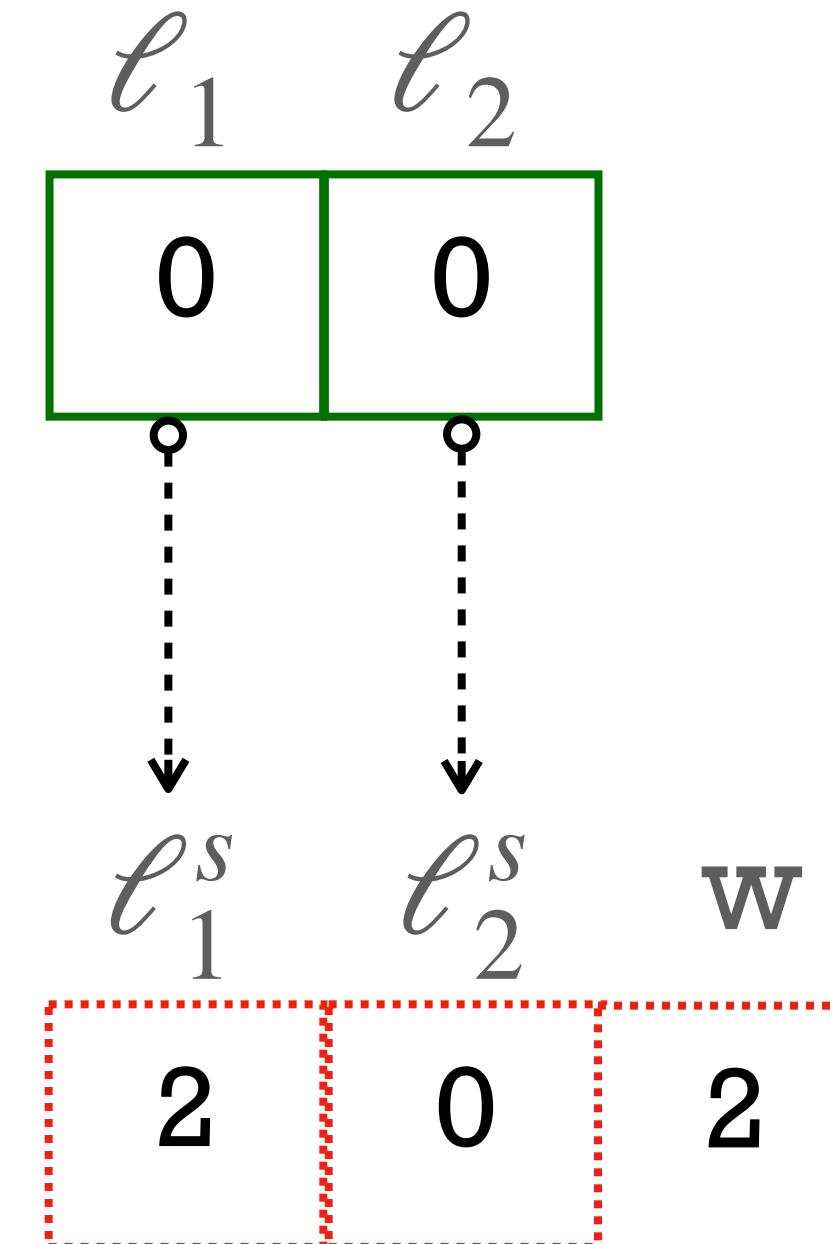
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

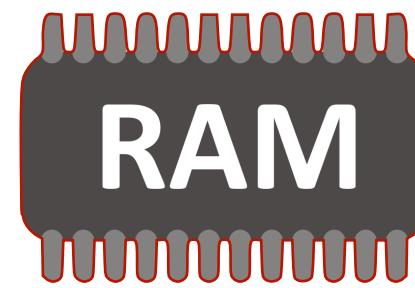
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Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
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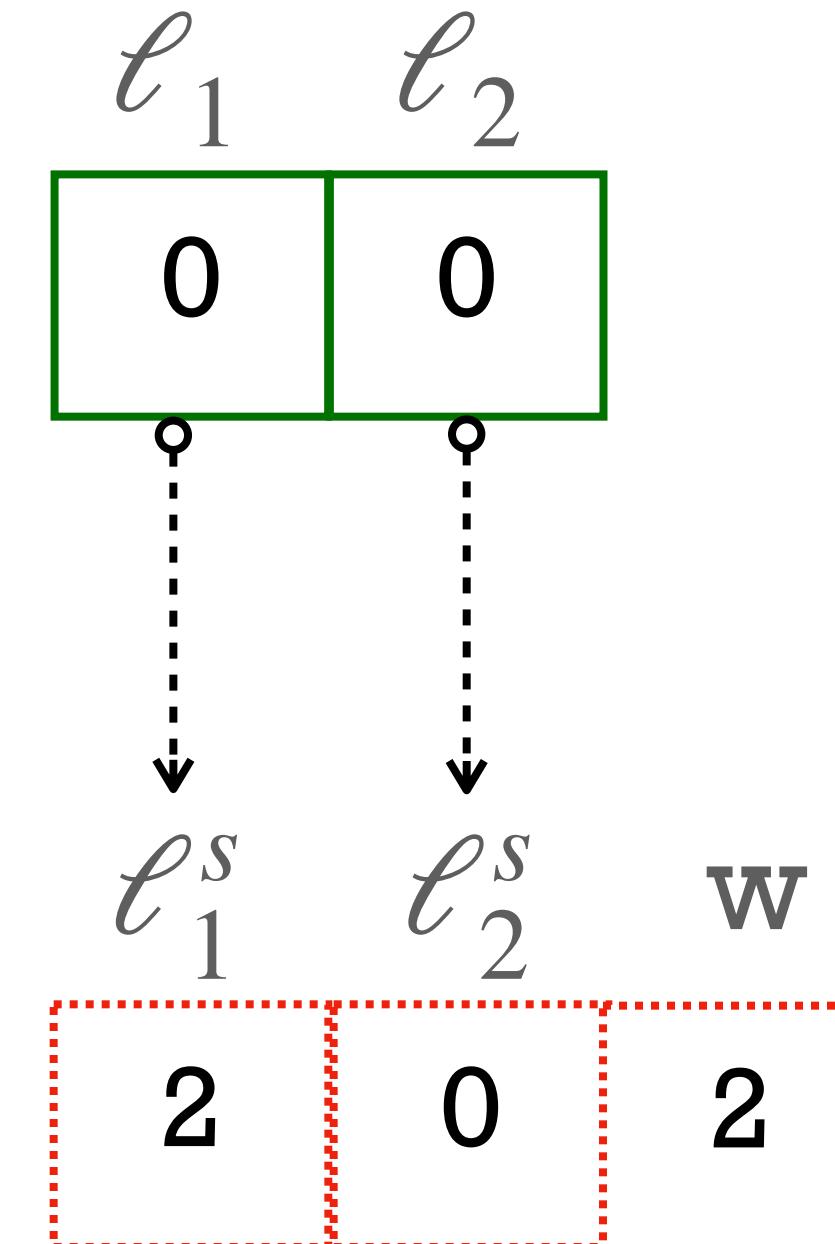


Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2

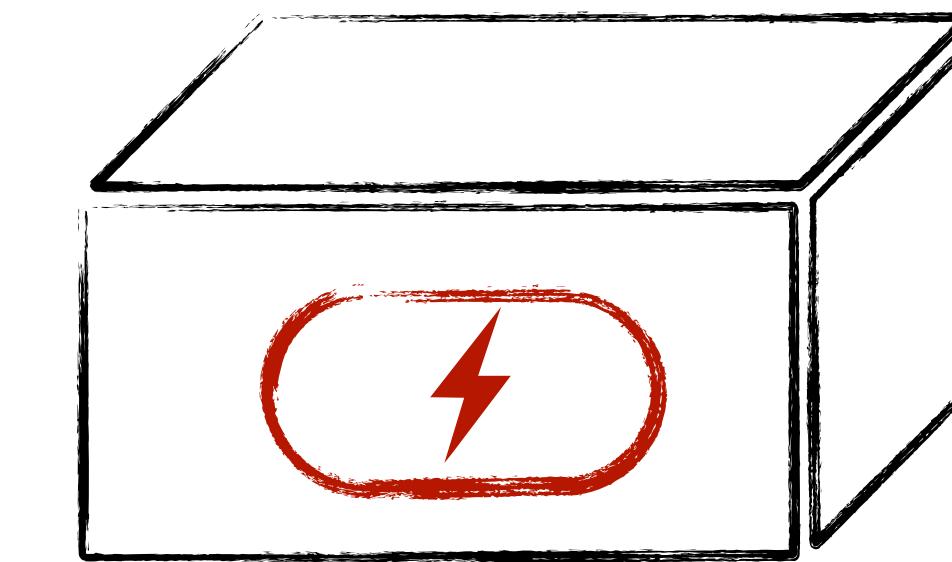
Power failure



pc →

Checkpoint

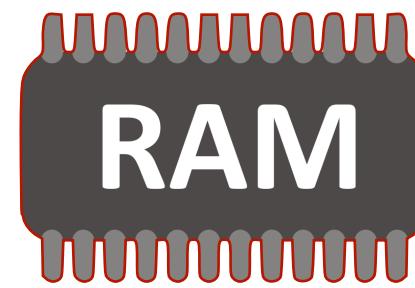
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Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values



Volatile memory
Unstable values

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ℓ_1	ℓ_2
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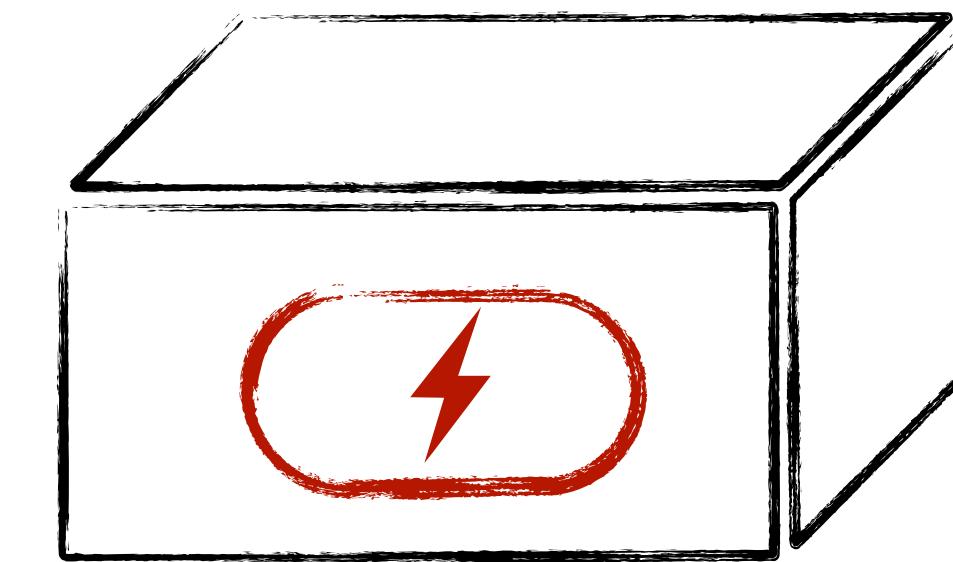
Power failure

ℓ_1	ℓ_2
0	0

pc →

Checkpoint

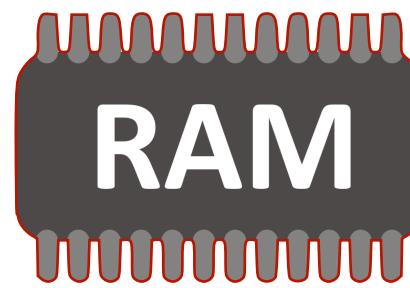
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Stable values



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Unstable values

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Power failure

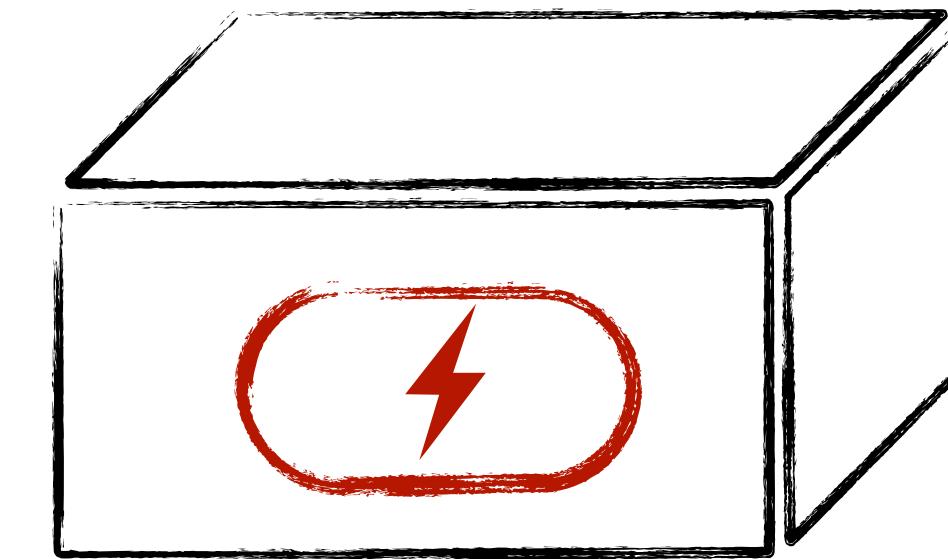
pc



Checkpoint

ℓ_1	ℓ_2
0	0

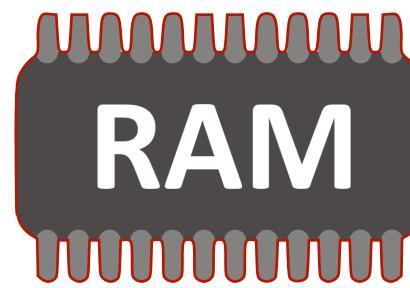
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Solution: Checkpointing blocks (checkpoint-restore-finalize)



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Stable values



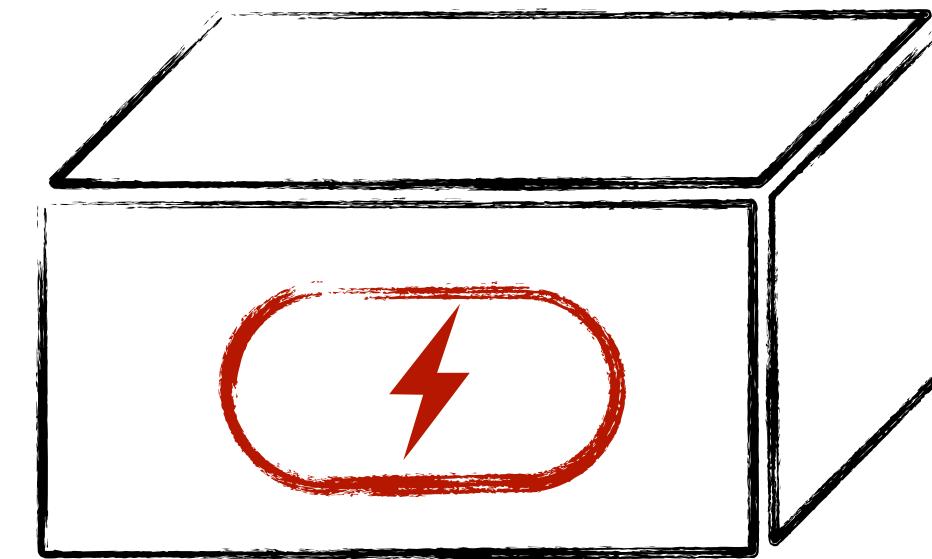
Volatile memory
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pc → Checkpoint

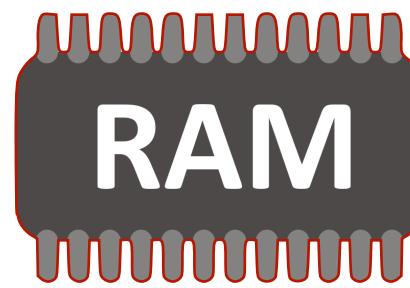
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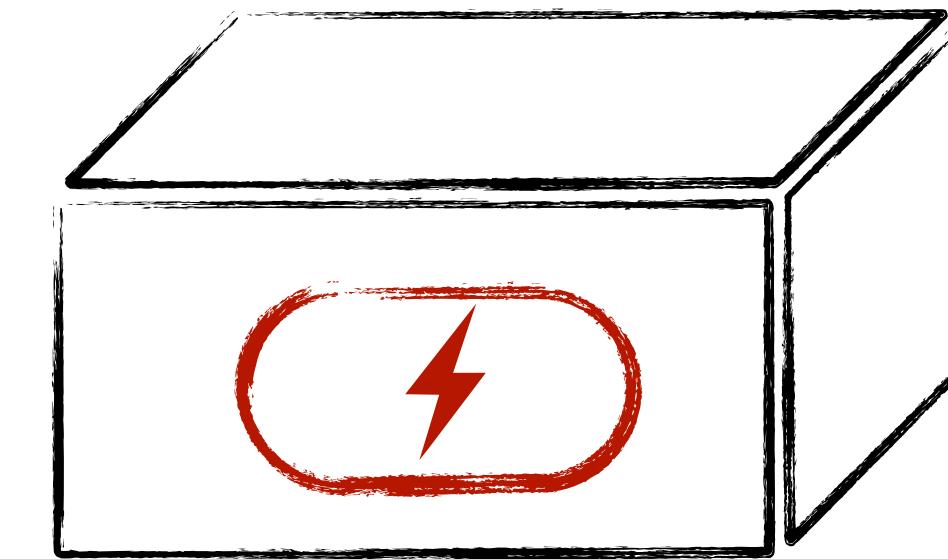
Recharge

pc



Checkpoint

```
let w:=2 in  
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  L2:= L2+L1
```

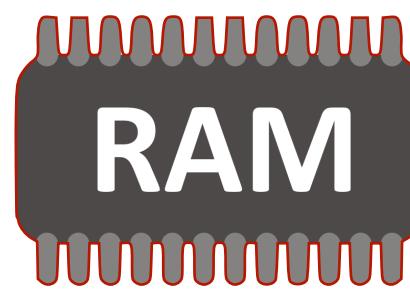


ℓ_1	ℓ_2
0	0

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ℓ_1	ℓ_2
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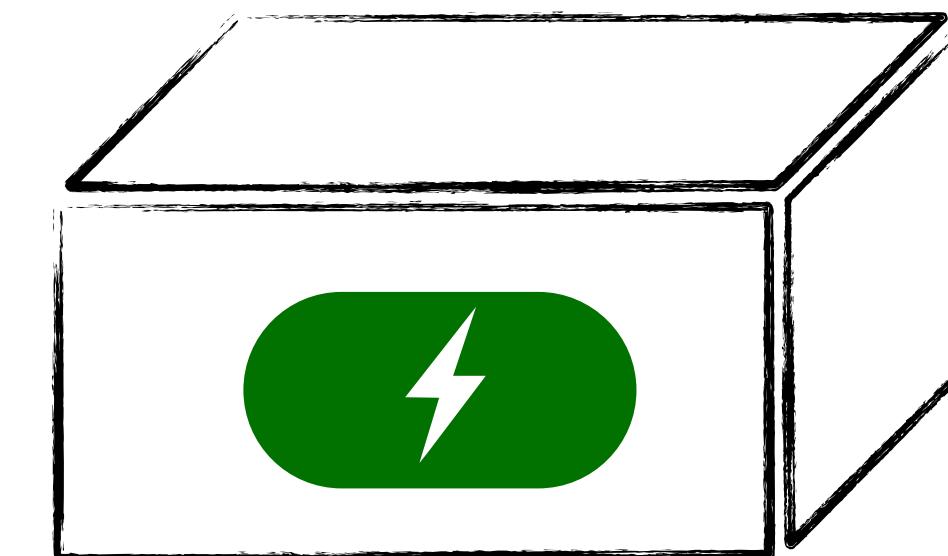
Recharge

pc



Checkpoint

```
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  L2:= L2+L1
```

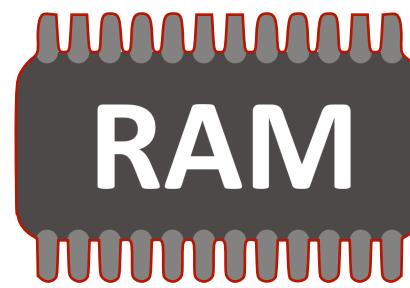


ℓ_1	ℓ_2
0	0

Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values



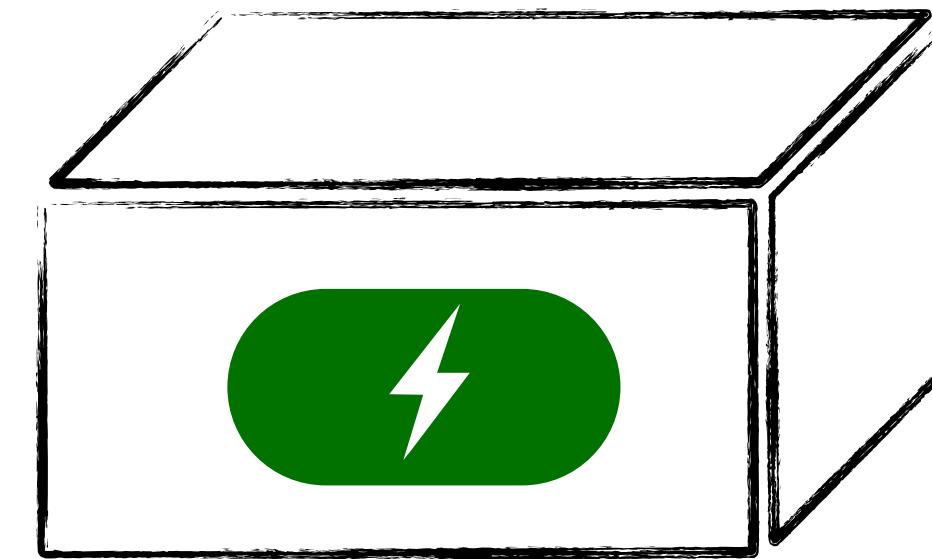
Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
0	0

pc → Checkpoint

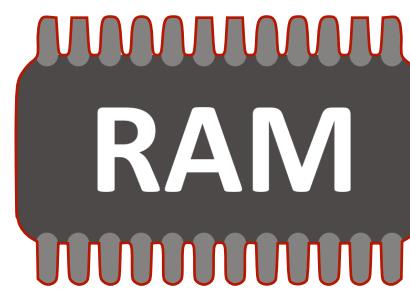
```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```



Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2

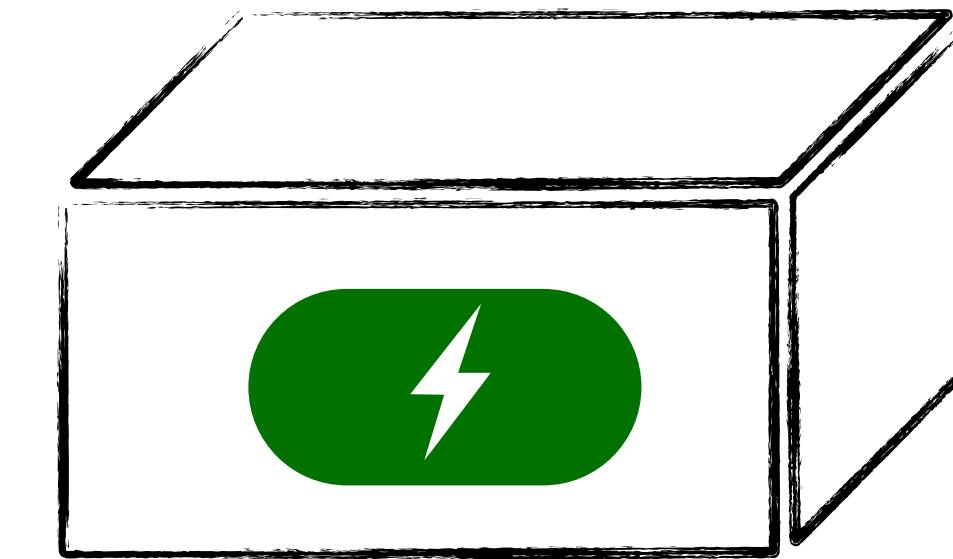
Restore

pc

Checkpoint

ℓ_1	ℓ_2
0	0

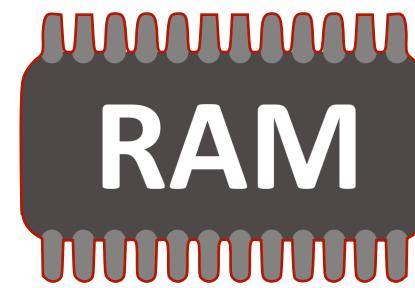
```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```



Solution: Checkpointing blocks (checkpoint-restore-finalize)



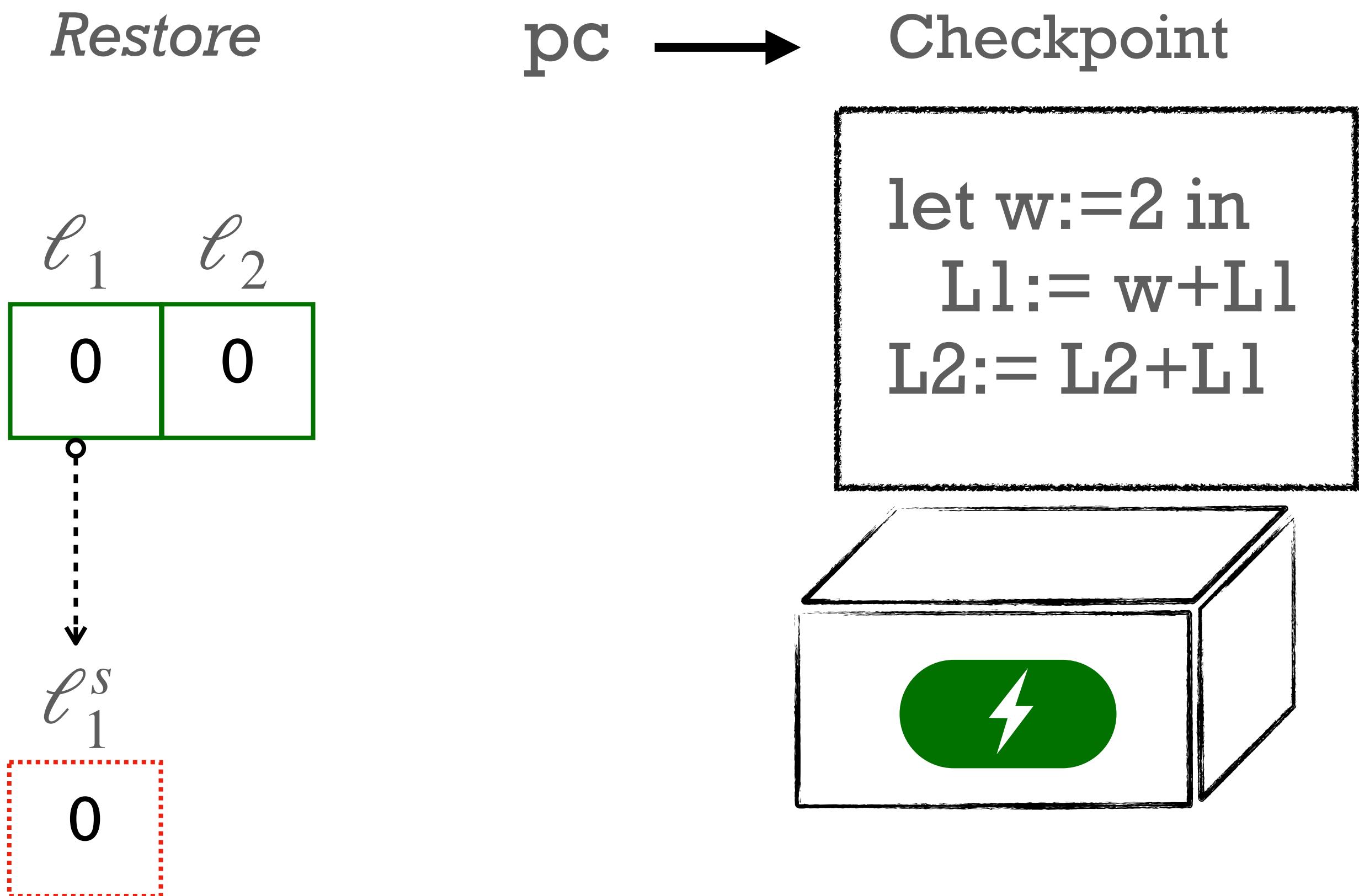
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

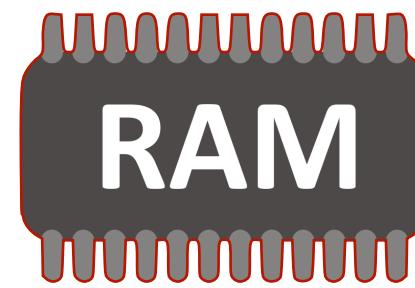
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



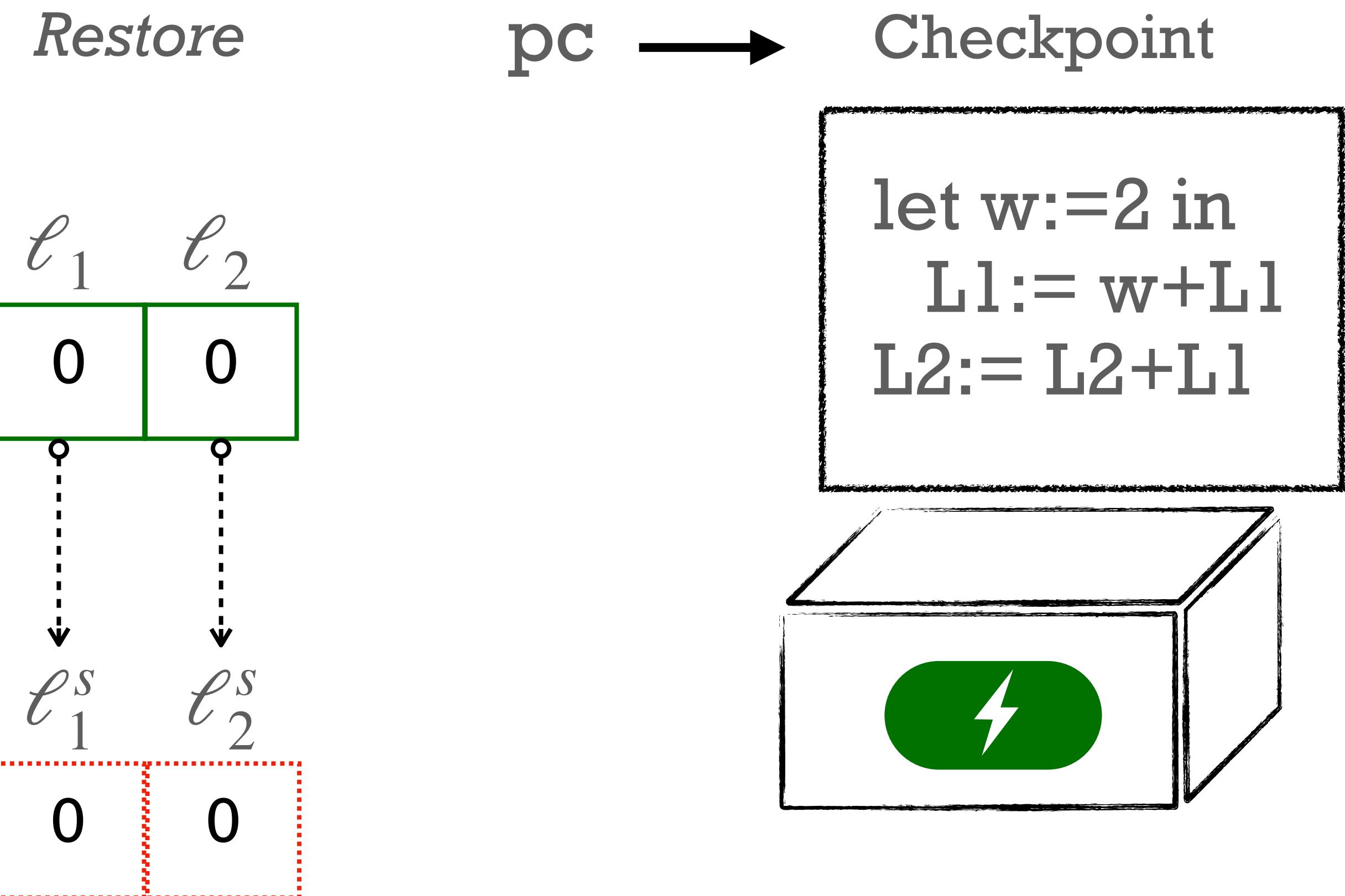
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

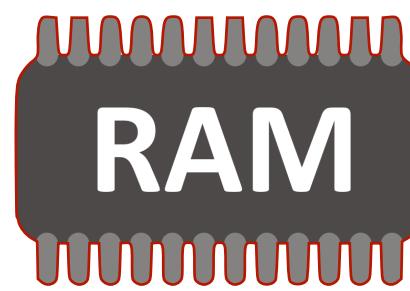
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



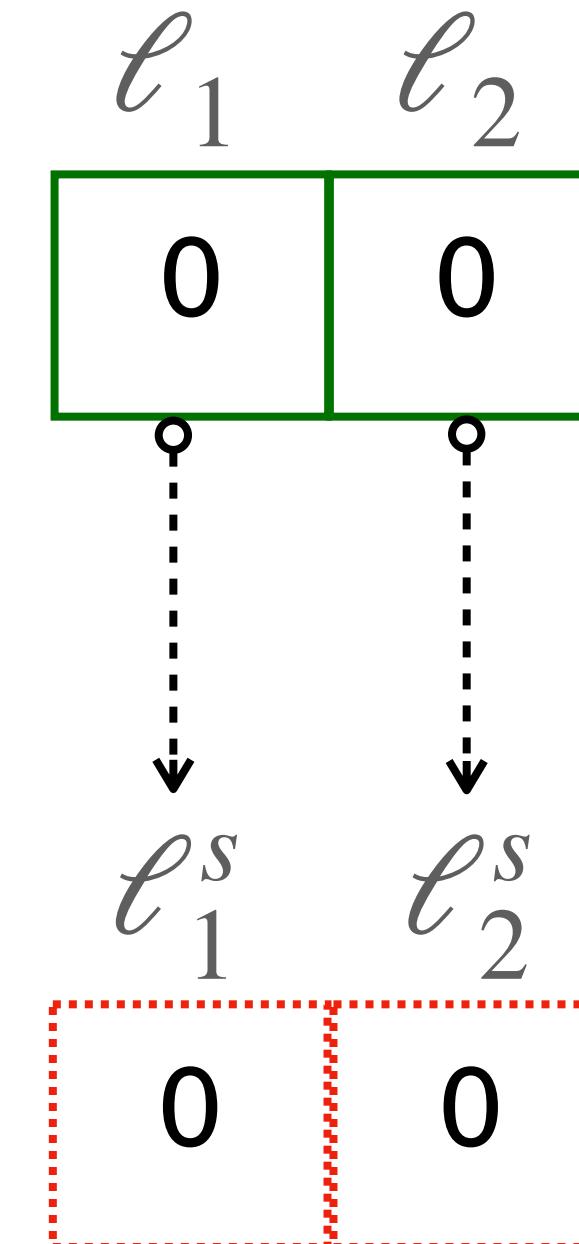
Nonvolatile memory
Stable values



Volatile memory
Unstable values

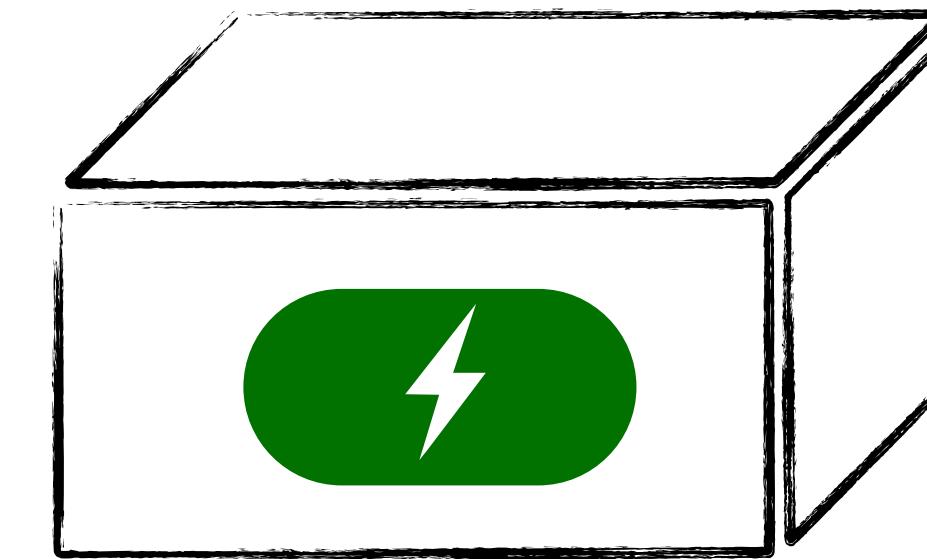
Final memory
state we expect:

ℓ_1	ℓ_2
2	2



pc → Checkpoint

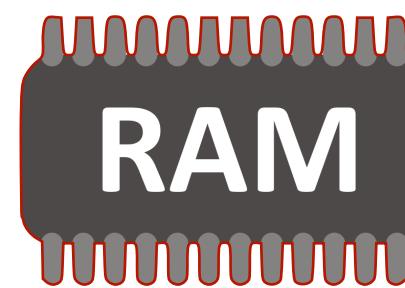
```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```



Solution: Checkpointing blocks (checkpoint-restore-finalize)



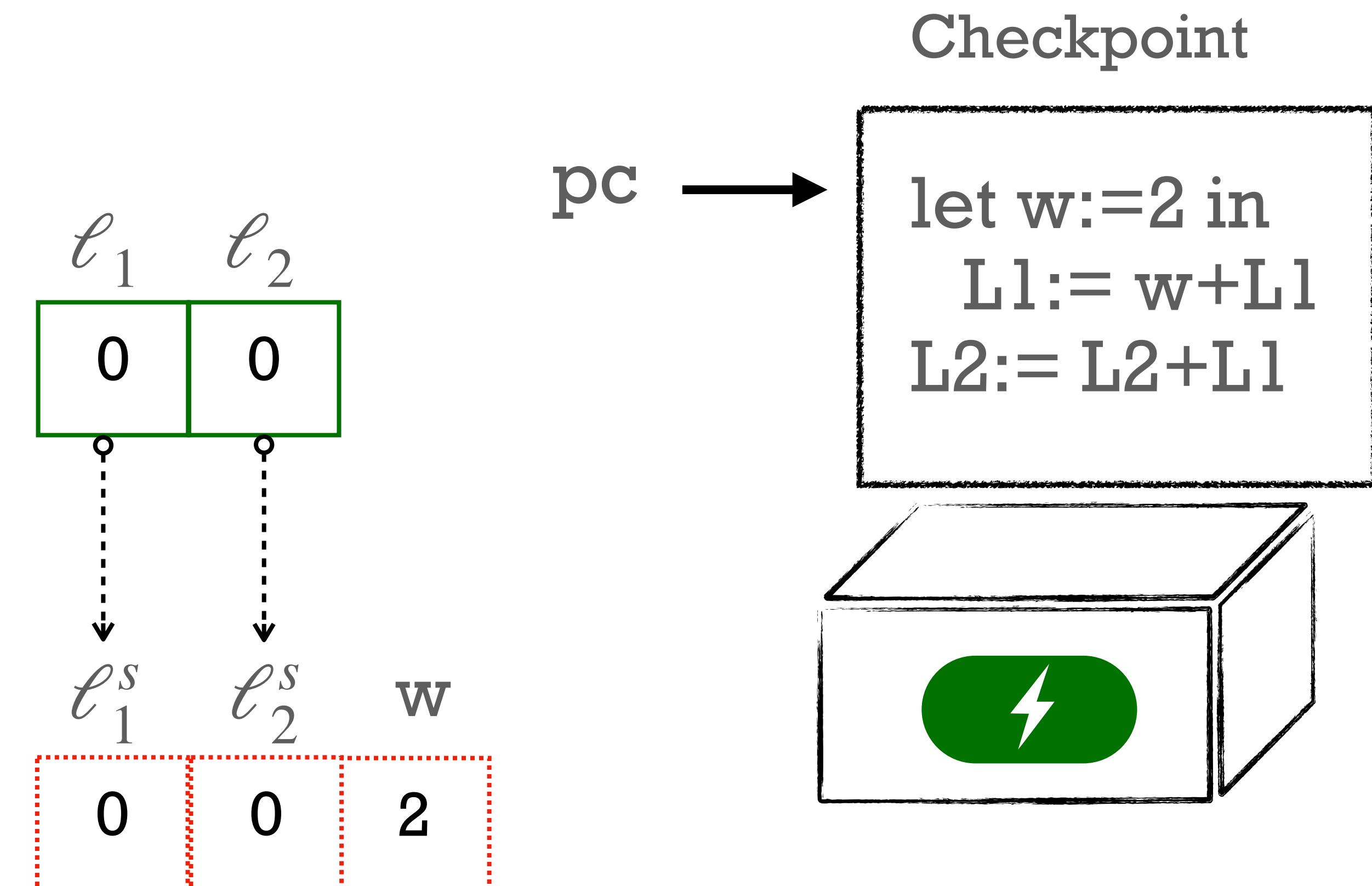
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

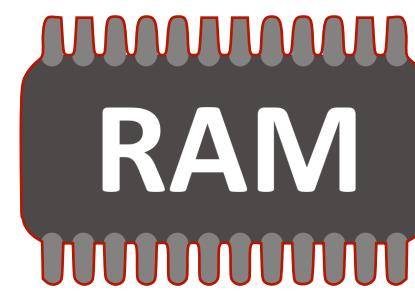
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



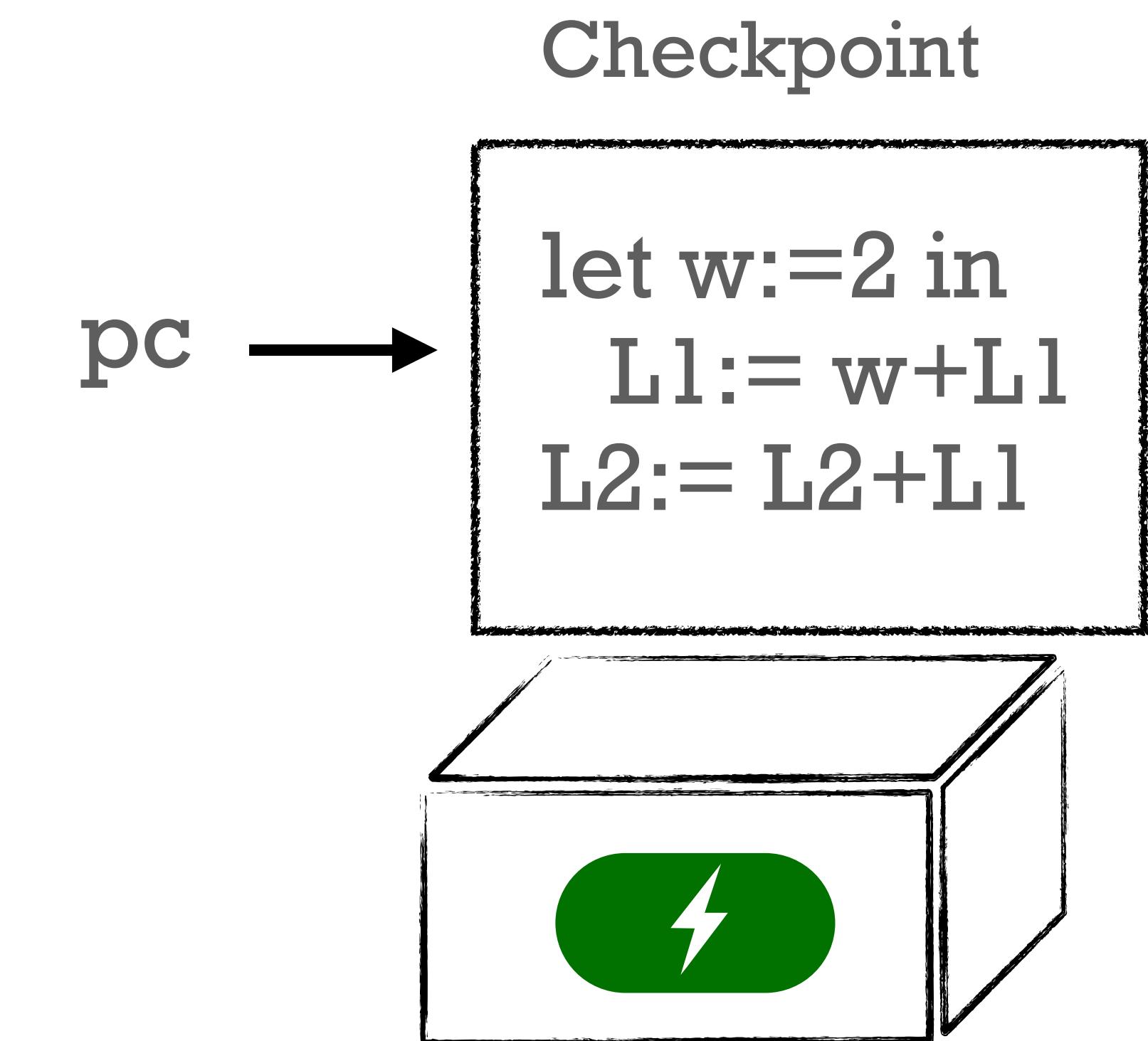
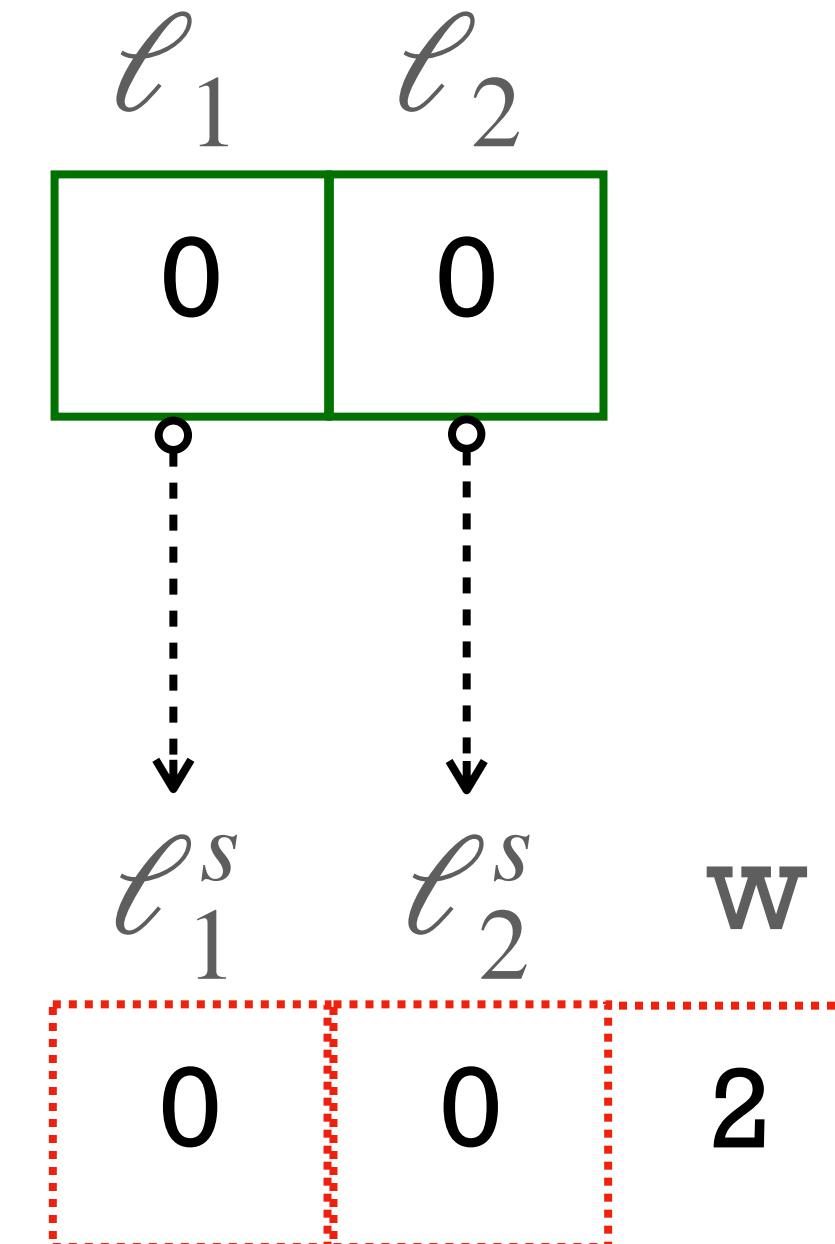
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

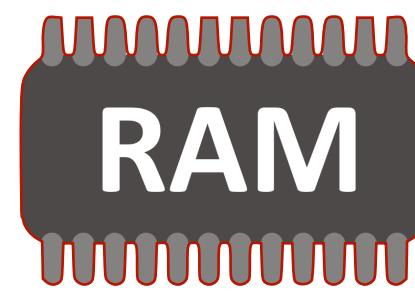
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



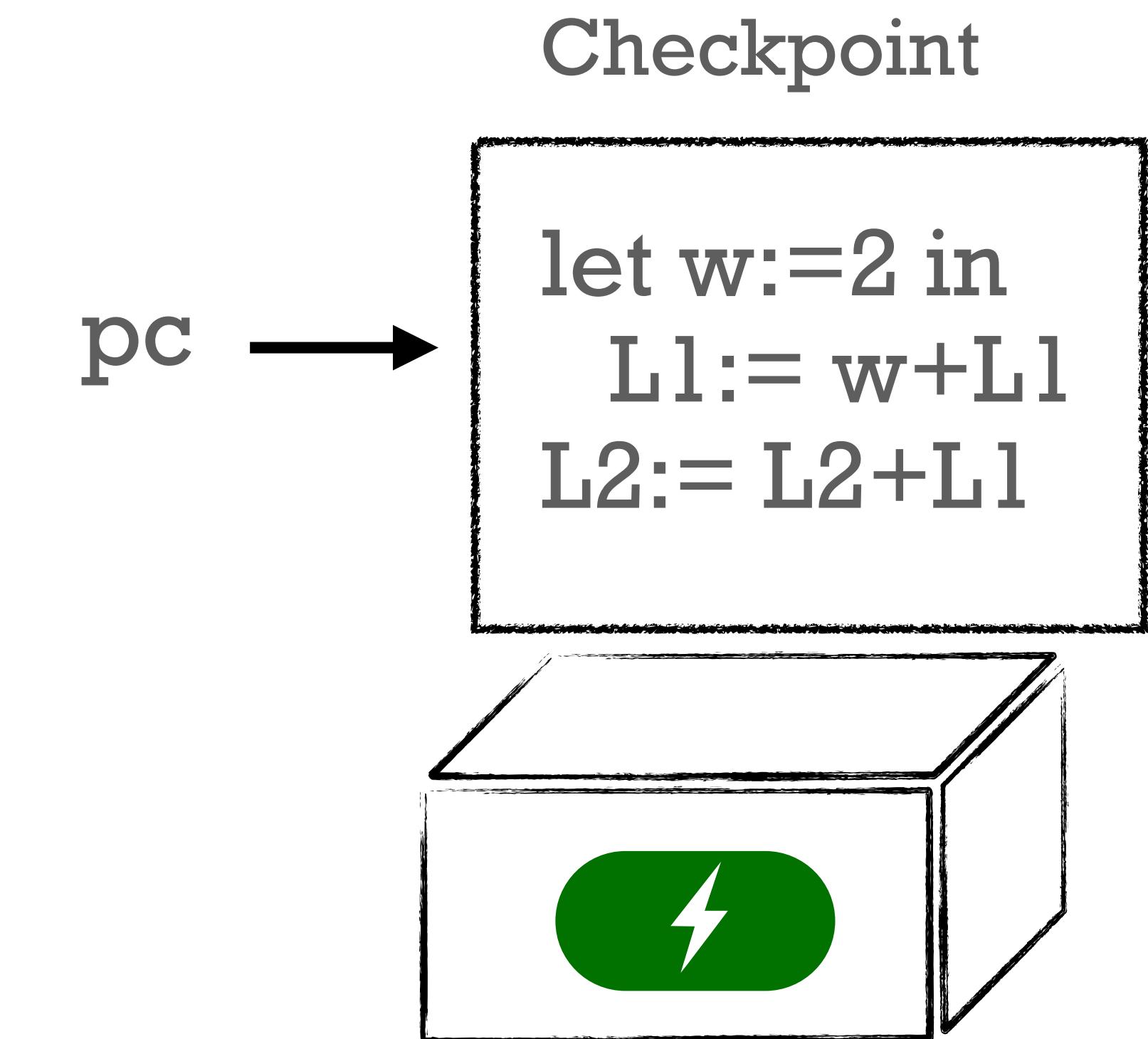
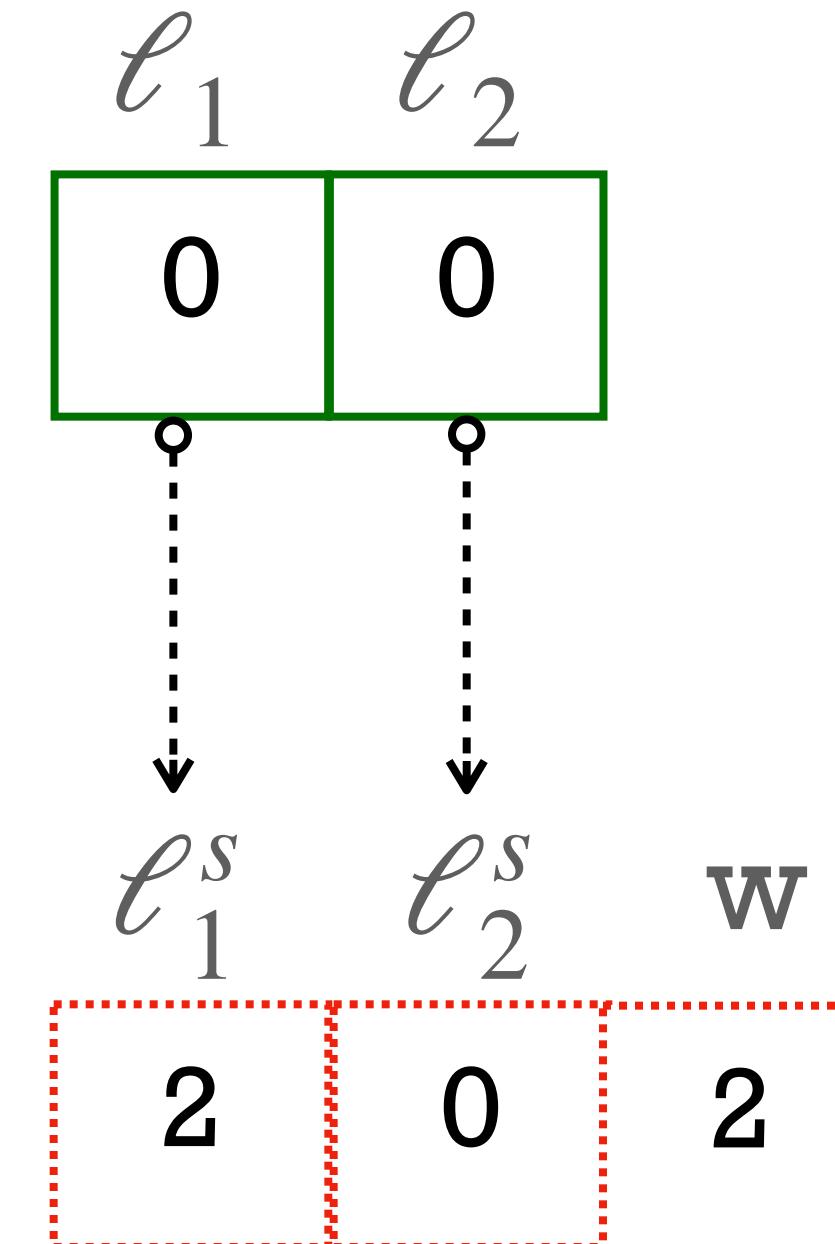
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

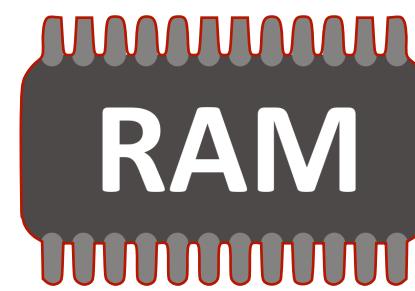
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



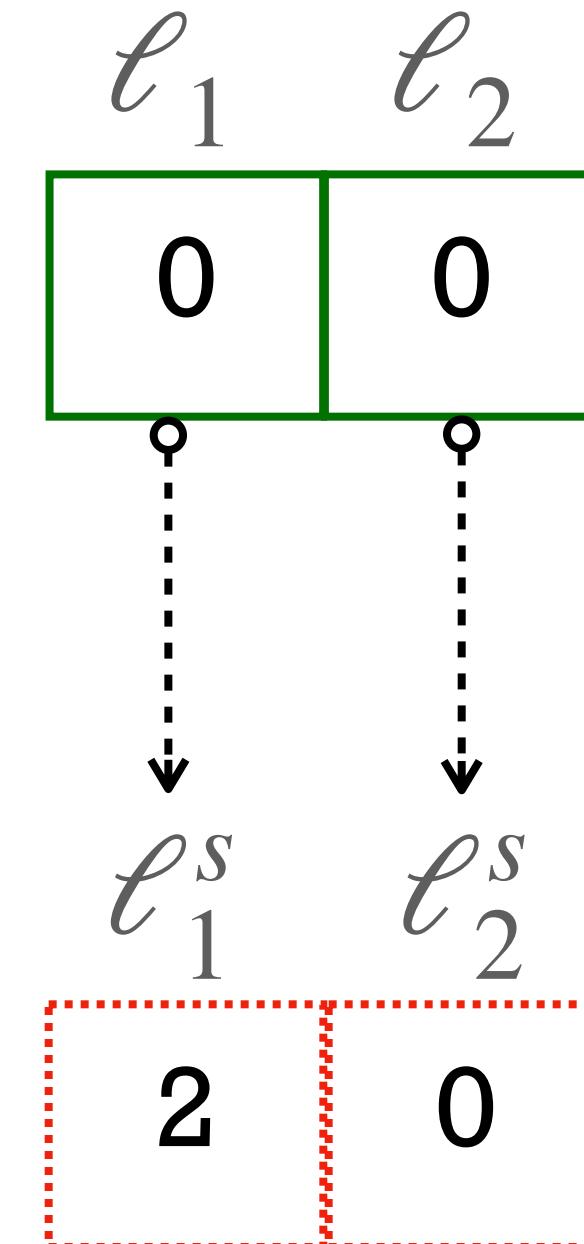
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

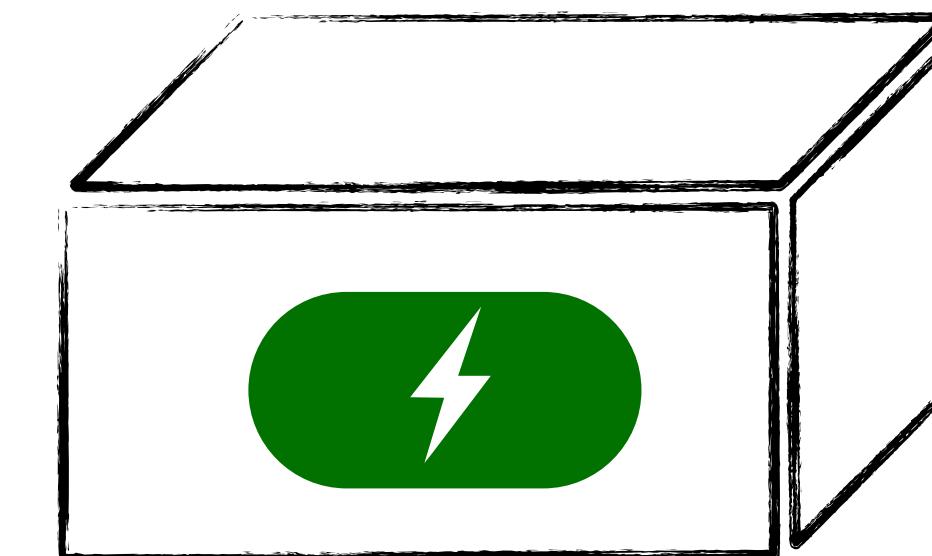
ℓ_1	ℓ_2
2	2



Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

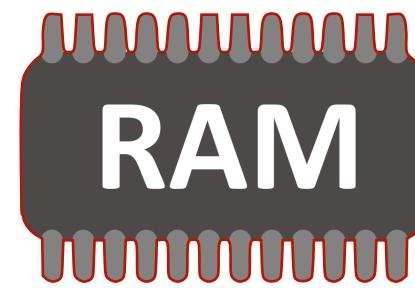
pc →



Solution: Checkpointing blocks (checkpoint-restore-finalize)



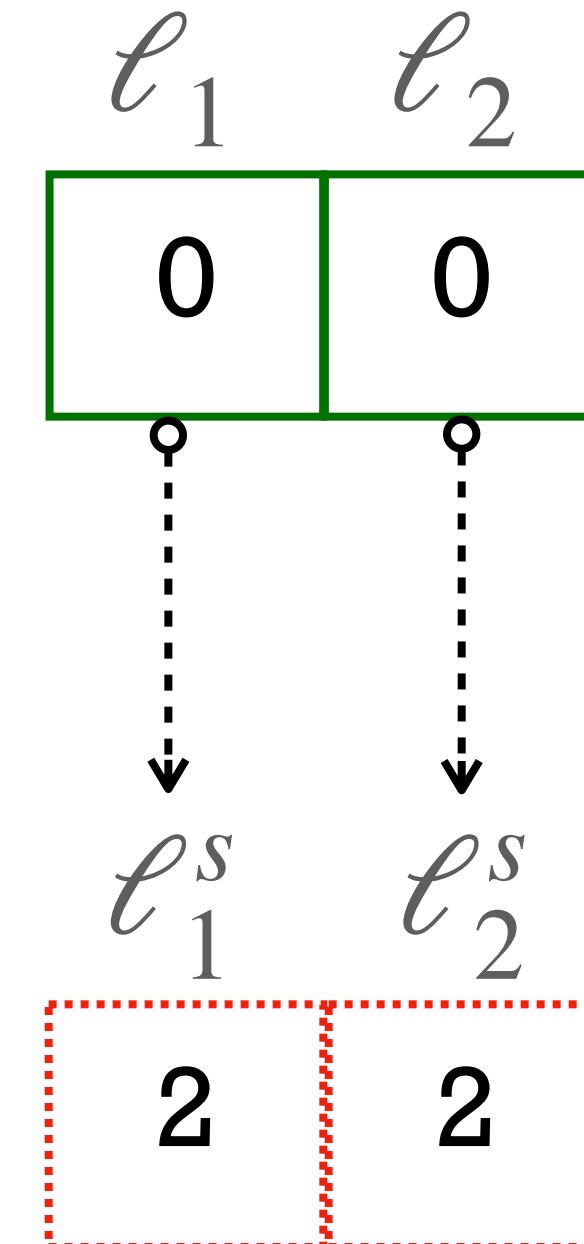
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

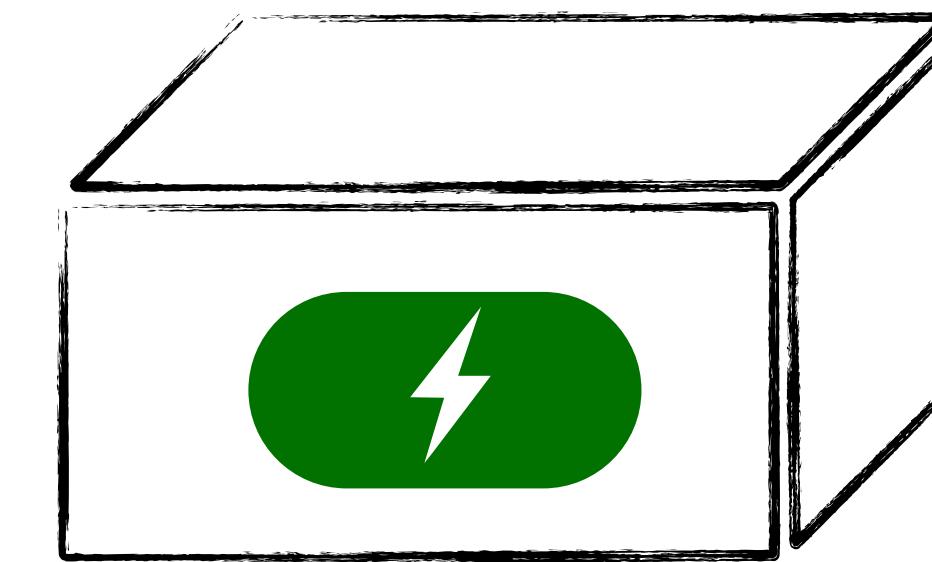
ℓ_1	ℓ_2
2	2



Checkpoint

pc →

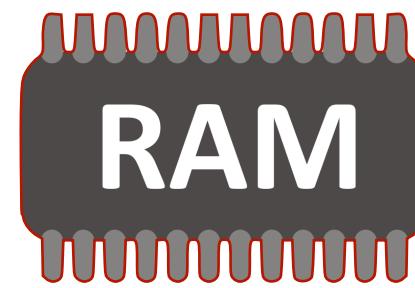
```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```



Solution: Checkpointing blocks (checkpoint-restore-finalize)



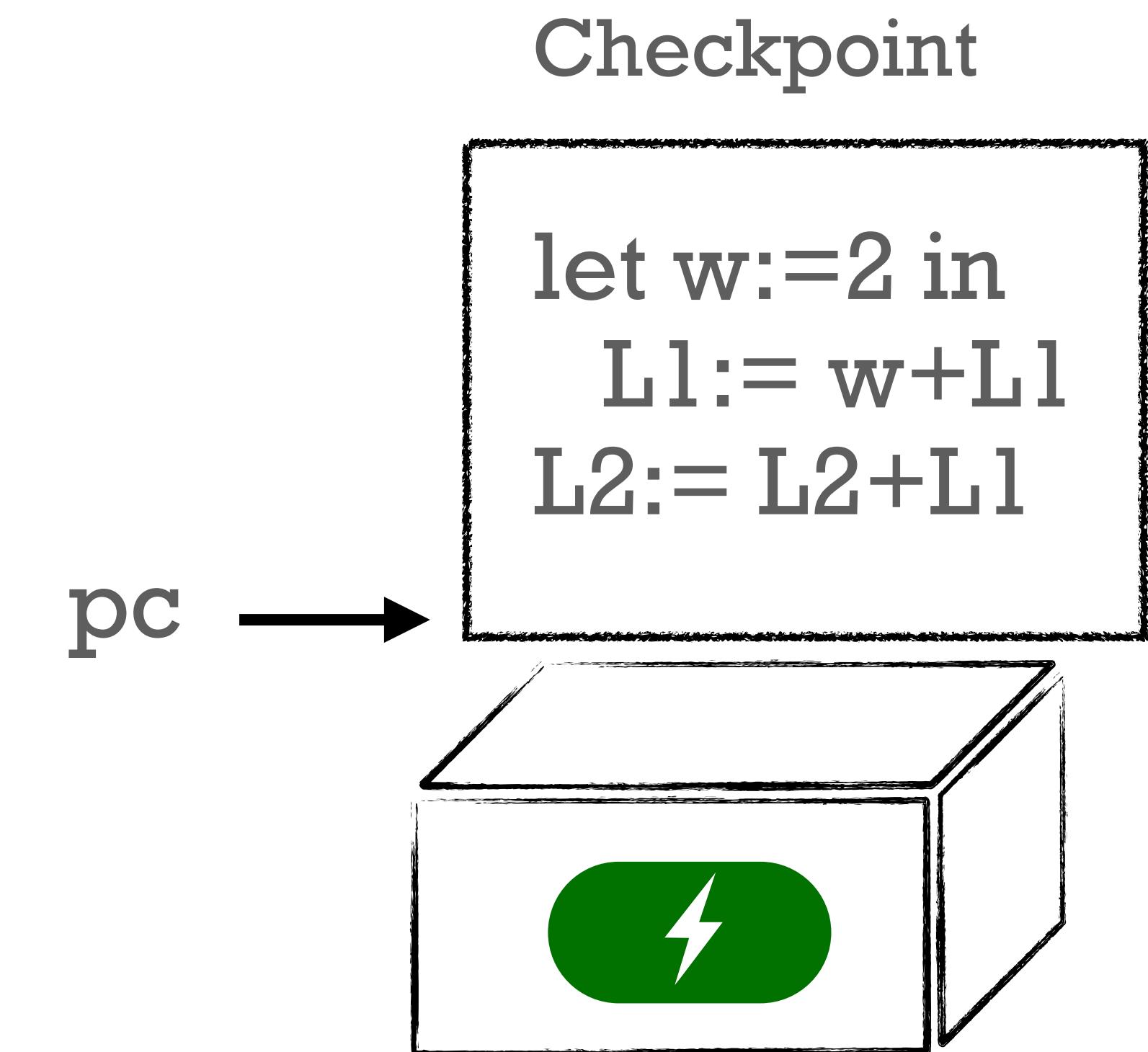
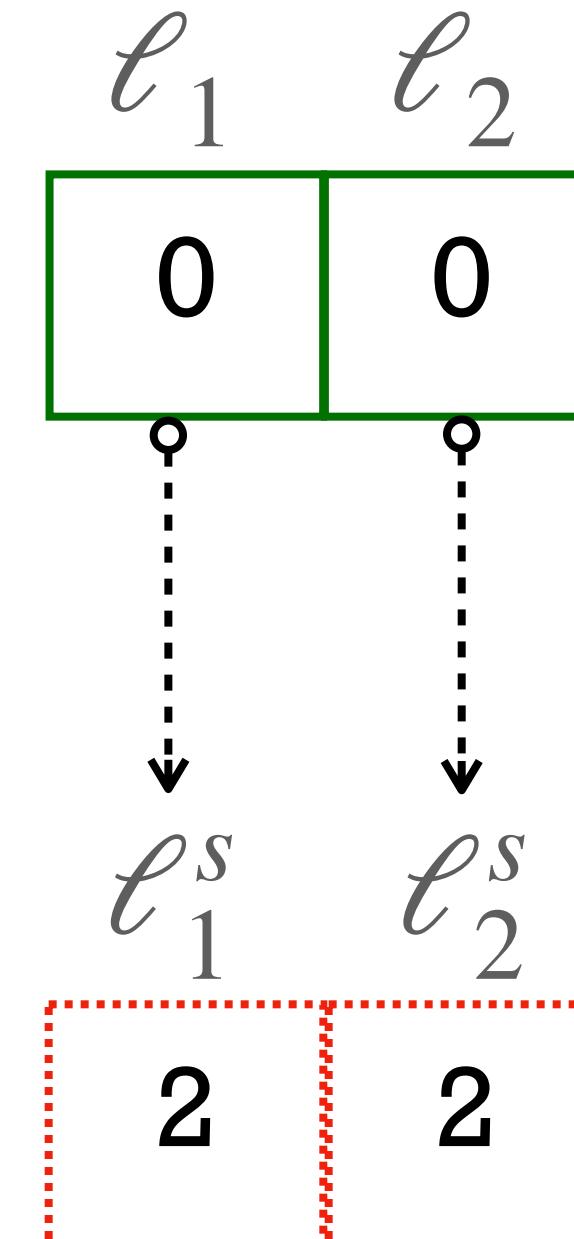
Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

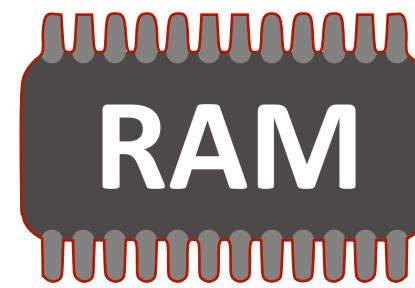
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values

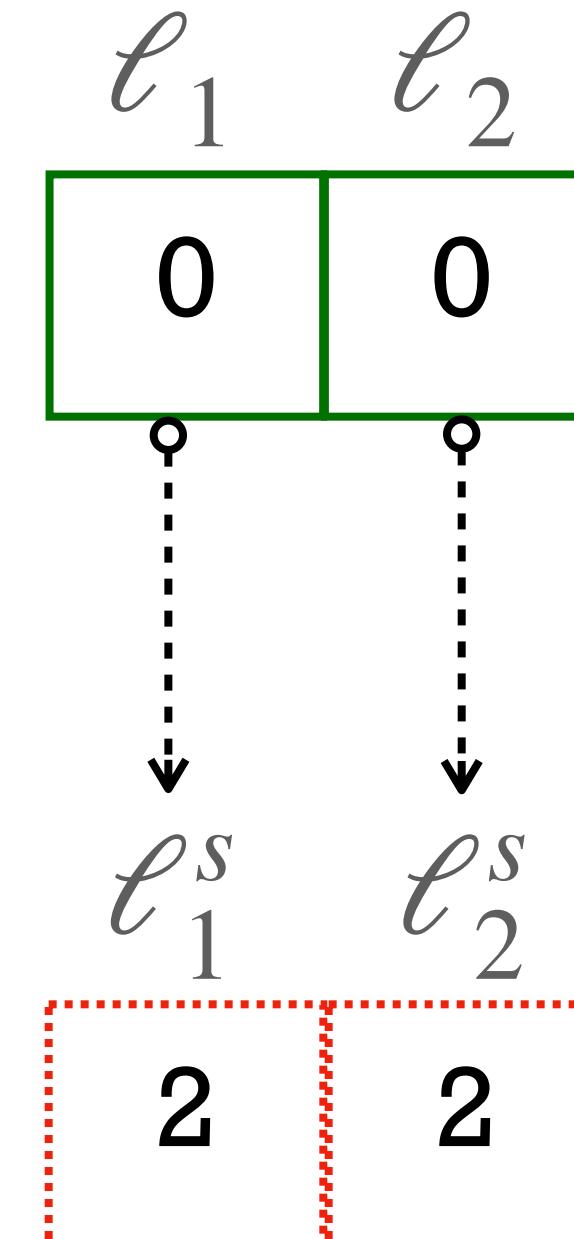


Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2

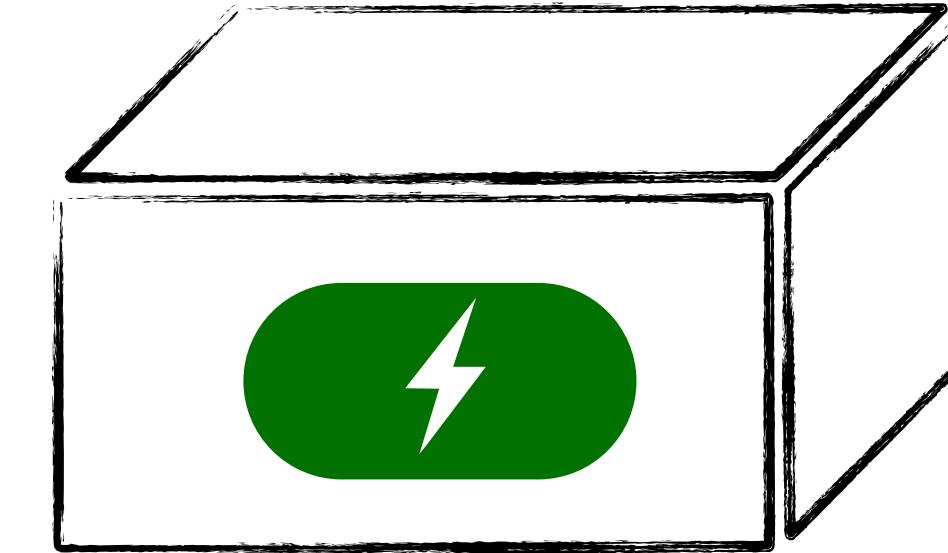
Finalize state



Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

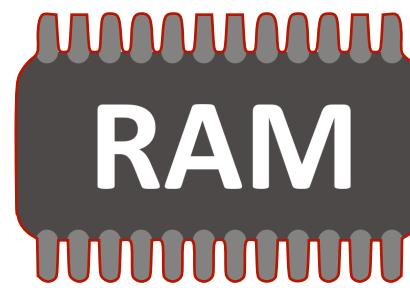
pc →



Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values

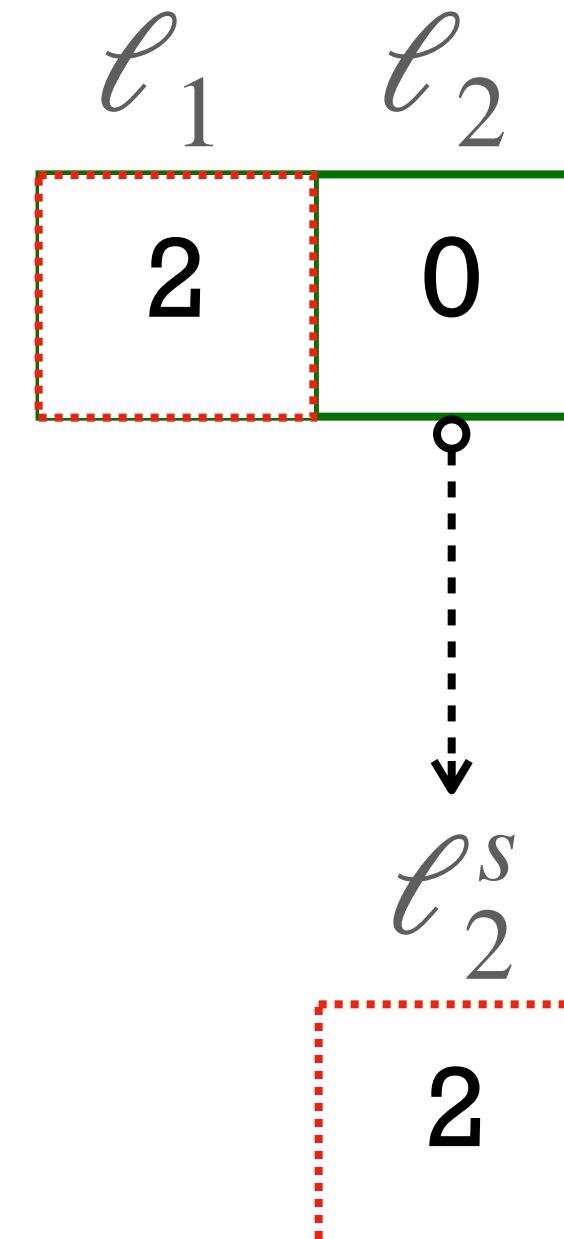


Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2

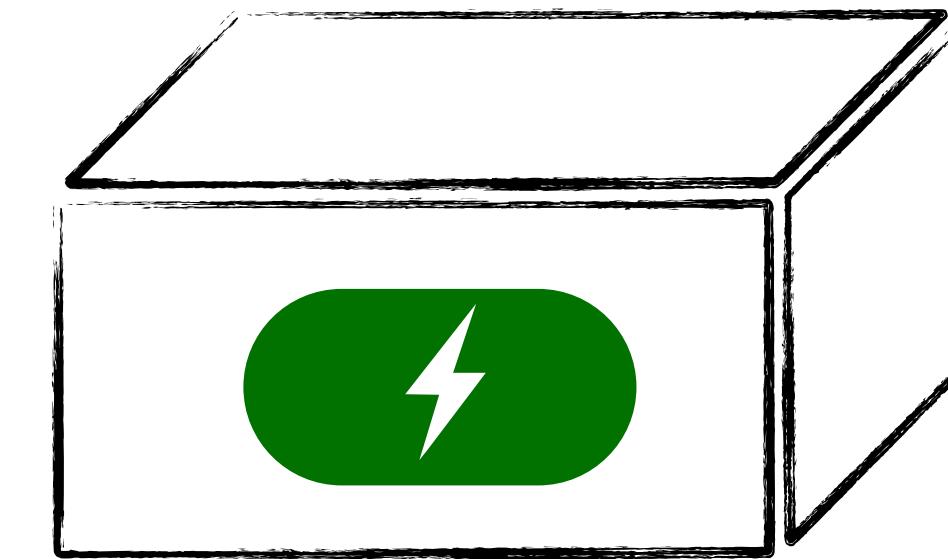
Finalize state



Checkpoint

```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```

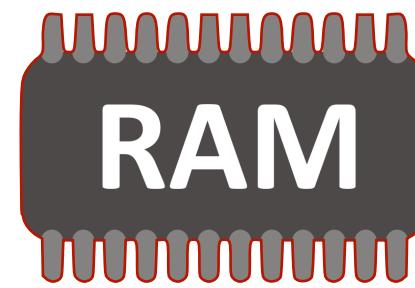
pc →



Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2

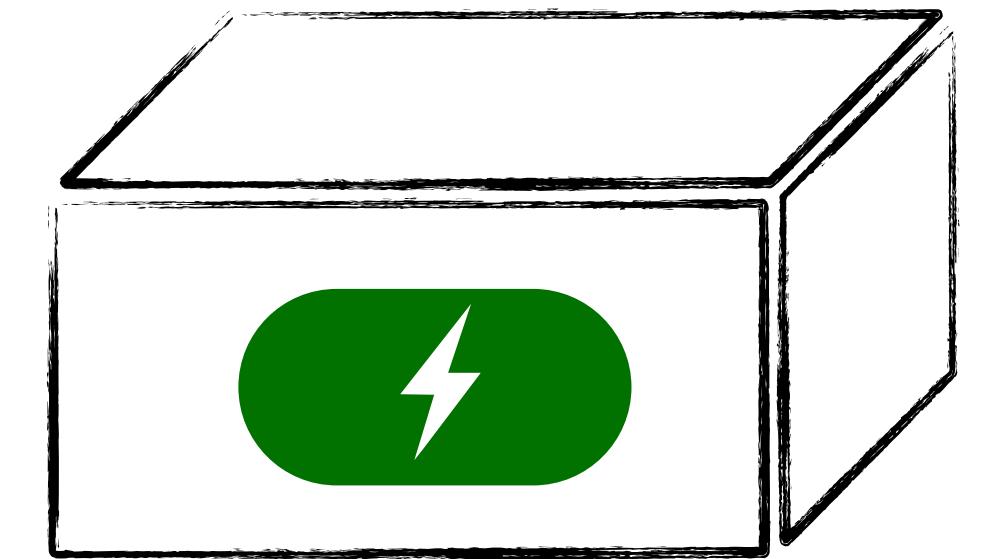
Finalize state

ℓ_1	ℓ_2
2	2

Checkpoint

```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```

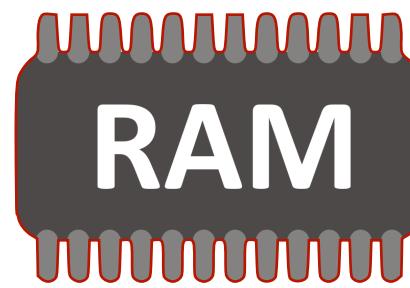
pc →



Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2

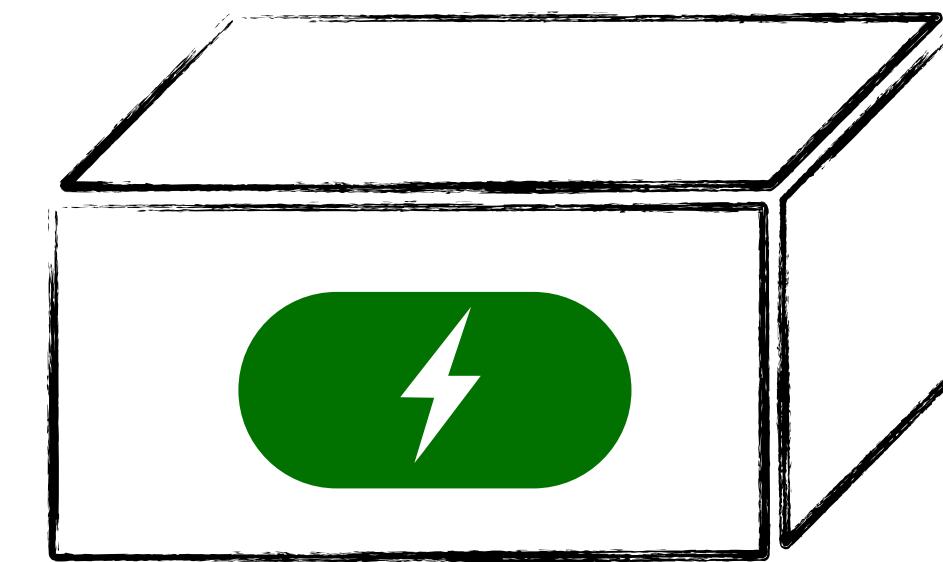
Finalize state

ℓ_1	ℓ_2
2	2

Checkpoint

```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```

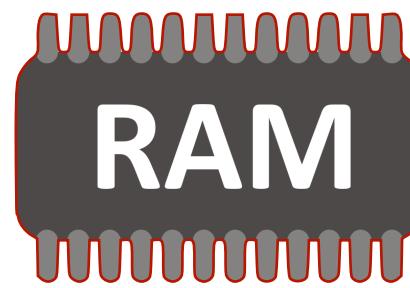
pc →



Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values

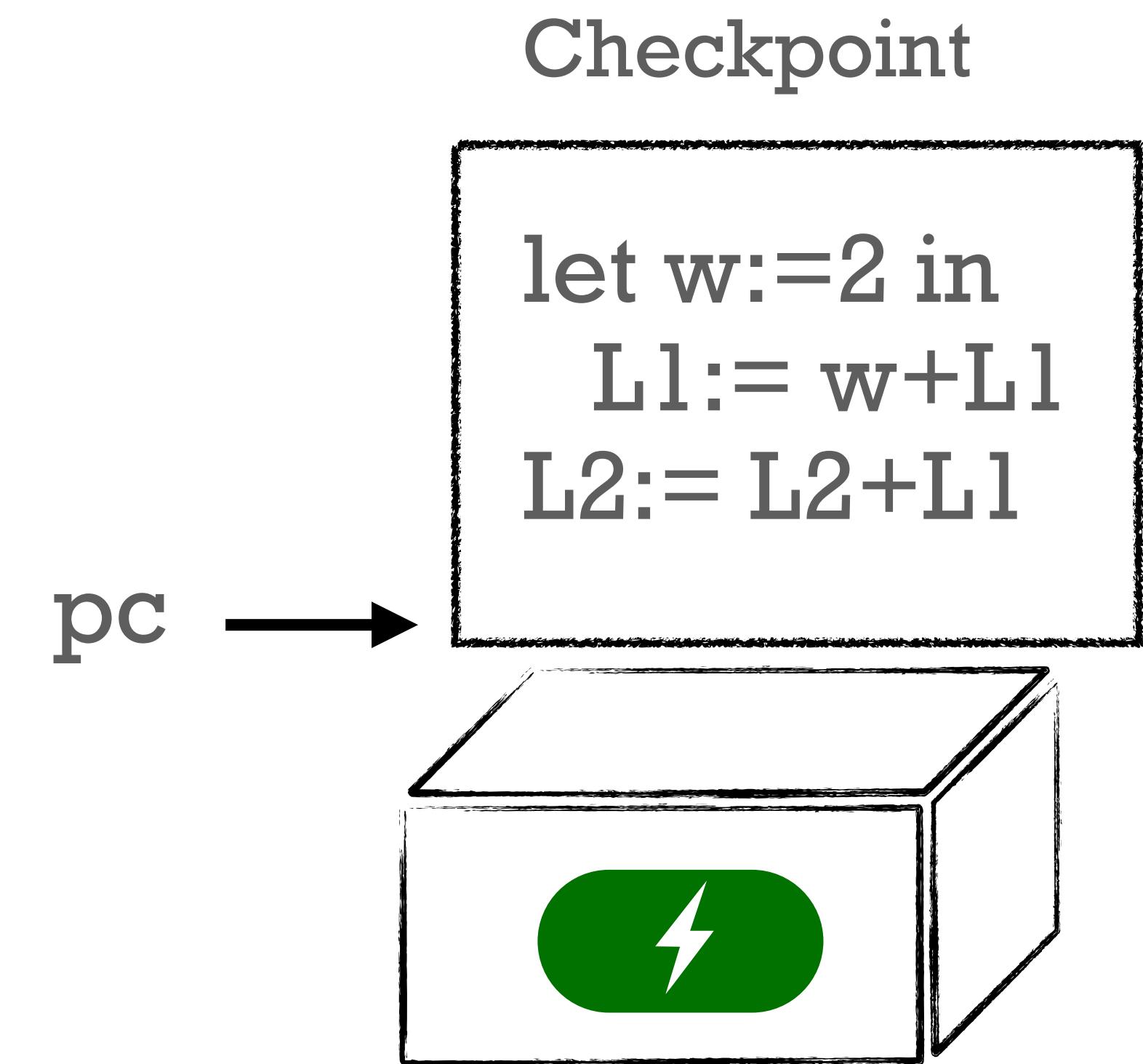


Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2

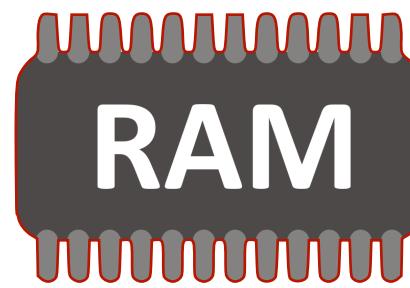
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values



Volatile memory
Unstable values

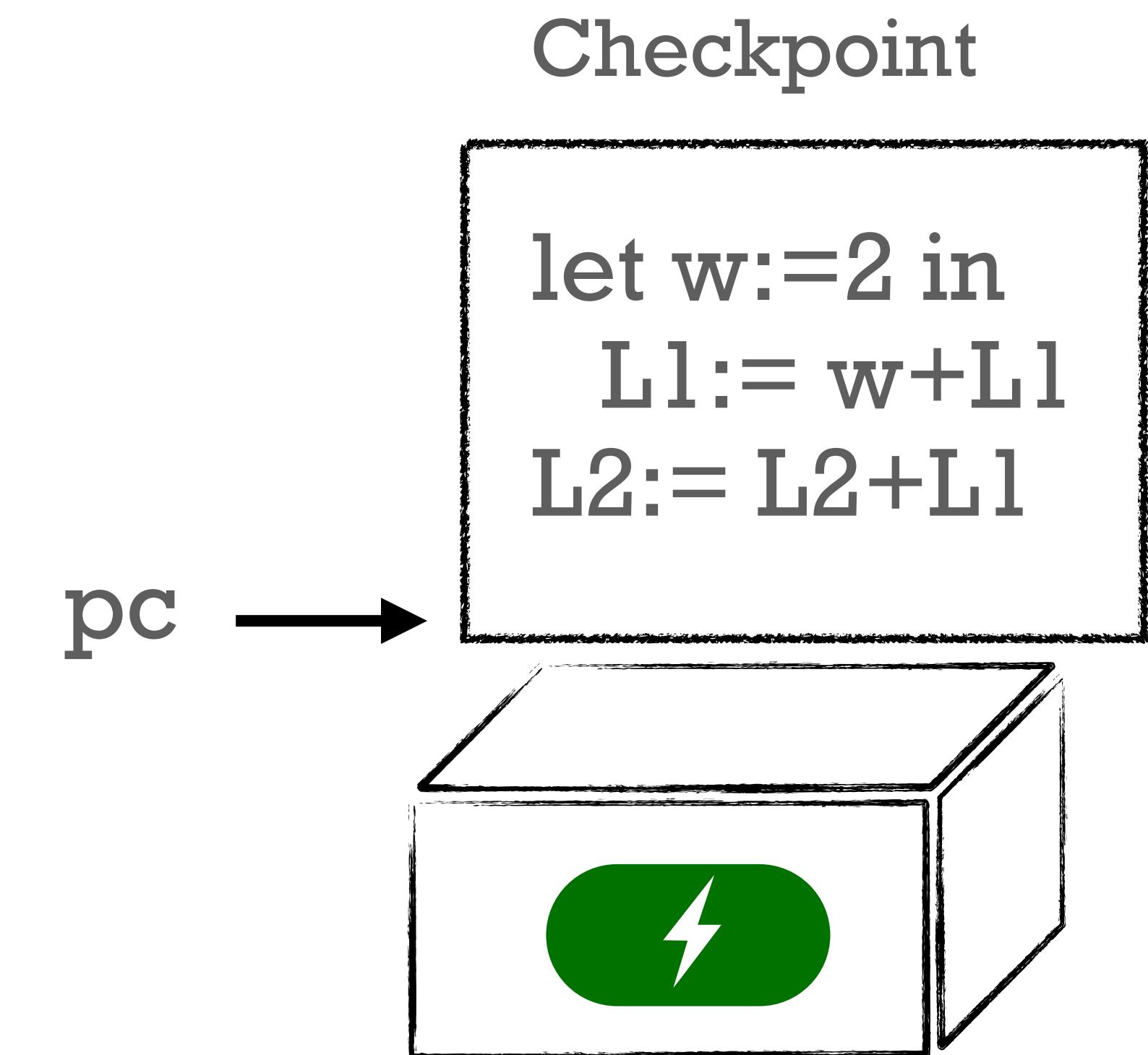
Final memory
state we expect:

ℓ_1	ℓ_2
2	2



Correct final state:

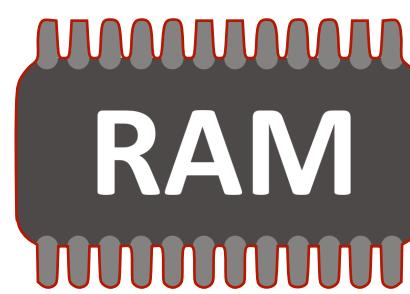
ℓ_1	ℓ_2
2	2



Solution: Checkpointing blocks (checkpoint-restore-finalize)



Nonvolatile memory
Stable values



Volatile memory
Unstable values

Final memory
state we expect:

ℓ_1	ℓ_2
2	2



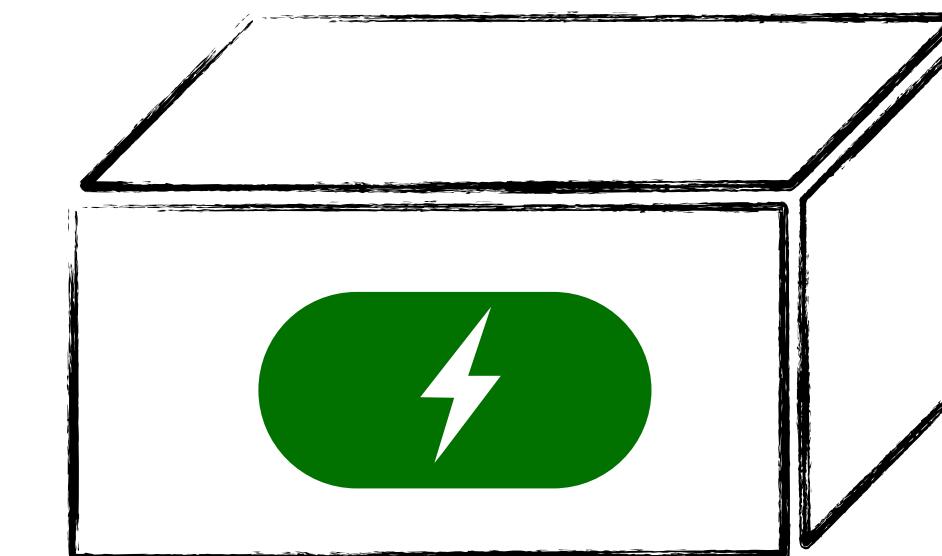
Correct final state:
equivalent to the continuous execution.

ℓ_1	ℓ_2
2	2

Checkpoint

pc →

```
let w:=2 in  
    L1:= w+L1  
    L2:= L2+L1
```



Intermittent execution in energy harvesting devices

Intermittent execution in energy harvesting devices

Prior work:

Practical solutions based on *checkpointing*, *restoring*, and *finalizing state*.
(Surbatovich et al. OOPSLA 2019, OOPSLA 2020, PLDI 2021, PLDI2023),

Intermittent execution in energy harvesting devices

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This work:

Fundamental logical underpinning of these operations.

Intermittent execution in energy harvesting devices

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Practical solutions based on *checkpointing*, *restoring*, and *finalizing state*.
(Surbatovich et al. OOPSLA 2019, OOPSLA 2020, PLDI 2021, PLDI2023),

This work:

Fundamental logical underpinning of these operations.

- Crash types based on adjoint logic
- A type system to rule out incorrect intermittent executions
- A logical relation to prove that all well-typed programs are correct

Outline

- A type system based on adjoint modalities and independence principle
- Correctness as a logical relation
- Conclusion

A type system based on adjoint logic

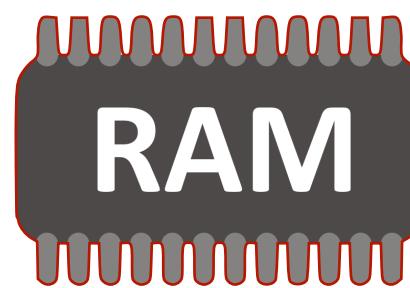


Nonvolatile memory
Stable values

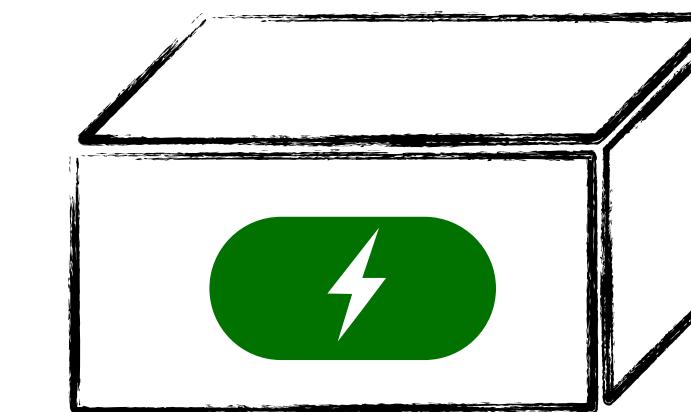
ℓ_1	ℓ_2
0	0

Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```



Volatile memory
Unstable values



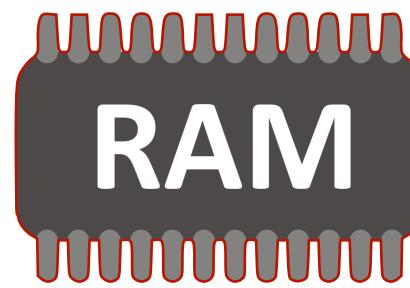
A type system based on adjoint logic



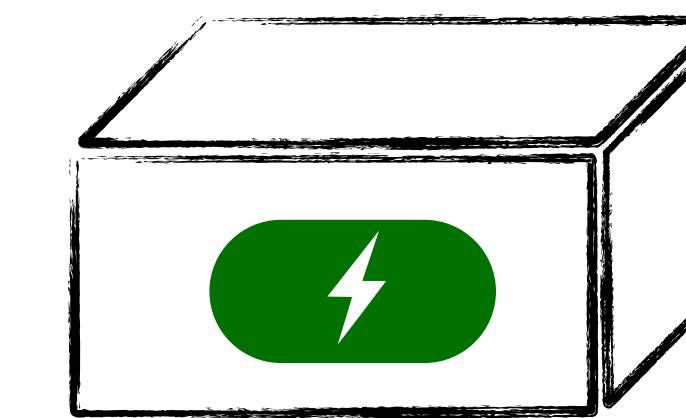
Nonvolatile memory
Stable values
Int

ℓ_1	ℓ_2
0	0

Checkpoint
let w:=2 in
L1:= w+L1
L2:= L2+L1



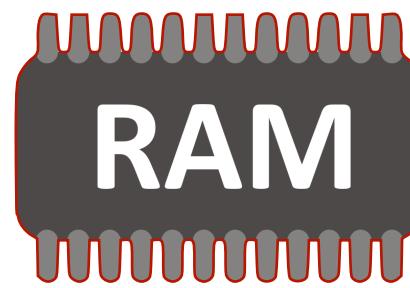
Volatile memory
Unstable values



A type system based on adjoint logic



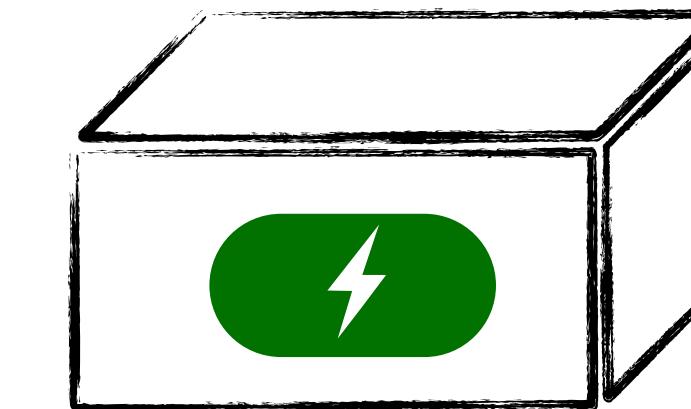
Nonvolatile memory
Stable values
↑ Int



Volatile memory
Unstable values

ℓ_1	ℓ_2
0	0

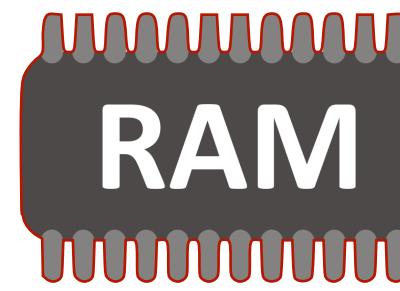
Checkpoint
let w:=2 in
L1:= w+L1
L2:= L2+L1



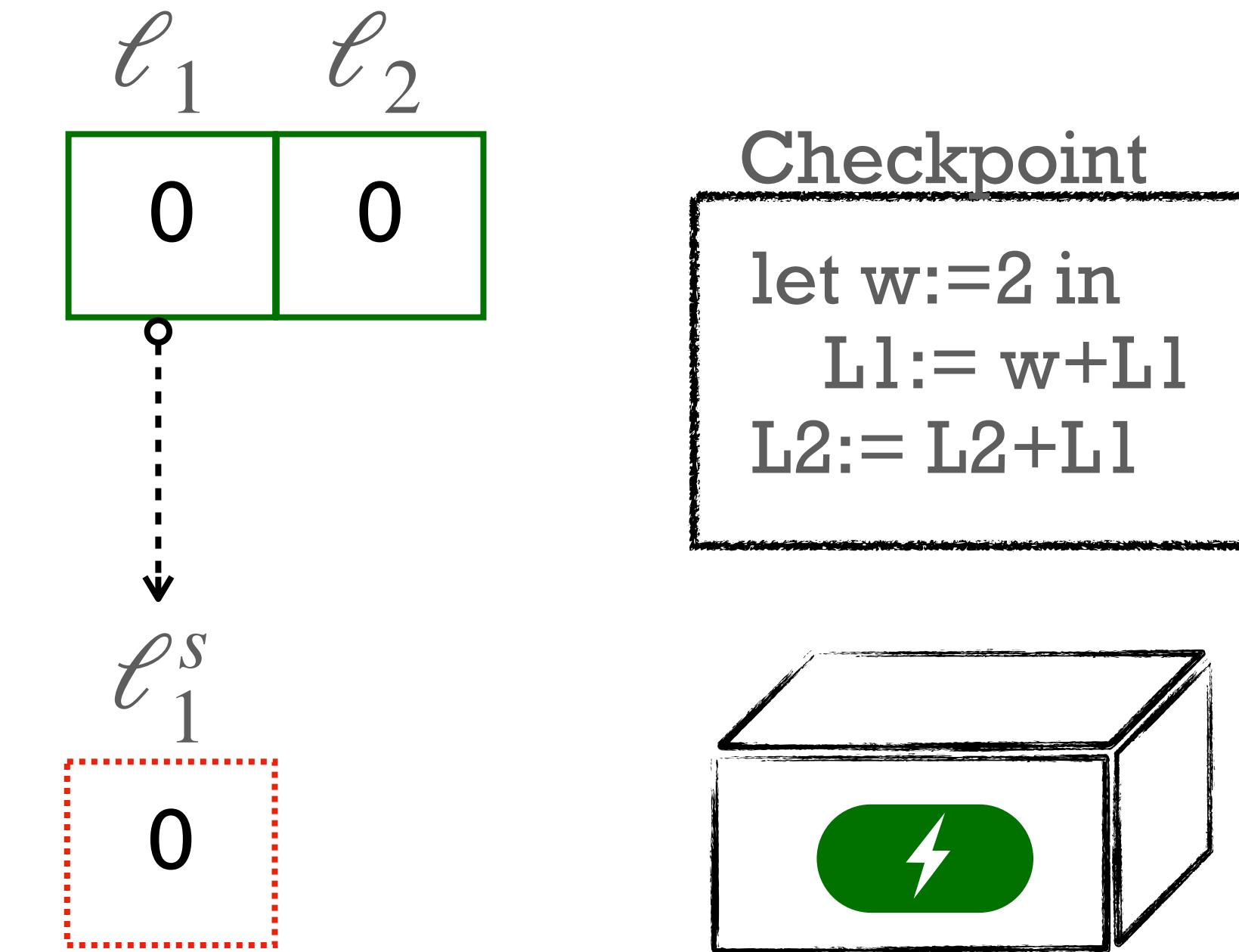
A type system based on adjoint logic



Nonvolatile memory
Stable values
↑ Int



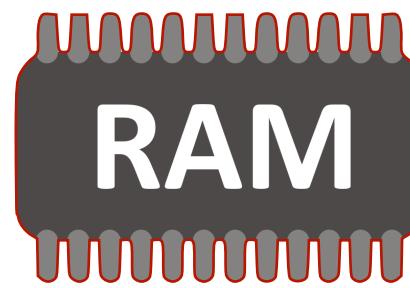
Volatile memory
Unstable values



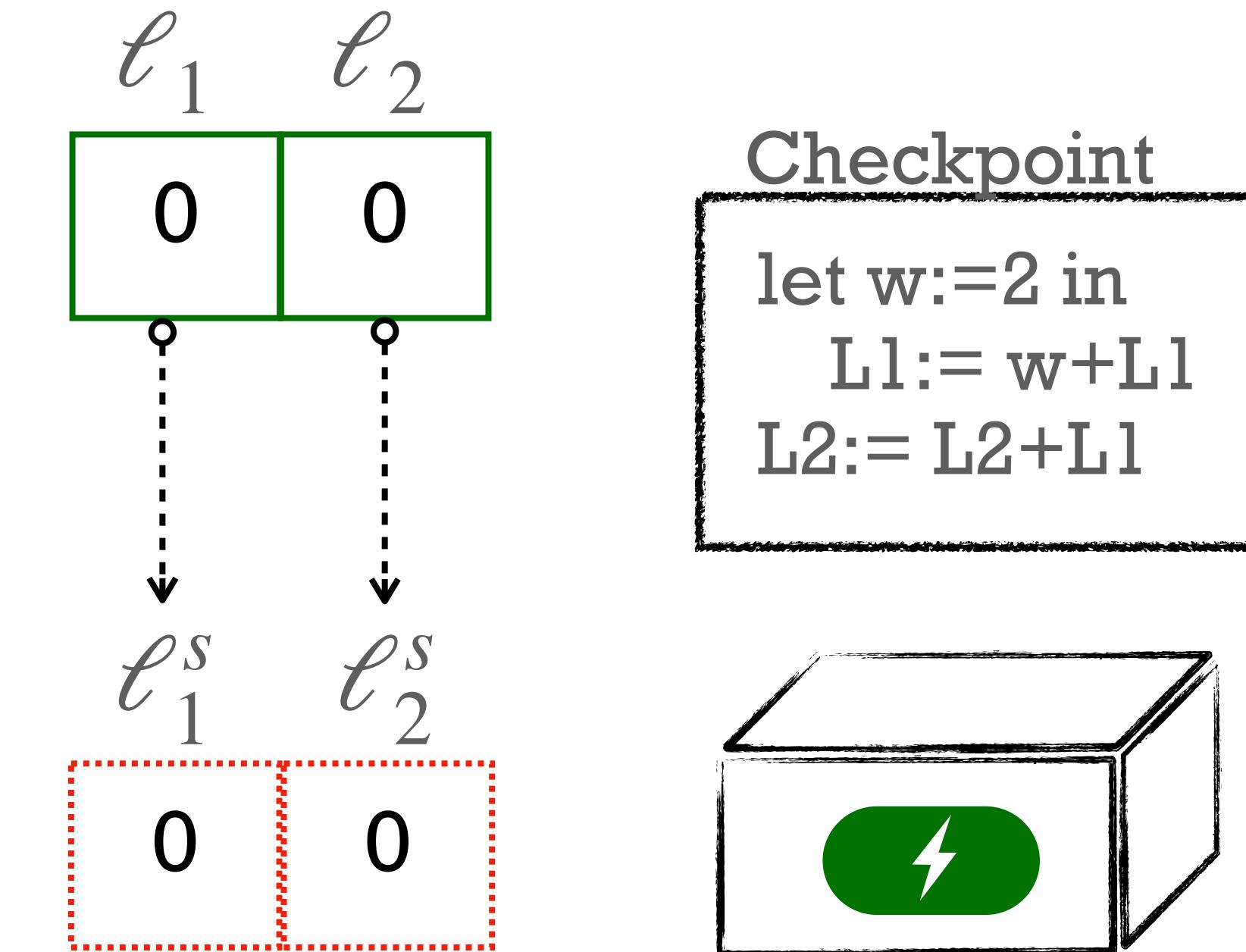
A type system based on adjoint logic



Nonvolatile memory
Stable values
↑ Int



Volatile memory
Unstable values

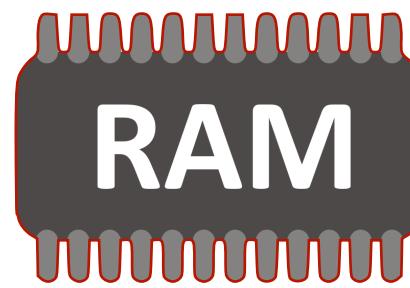


A type system based on adjoint logic



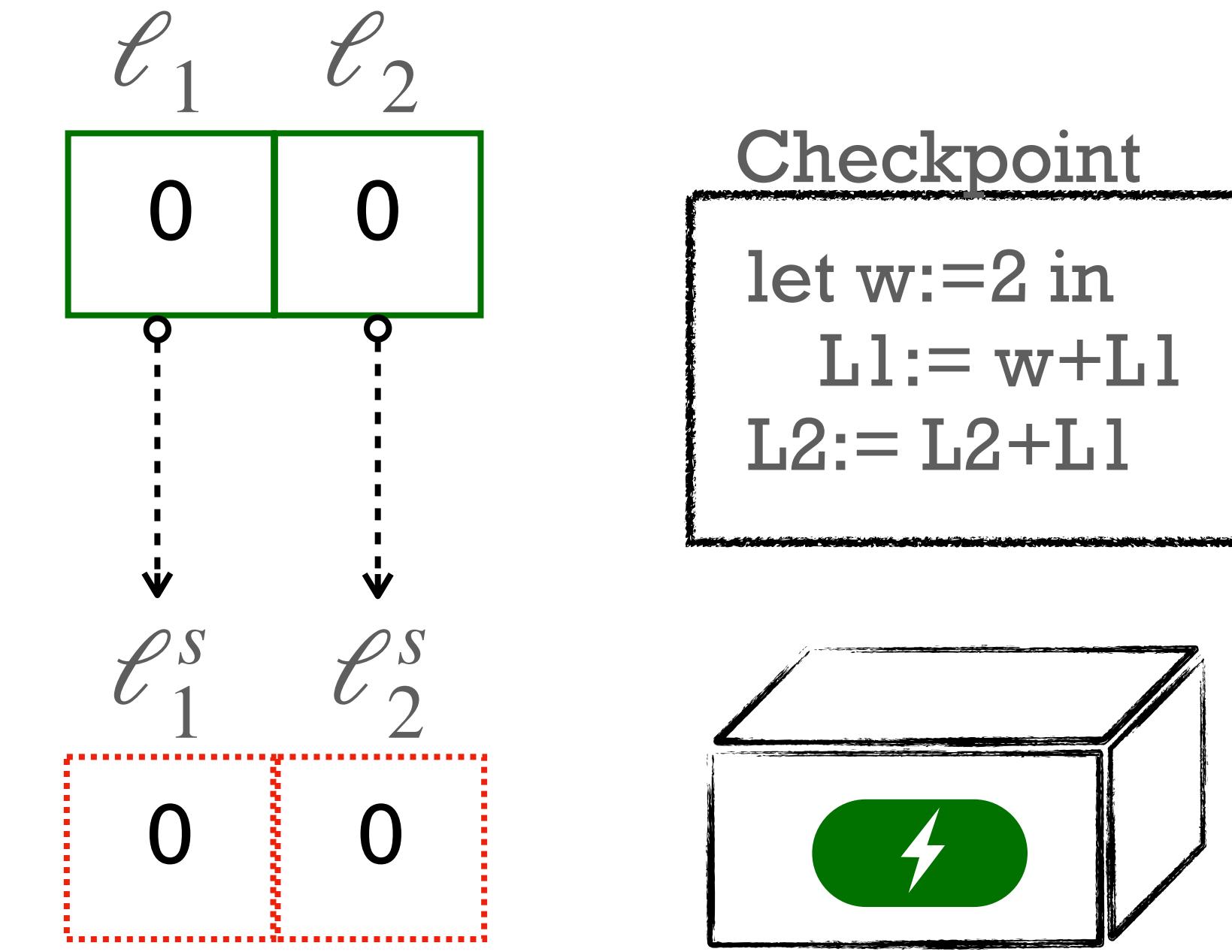
Nonvolatile memory
Stable values

↑ Int



Volatile memory
Unstable values

↑ Int

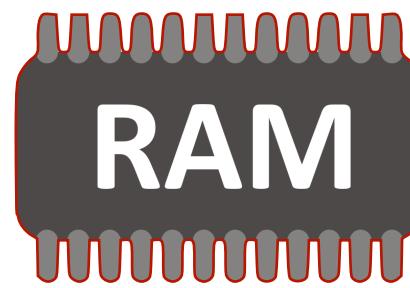


A type system based on adjoint logic



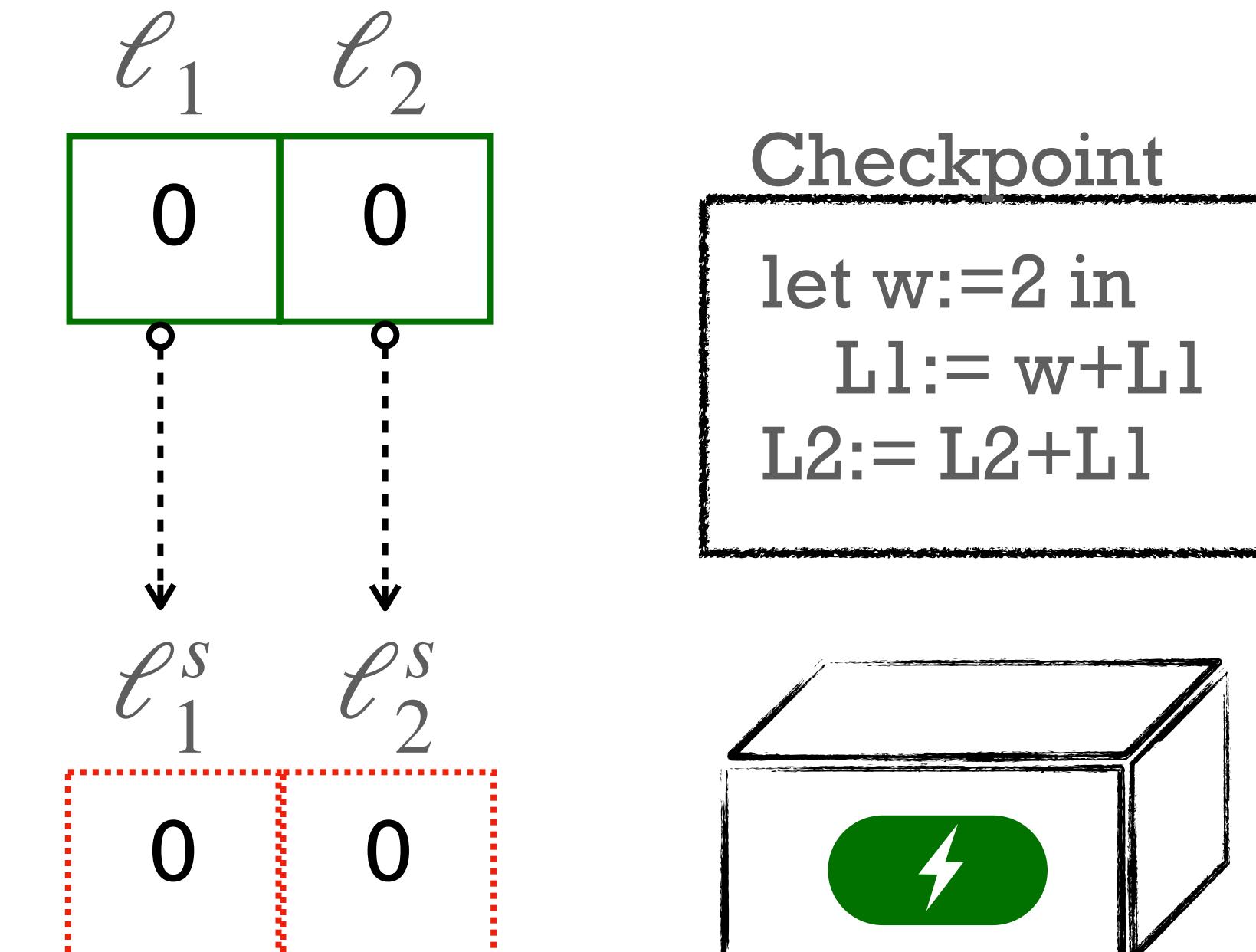
Nonvolatile memory
Stable values

↑ Int



Volatile memory
Unstable values

↓↑ Int

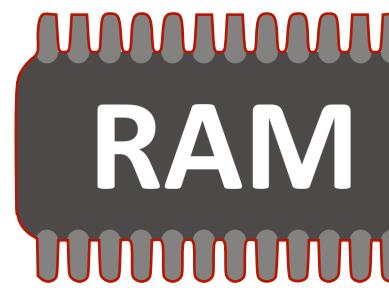


A type system based on adjoint logic



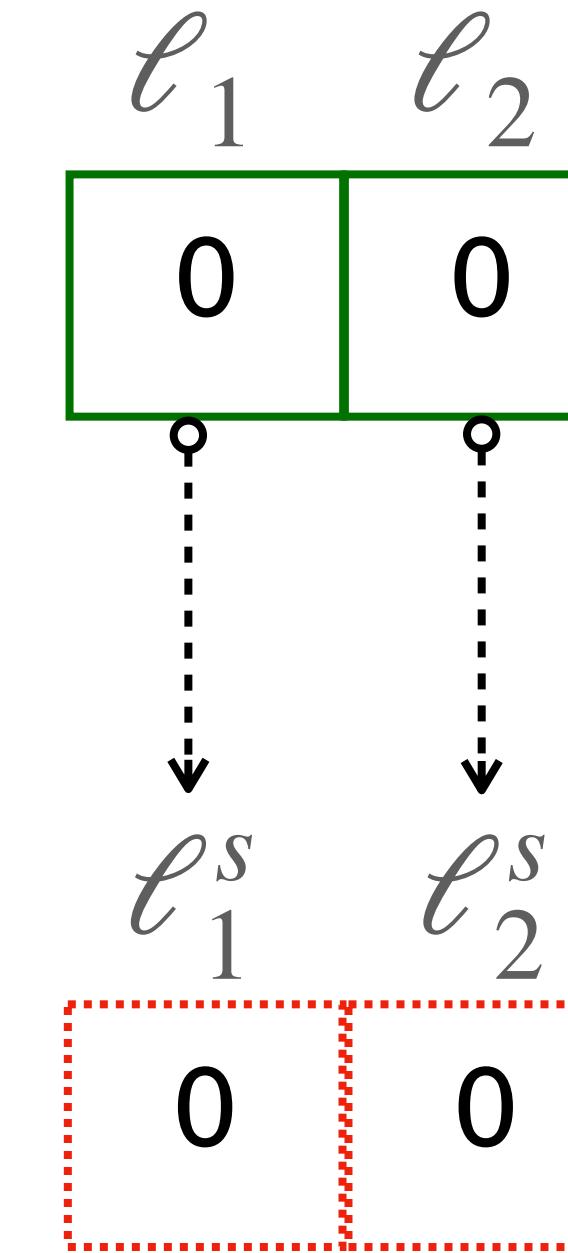
Nonvolatile memory
Stable values

\uparrow Int



Volatile memory
Unstable values

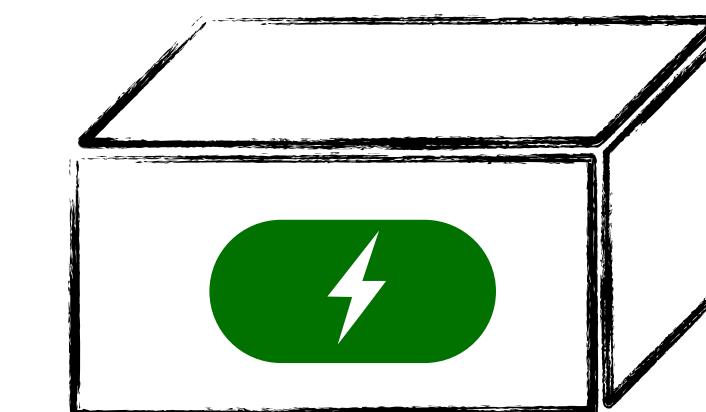
$\downarrow \uparrow$ Int



Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
Unstable types	$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$
Basic types	$T := A \mid \text{unit}$
Store types	$A := \text{int} \mid \text{bool}$

Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

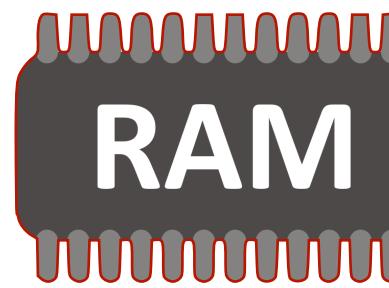


A type system based on adjoint logic



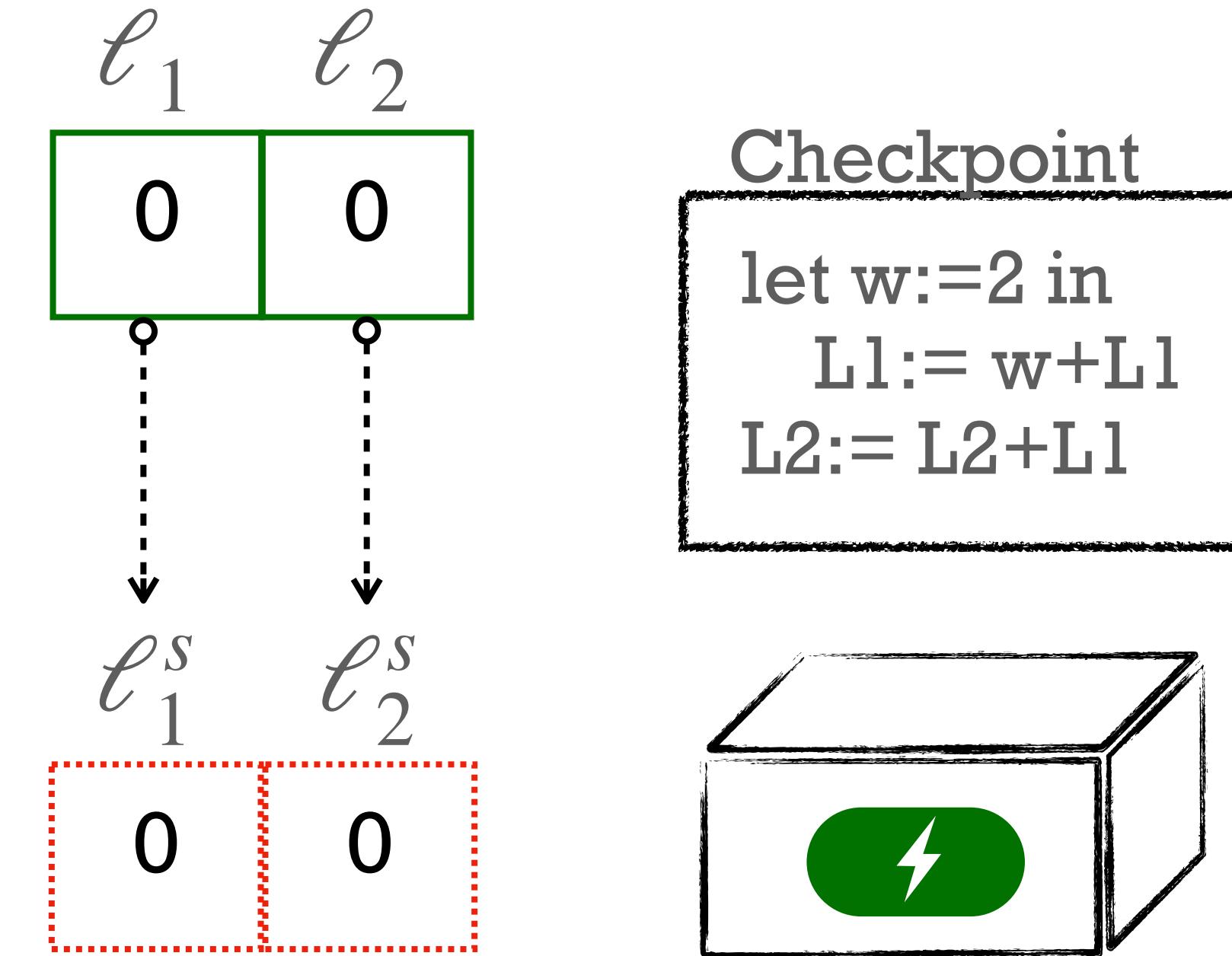
Nonvolatile memory
Stable values

\uparrow Int



Volatile memory
Unstable values

$\downarrow \uparrow$ Int



Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
Unstable types	$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$
Basic types	$T := A \mid \text{unit}$
Store types	$A := \text{int} \mid \text{bool}$

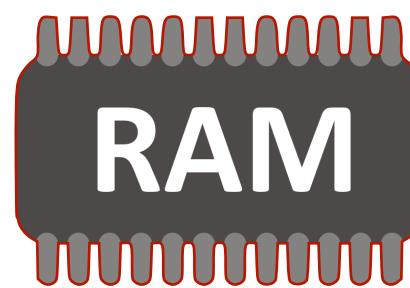
Computation
return type

A type system based on adjoint logic



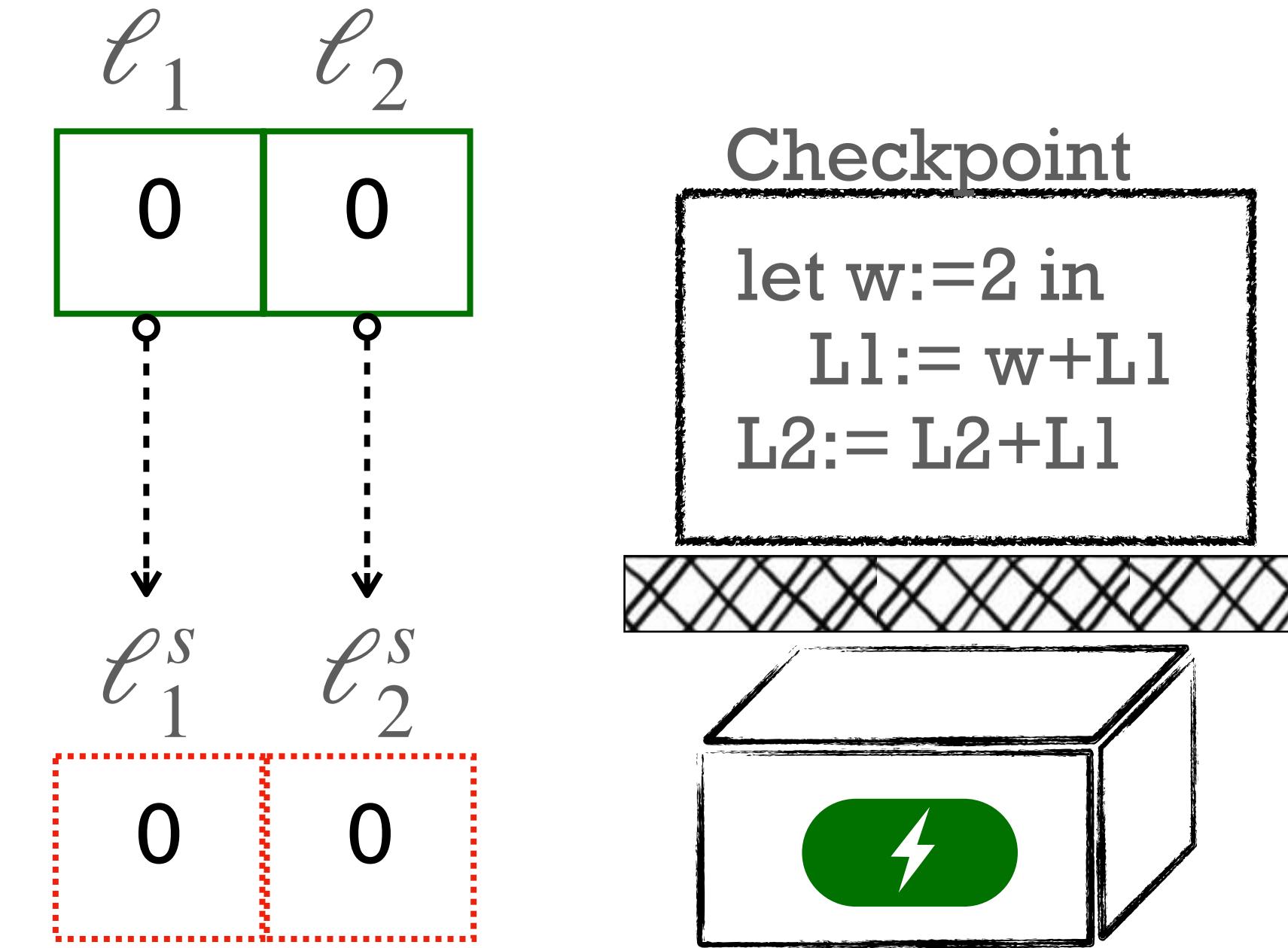
Nonvolatile memory
Stable values

\uparrow Int



Volatile memory
Unstable values

$\downarrow \uparrow$ Int



Stable types
Unstable types
Basic types
Store types

$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
 $\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$
 $T := A \mid \text{unit}$
 $A := \text{int} \mid \text{bool}$

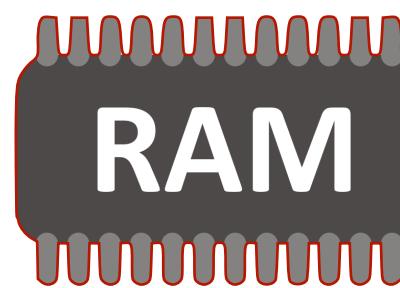
Computation
return type

τ_s Stable
computation

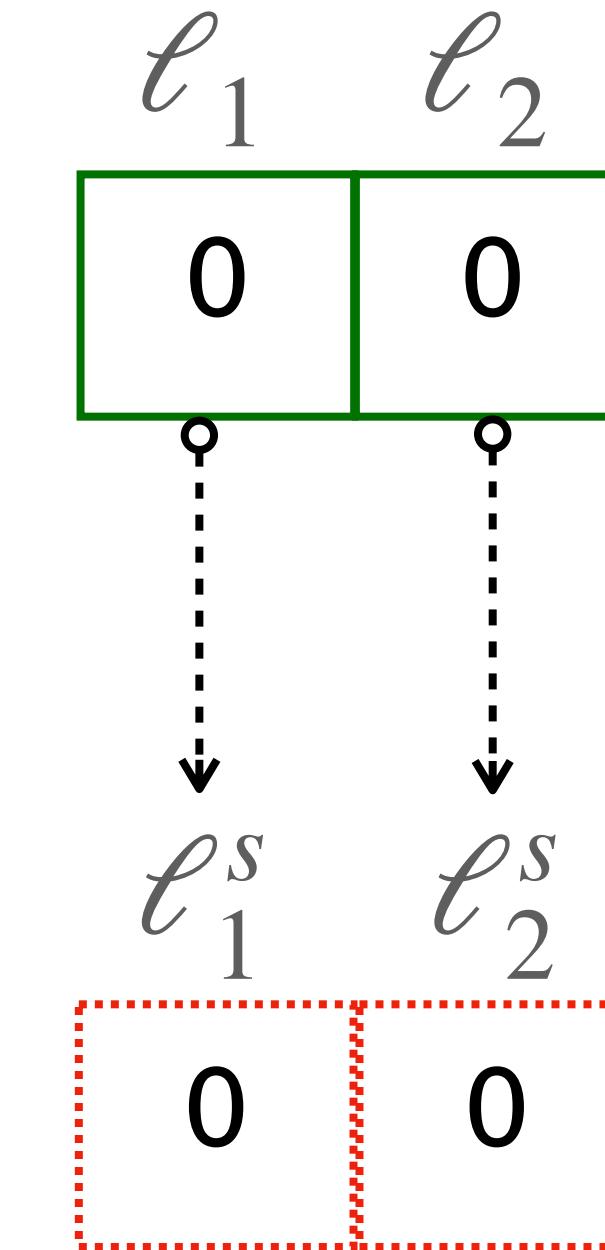
A type system based on adjoint logic



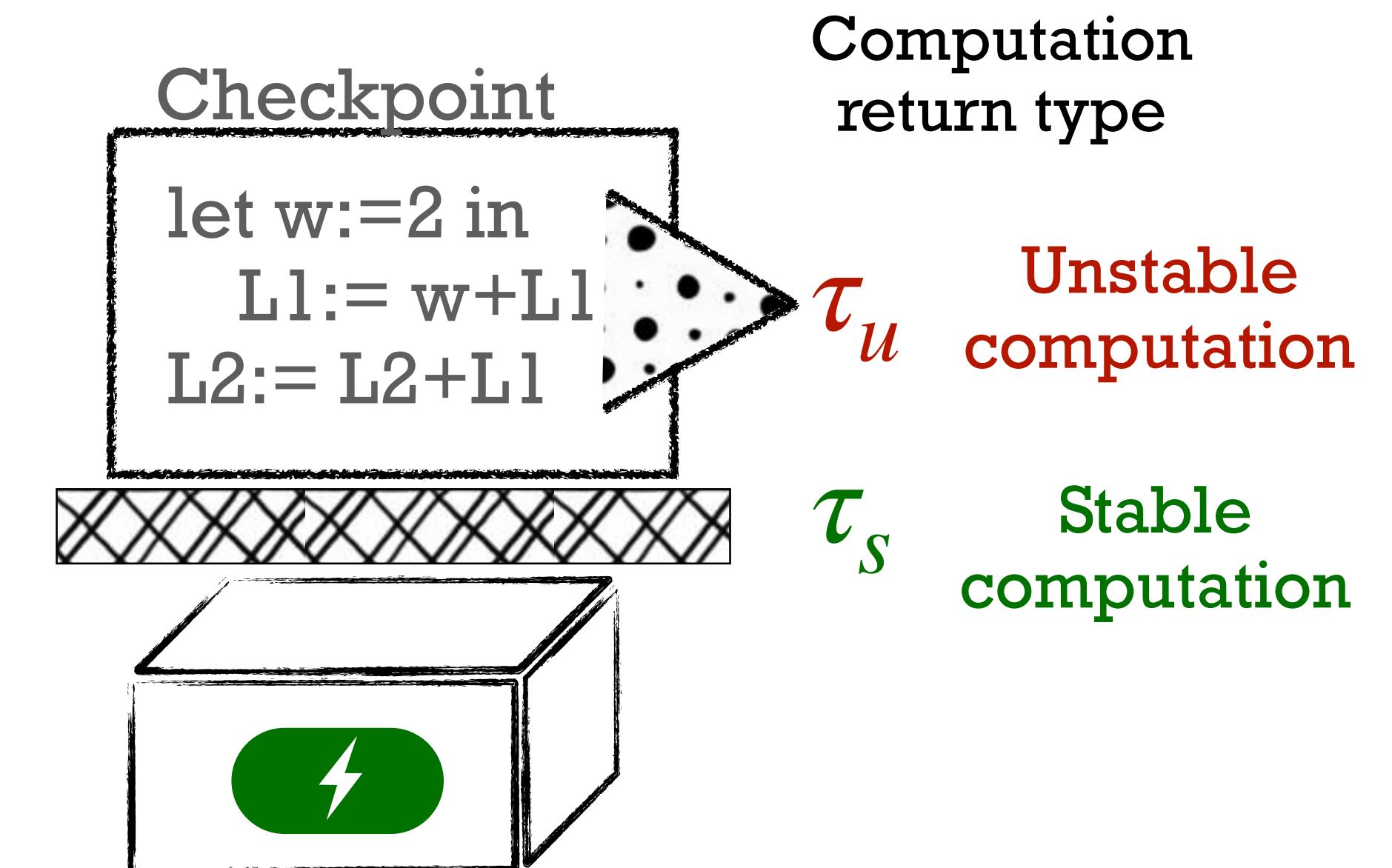
Nonvolatile memory
Stable values
 $\uparrow \text{Int}$



Volatile memory
Unstable values
 $\downarrow \uparrow \text{Int}$



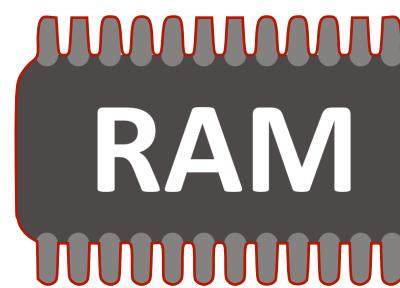
Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
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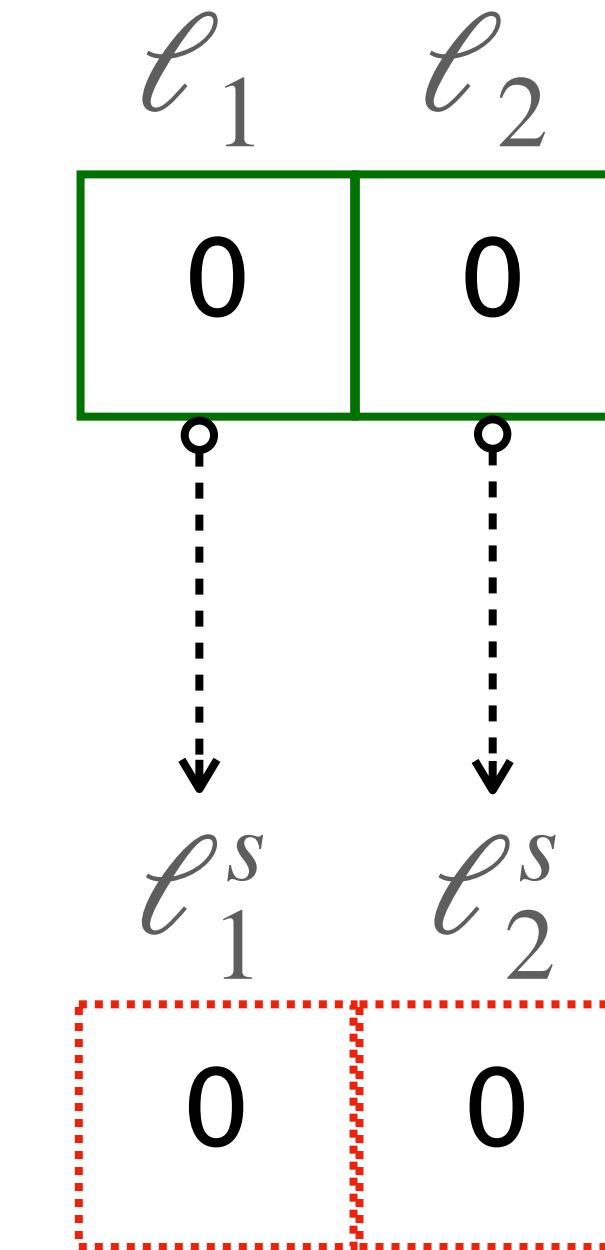
A type system based on adjoint logic



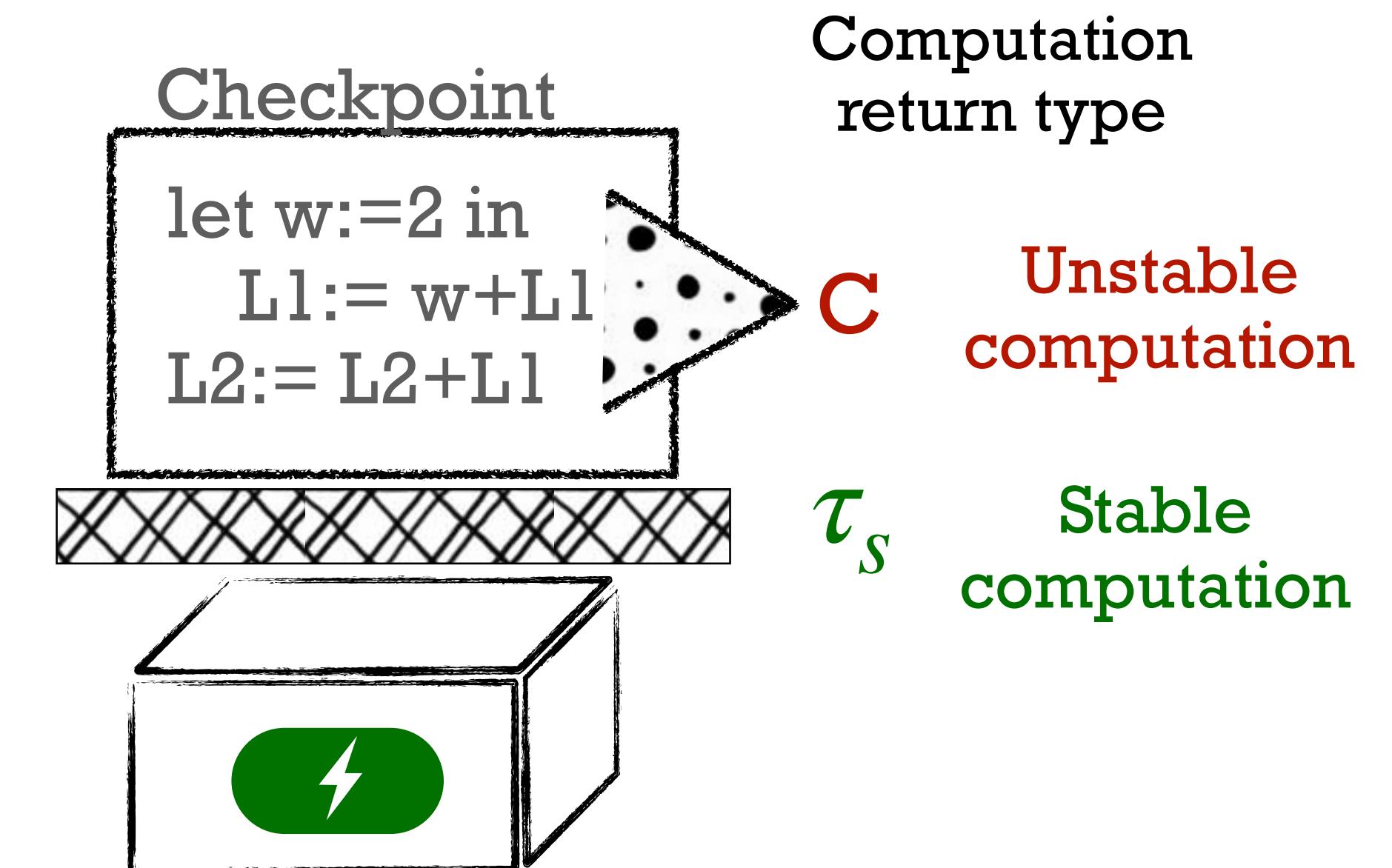
Nonvolatile memory
Stable values
 $\uparrow \text{Int}$



Volatile memory
Unstable values
 $\downarrow \uparrow \text{Int}$



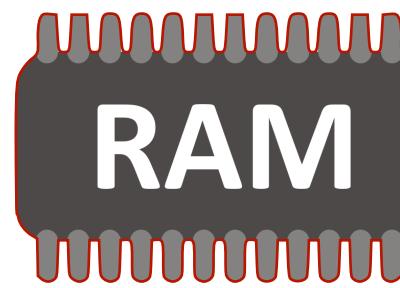
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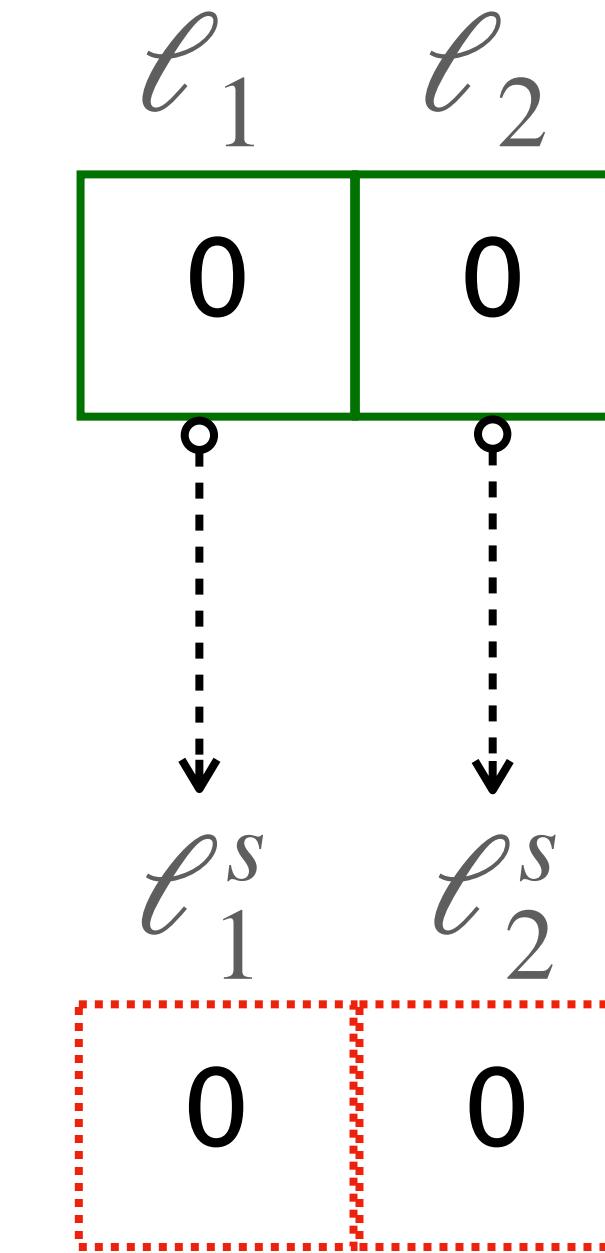
A type system based on adjoint logic



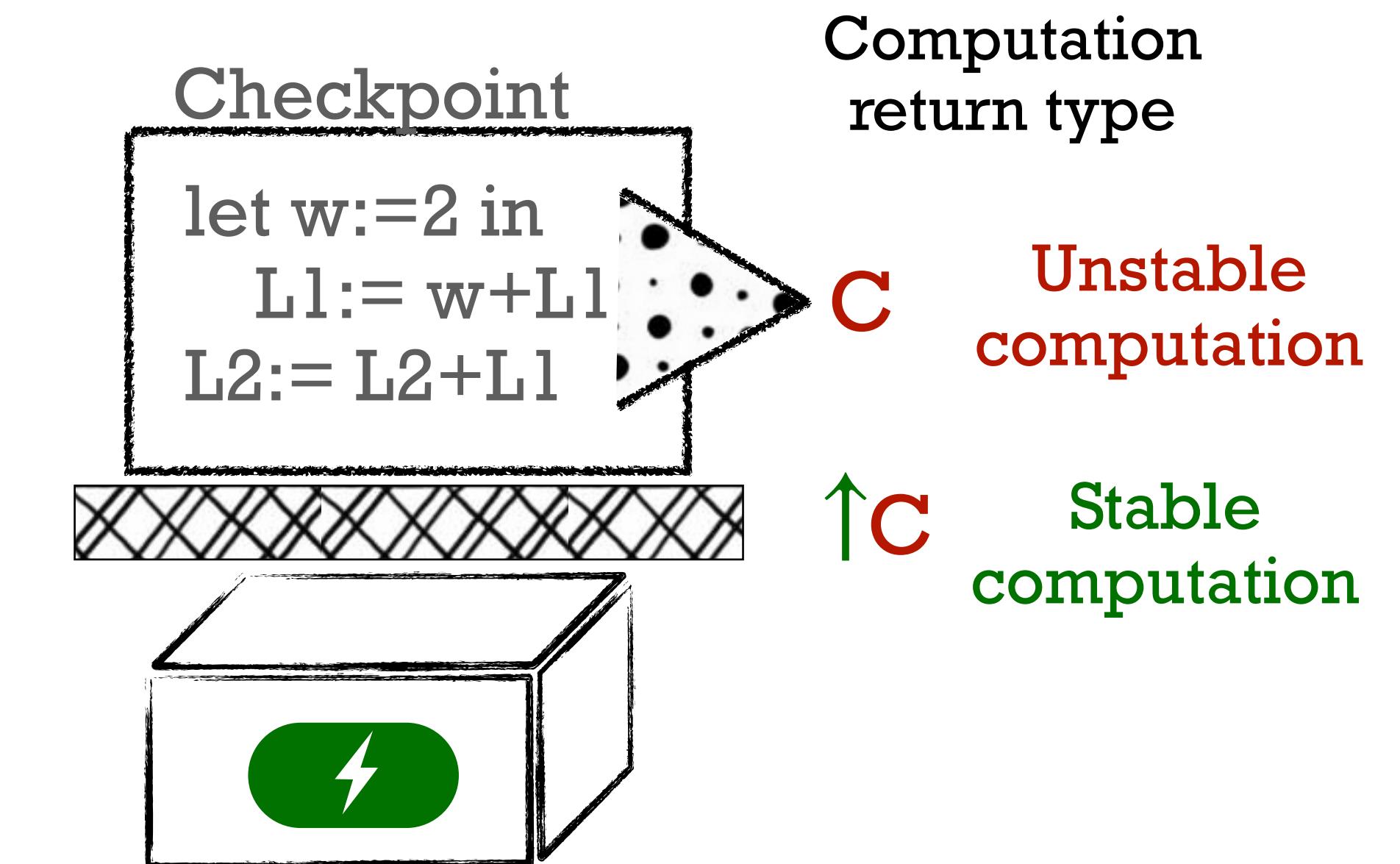
Nonvolatile memory
Stable values
 $\uparrow \text{Int}$



Volatile memory
Unstable values
 $\downarrow \uparrow \text{Int}$



Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
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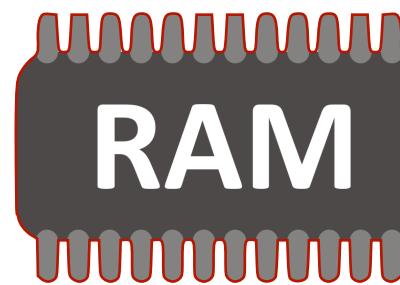
A type system based on adjoint logic



Nonvolatile memory

Stable values

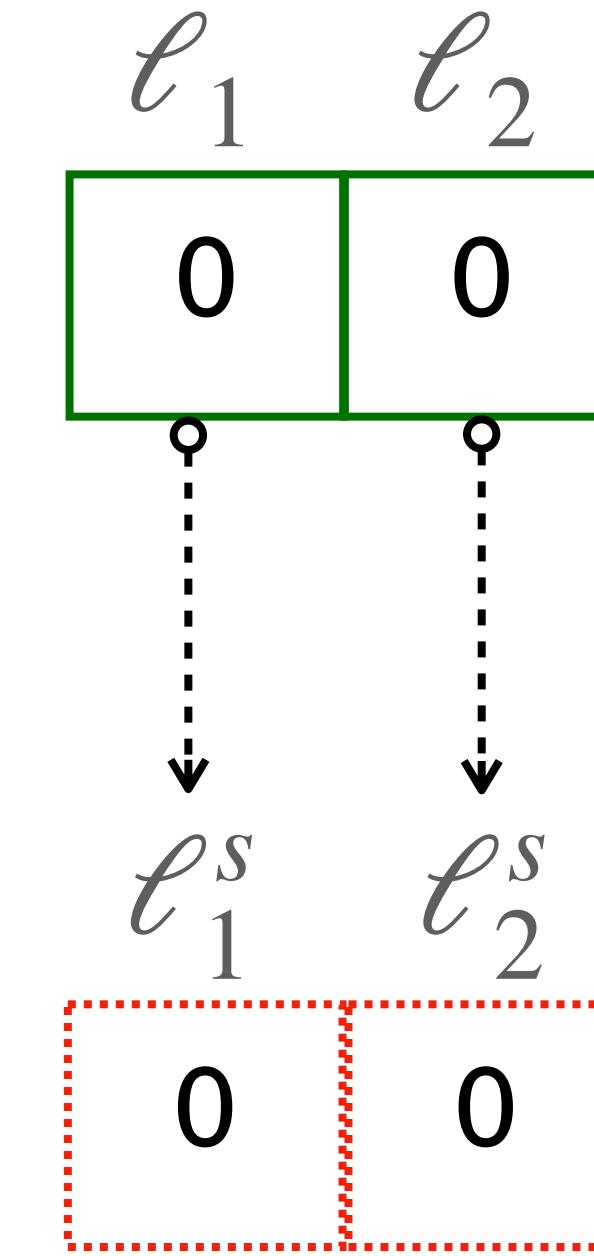
$\uparrow \text{Int}$



Volatile memory

Unstable values

$\downarrow \uparrow \text{Int}$



Stable types

$$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$$

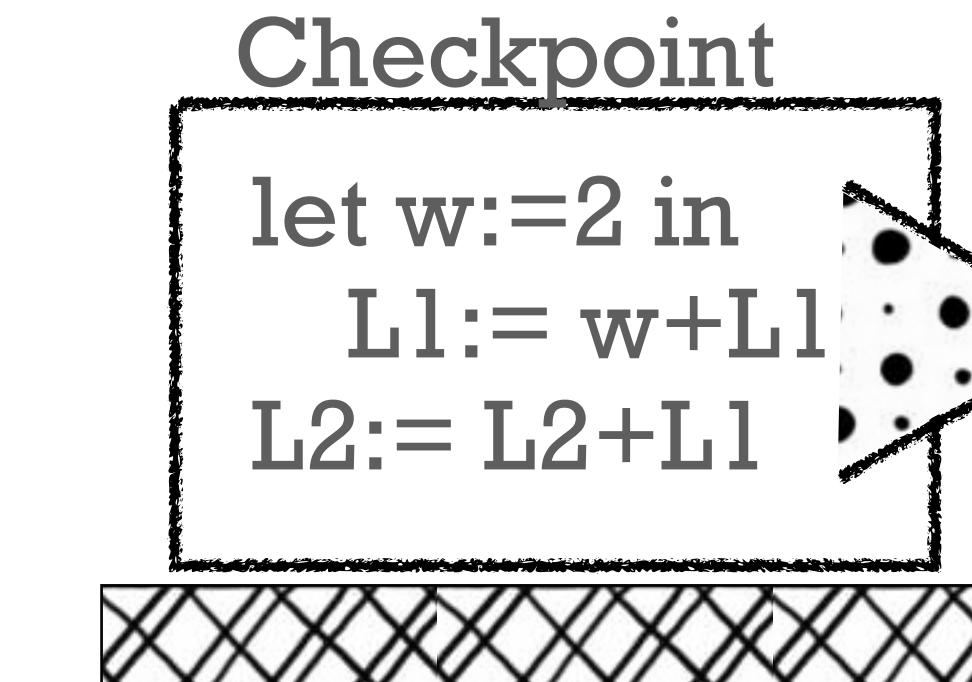
Unstable types

$$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$$

Basic types

$$T := A \mid \text{unit}$$

Store types

$$A := \text{int} \mid \text{bool}$$


Computation
return type

C Unstable
computation

$\uparrow \text{C}$ Stable
computation

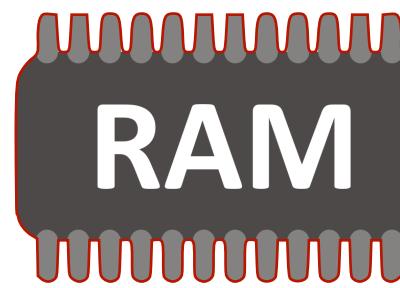
Independence principle: stable values cannot depend on unstable values.

A type system based on adjoint logic



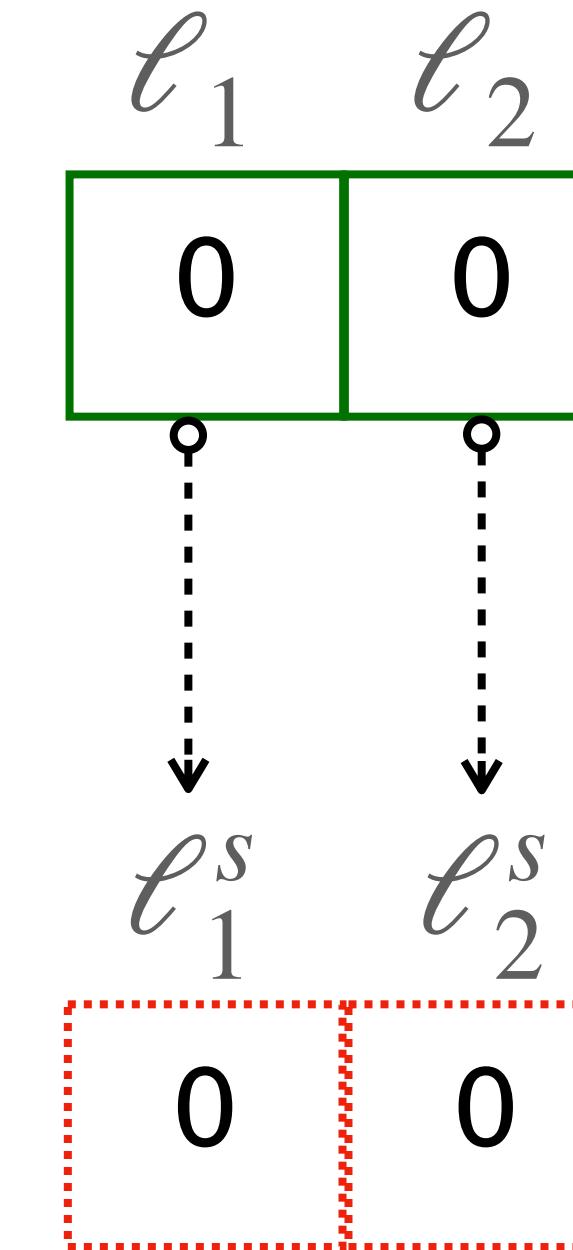
Nonvolatile memory
Stable values

\uparrow Int

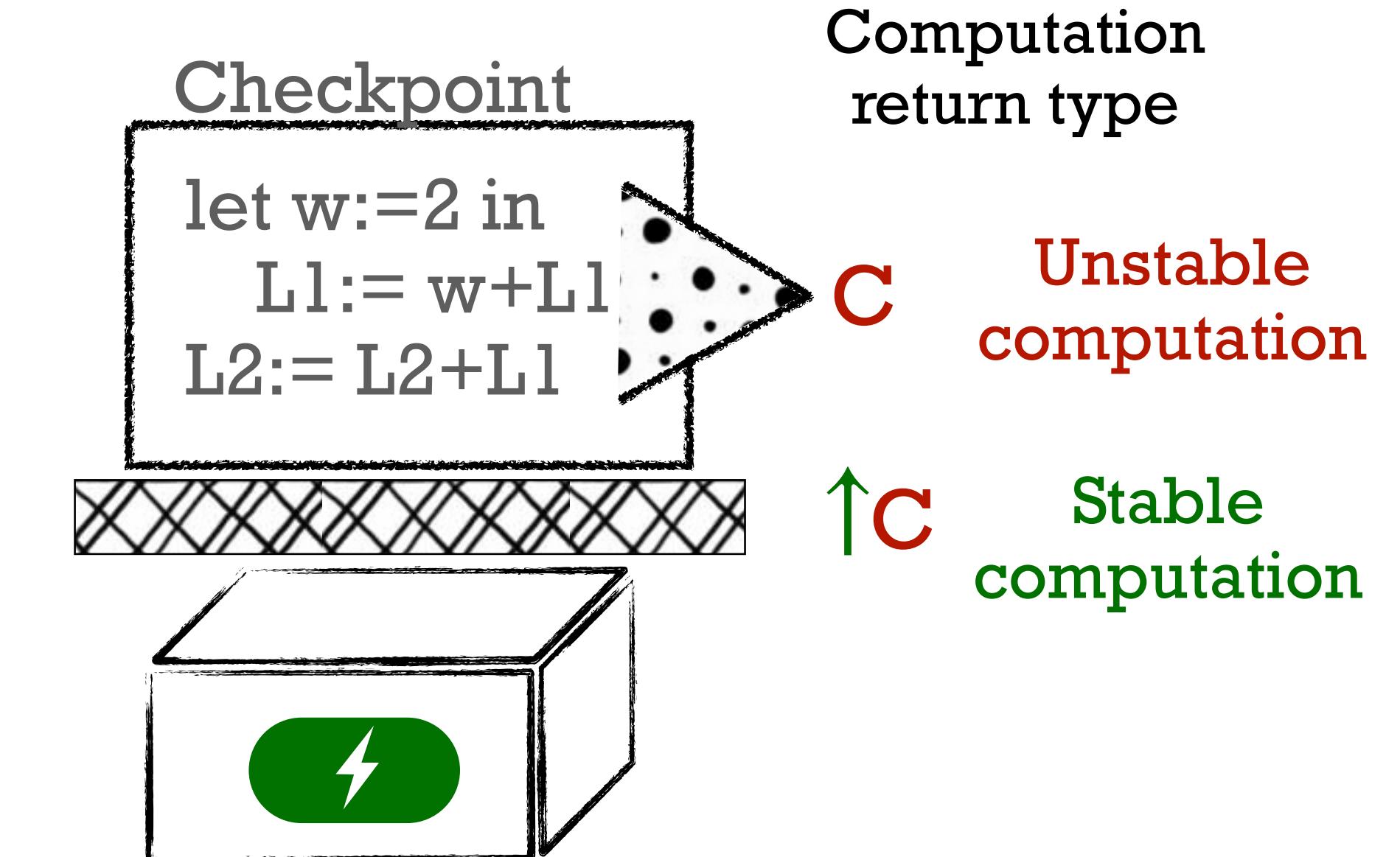


Volatile memory
Unstable values

$\downarrow \uparrow$ Int



Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
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Basic types	$T := A \mid \text{unit}$
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Independence principle: stable values cannot depend on unstable values.

We borrow \uparrow and \downarrow from adjoint logic.

Different judgments judge different things

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
Unstable types	$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$
Basic types	$T := A \mid \text{unit}$
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Different judgments judge different things

Stable judgments:

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
Unstable types	$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$
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Different judgments judge different things

Stable types

$$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$$

Unstable types

$$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$$

Basic types

$$T := A \mid \text{unit}$$

Store types

$$A := \text{int} \mid \text{bool}$$

Stable judgments:

Unstable judgments:

Different judgments judge different things

Stable types

$$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$$

Unstable types

$$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$$

Basic types

$$T := A \mid \text{unit}$$

Store types

$$A := \text{int} \mid \text{bool}$$

Stable judgments:

Nonvolatile
memory

$$\Omega \vdash \ell : \uparrow A$$

Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

: \uparrow C

Unstable judgments:

Different judgments judge different things

Stable types
Unstable types
Basic types
Store types

$$\begin{aligned}\tau_s &:= \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s \\ \tau_u &:= T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t \\ T &:= A \mid \text{unit} \\ A &:= \text{int} \mid \text{bool}\end{aligned}$$

Stable judgments:

$$\Omega \vdash \ell : \uparrow A$$

Nonvolatile memory

Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

:↑C

Unstable judgments:

$$\Omega ; \Sigma \vdash \ell : \downarrow \uparrow A$$

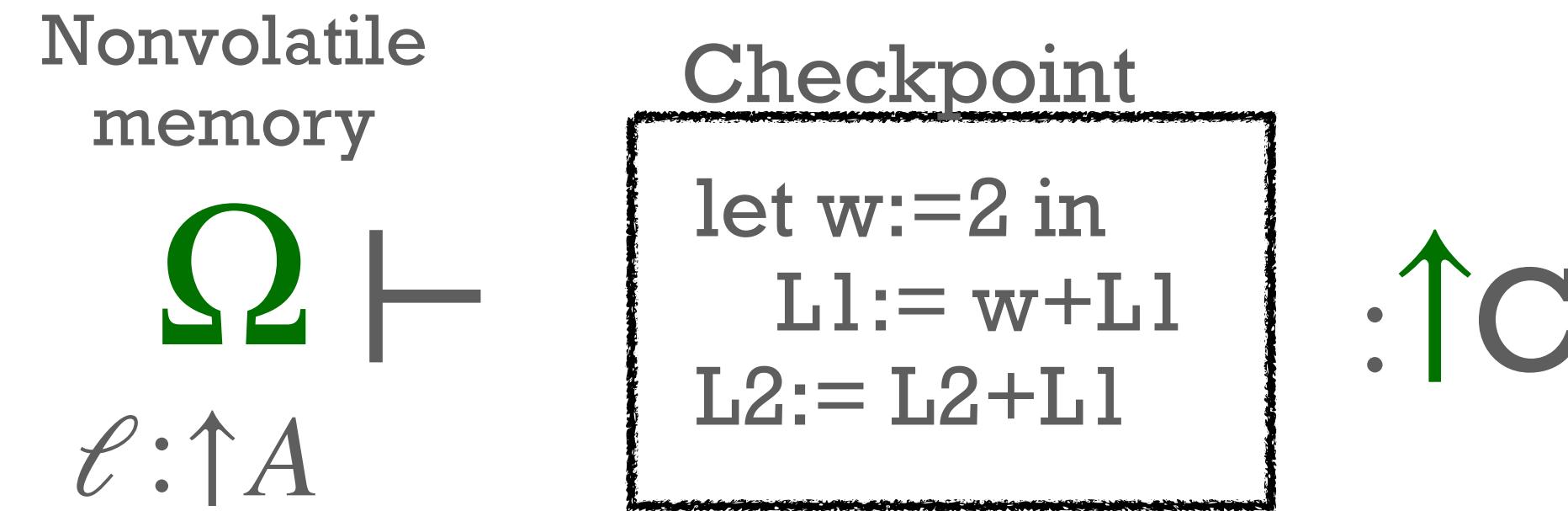
Nonvolatile memory Volatile memory

```
let w:=2 in
  L1:= w+L1 : C
  L2:= L2+L1
```

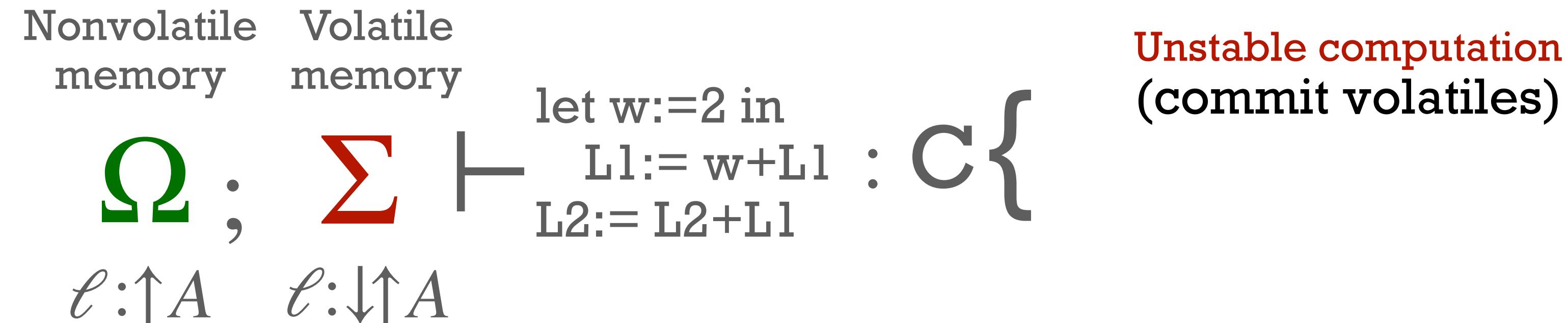
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Stable judgments:



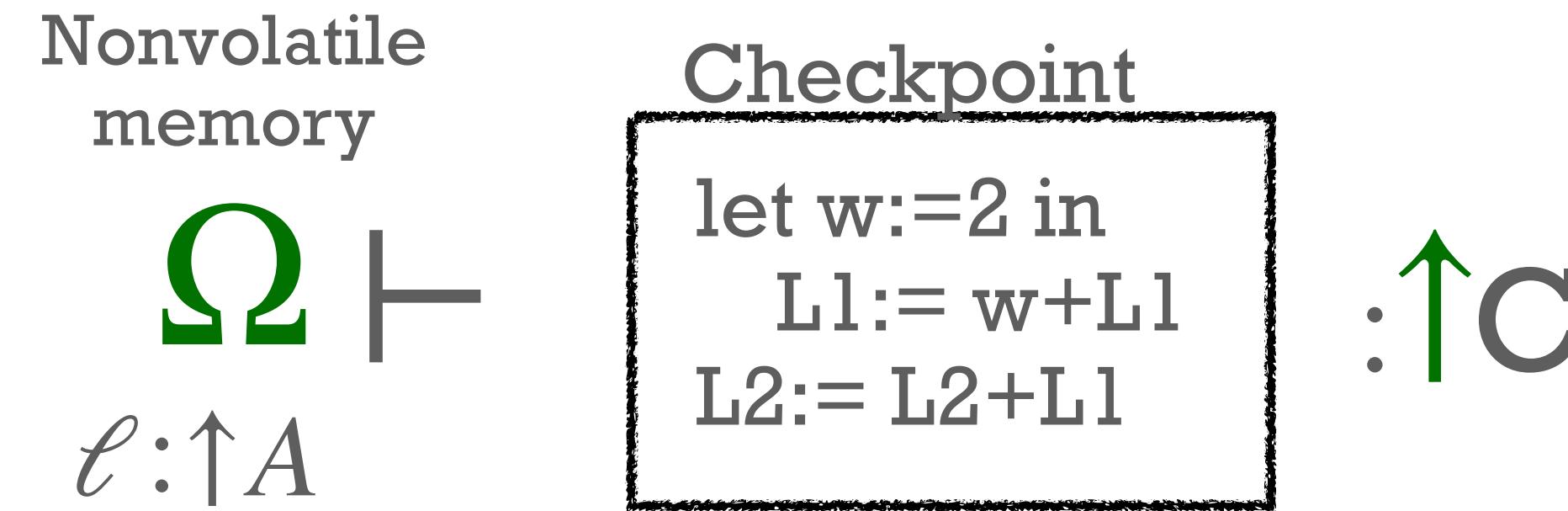
Unstable judgments:



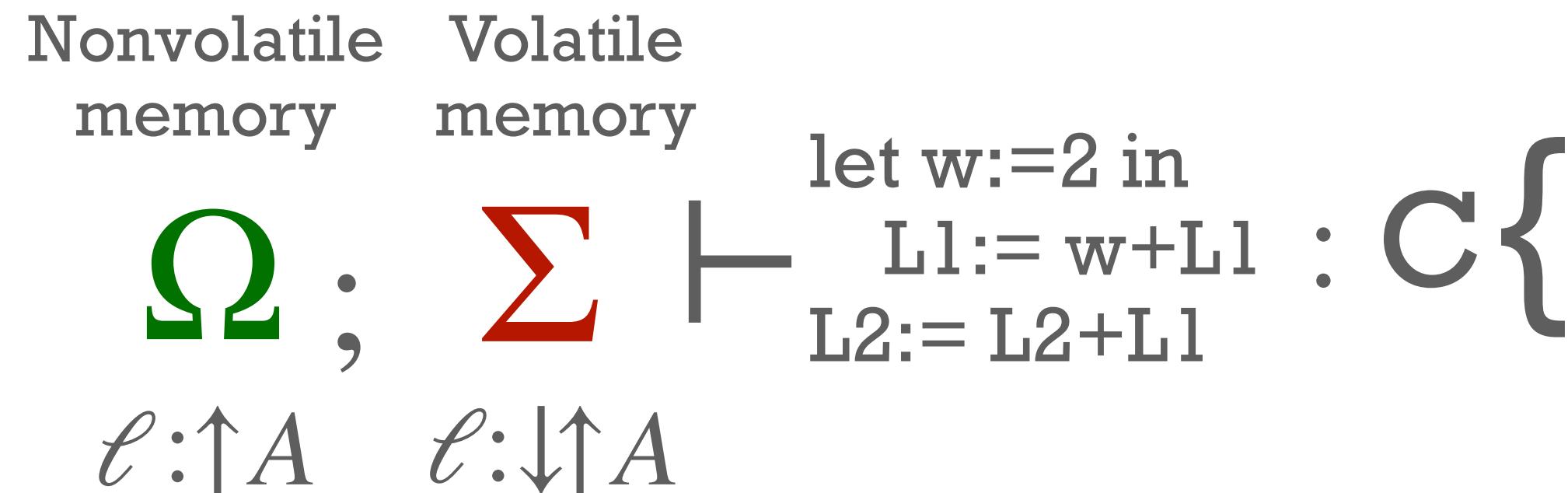
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Stable judgments:



Unstable judgments:



Unstable computation
(commit volatiles)
or

Different judgments judge different things

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
Unstable types	$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$
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Stable judgments:

$$\Omega \vdash \ell : \uparrow A$$

Nonvolatile memory

Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

:↑C

Unstable judgments:

$$\Omega ; \sum \vdash \ell : \downarrow \uparrow A \quad \ell : \uparrow A$$

Nonvolatile memory Volatile memory

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

:C{

Unstable computation
(commit volatiles)
or
Unstable computation
(delete volatiles)

Different judgments judge different things

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
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Stable judgments:

$$\Omega \vdash \ell : \uparrow A$$

Nonvolatile memory

Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

: $\uparrow C$

Unstable judgments:

$$\Omega ; \Sigma \vdash \ell : \downarrow \uparrow A$$

Nonvolatile memory Volatile memory

let w:=2 in

```
L1:= w+L1
  L2:= L2+L1
```

: $C\{$

⚡ **Unstable computation**
 (commit volatiles)
 or
Unstable computation
 (delete volatiles)

Different judgments judge different things

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
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Basic types	$T := A \mid \text{unit}$
Store types	$A := \text{int} \mid \text{bool}$

Stable judgments:

The diagram illustrates a system architecture. On the left, the text "Nonvolatile memory" is displayed above a symbol consisting of a green Greek letter Omega (Ω) and a gray plus sign (+). Below this symbol is the text " $\ell : \uparrow A$ ". In the center, the word "Checkpoint" is enclosed in a black-framed box. Inside the box, the following code snippet is shown:

```
let w:=2 in  
    L1:= w+L1  
    L2:= L2+L1
```

To the right of the central box, there is a vertical sequence of symbols: a green arrow pointing up, followed by a gray colon (:), another green arrow pointing up, and finally a gray letter C.

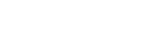
Unstable judgments:

Nonvolatile memory Volatile memory

Ω ; Σ \vdash

let w:=2 in
L1:= w+L1 : C{

 **Unstable computation**
(commit volatiles)

 **Unstable computation**
(delete volatiles)

or

L2:= L2+L1 }

$\ell : \uparrow A$ $\ell : \downarrow \uparrow A$

Different judgments judge different things

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
Unstable types	$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$
Basic types	$T := A \mid \text{unit}$
Store types	$A := \text{int} \mid \text{bool}$

Stable judgments:

The diagram illustrates the state of memory at a checkpoint. On the left, the text "Nonvolatile memory" is displayed above a symbol consisting of a green Greek letter Omega (Ω) and a gray plus sign (+). Below this symbol is the expression " $\ell : \uparrow A$ ". In the center, the word "Checkpoint" is written above a rectangular box with a black textured border. Inside the box, the following code is shown:

```
let w:=2 in  
    L1:= w+L1  
    L2:= L2+L1
```

To the right of the central box, there is a vertical sequence of symbols: a green arrow pointing up, followed by a gray colon (:), another green arrow pointing up, and finally a gray letter C.

Unstable judgments:

Nonvolatile memory Volatile memory

Ω ; Σ \vdash

let w:=2 in
L1:= w+L1 : C{

 **Unstable computation**
(commit volatiles)

 **Unstable computation**
(delete volatiles)

or

↓

Different judgments judge different things

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
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Basic types	$T := A \mid \text{unit}$
Store types	$A := \text{int} \mid \text{bool}$

Stable judgments:

The diagram illustrates the combination of Nonvolatile memory and Checkpointing. On the left, the text "Nonvolatile memory" is displayed above a symbol consisting of a green Greek letter Ω and a gray plus sign (+). Below this symbol is the expression $\ell : \uparrow A$. On the right, the word "Checkpoint" is centered within a black-framed box. Inside the box, the following code snippet is shown:

```
let w:=2 in  
    L1:= w+L1  
    L2:= L2+L1
```

To the far right of the box, there is a vertical sequence of symbols: a green arrow pointing up, followed by a gray colon (:), another green arrow pointing up, and finally a gray C-shaped symbol.

Unstable judgments:

Nonvolatile memory Volatile memory

Ω ; Σ \vdash

let w:=2 in
L1:= w+L1 : C{

Unstable computation (commit volatiles)

Unstable computation (delete volatiles)

or

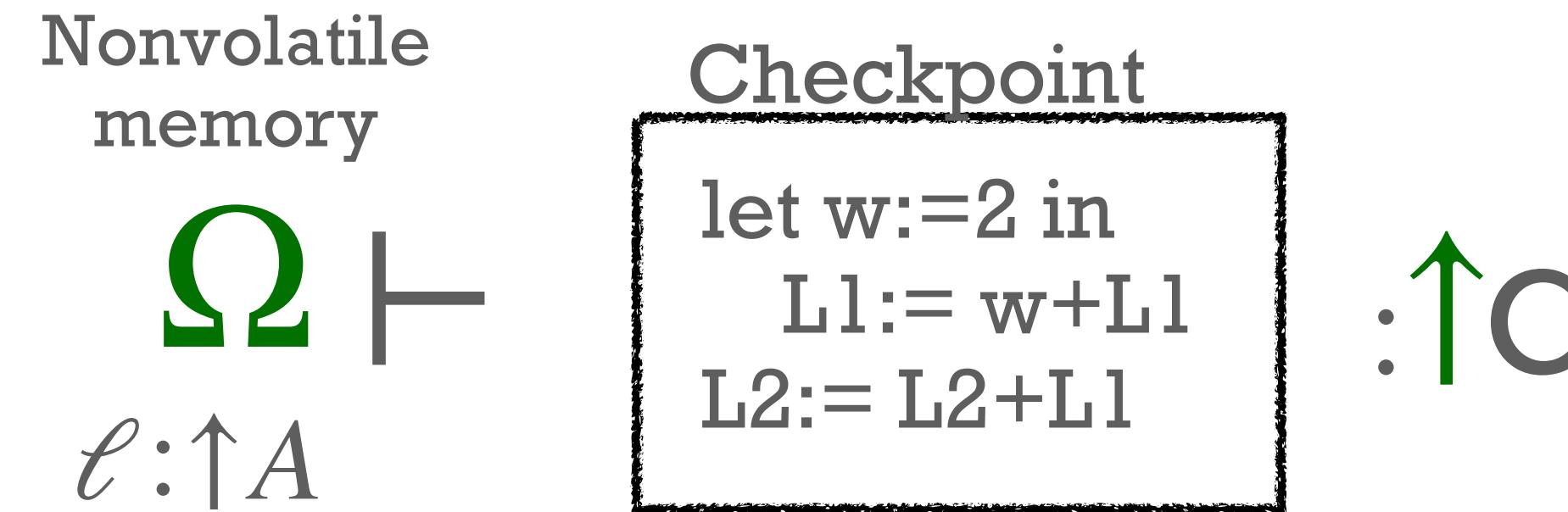
$\ell : \uparrow A$ $\ell : \downarrow \uparrow A$

$\downarrow \uparrow$

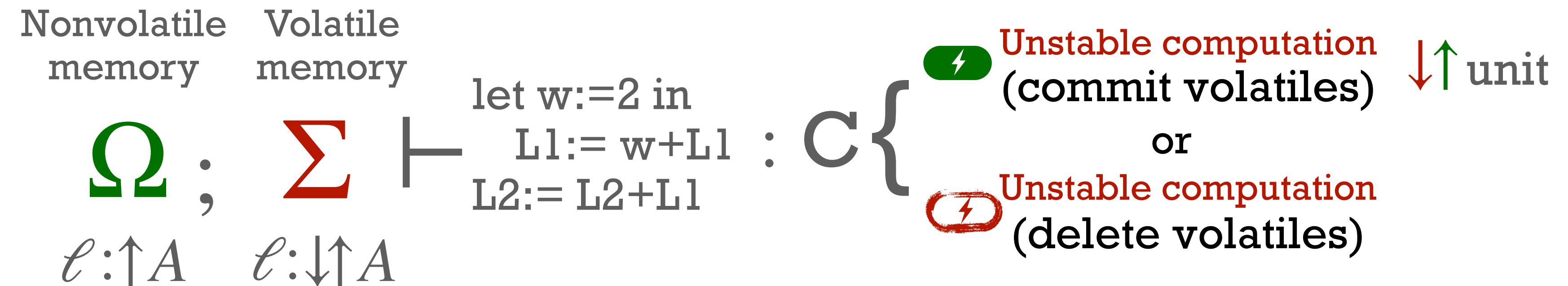
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Stable judgments:



Unstable judgments:



Different judgments judge different things

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
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Stable judgments:

$$\Omega \vdash \ell : \uparrow A$$

Nonvolatile memory

Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

: $\uparrow C$

Unstable judgments:

$$\Omega ; \Sigma \vdash \ell : \uparrow A \quad \ell : \downarrow \uparrow A \quad \vdash \text{let } w:=2 \text{ in} \\ \text{ L1:= } w+\text{L1} \quad \text{L2:= } \text{L2+L1} : C \{$$

Nonvolatile memory Volatile memory

↓

↑

Unstable computation
(commit volatiles)

↓ unit

or

Unstable computation
(delete volatiles)

↓

Different judgments judge different things

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
Unstable types	$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$
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Store types	$A := \text{int} \mid \text{bool}$

Stable judgments:

$$\Omega \vdash \ell : \uparrow A$$

Nonvolatile memory

Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

: $\uparrow C$

Unstable judgments:

$$\Omega ; \sum \vdash \ell : \downarrow \uparrow A \quad \ell : \downarrow \uparrow A \quad \text{Volatile memory}$$

Nonvolatile memory

let w:=2 in
 L1:= w+L1
 L2:= L2+L1

: $C \{$

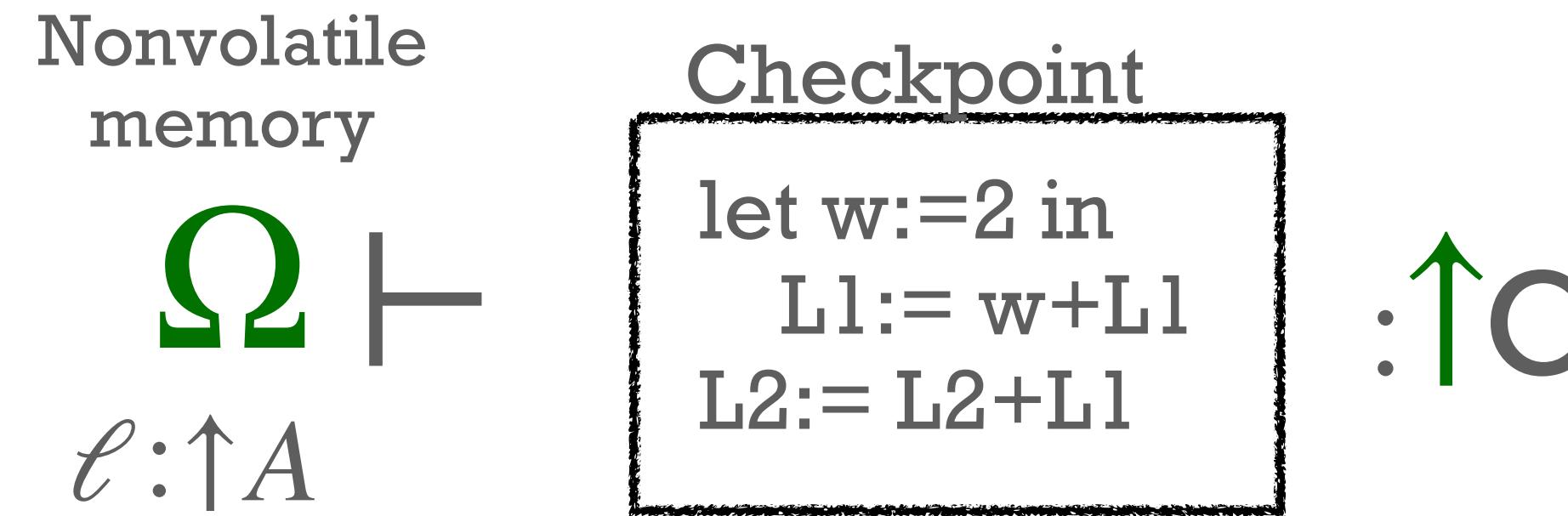
⚡
Unstable computation
(commit volatiles)
↓↑ unit

⚡
Unstable computation
(delete volatiles)
↓(nat ↘↑C)

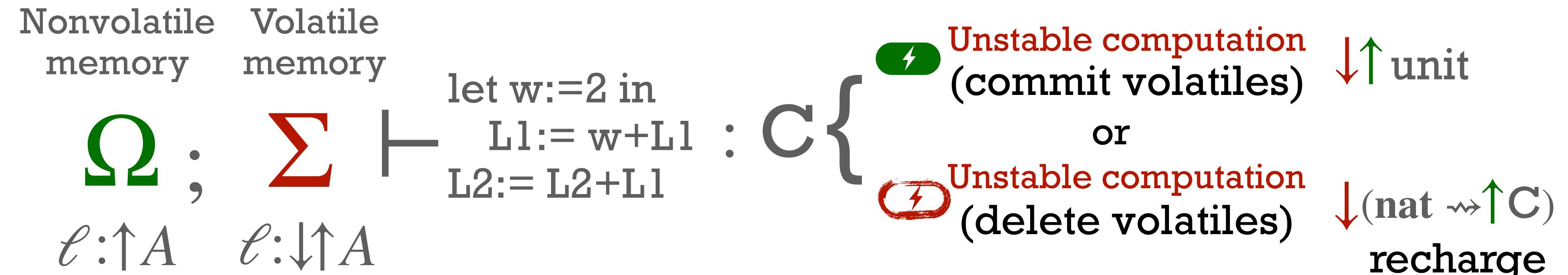
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Stable judgments:



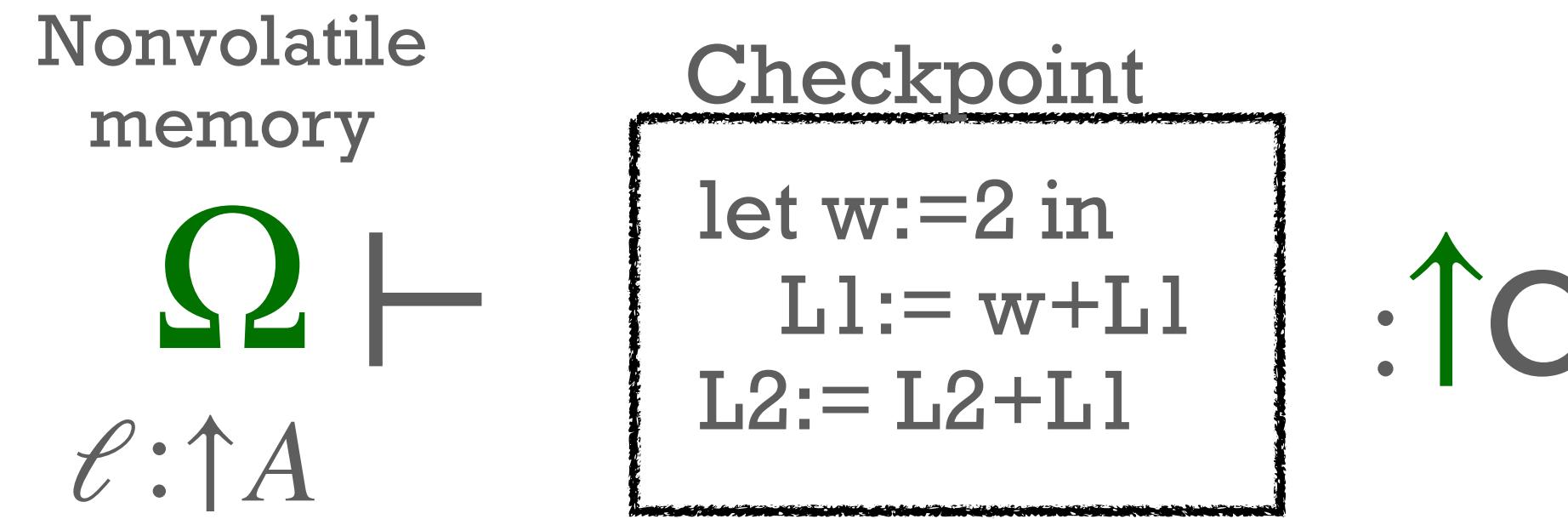
Unstable judgments:



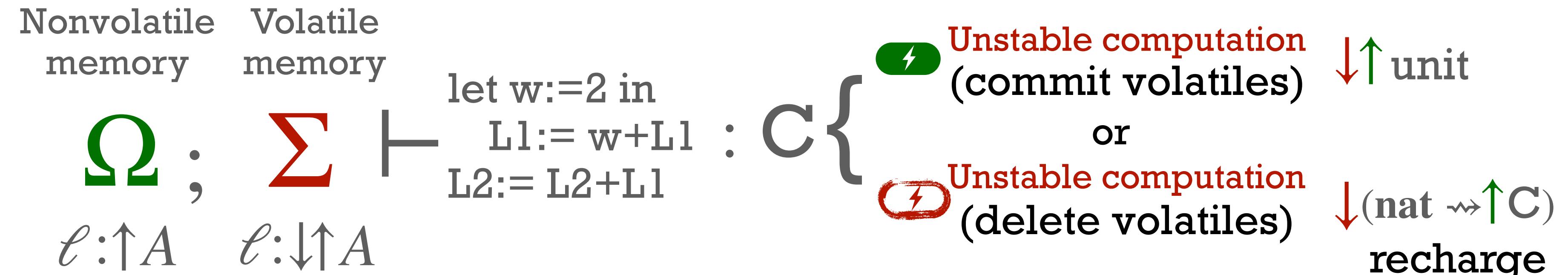
Different judgments judge different things

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Stable judgments:



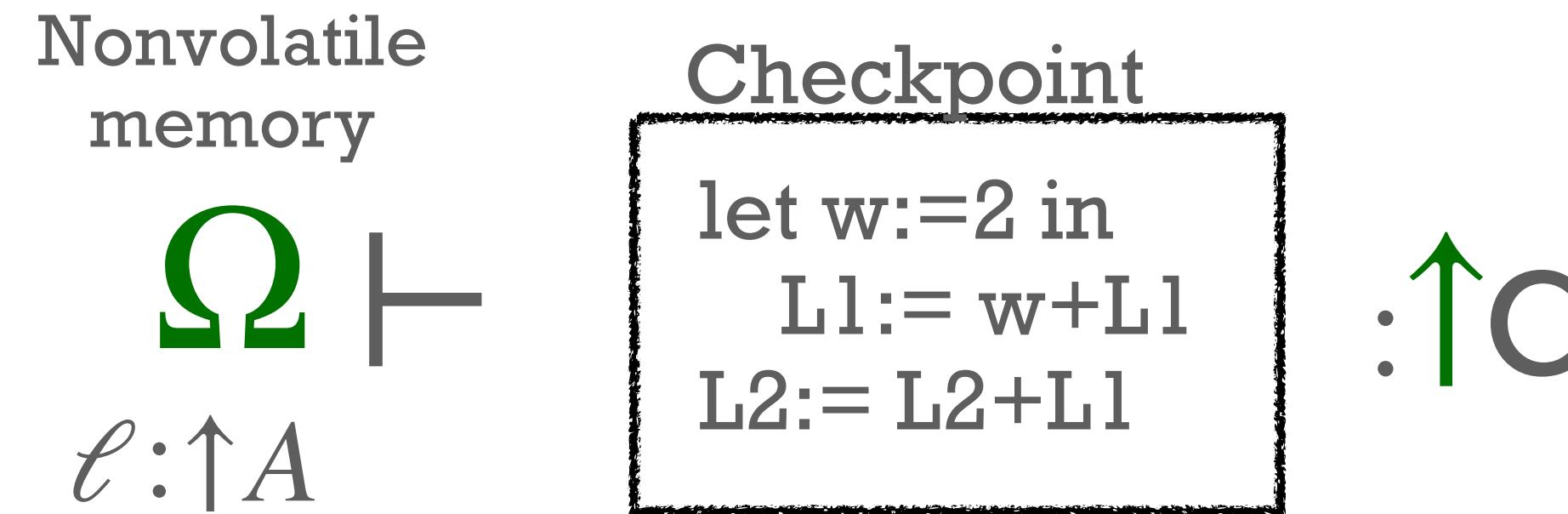
Unstable judgments:



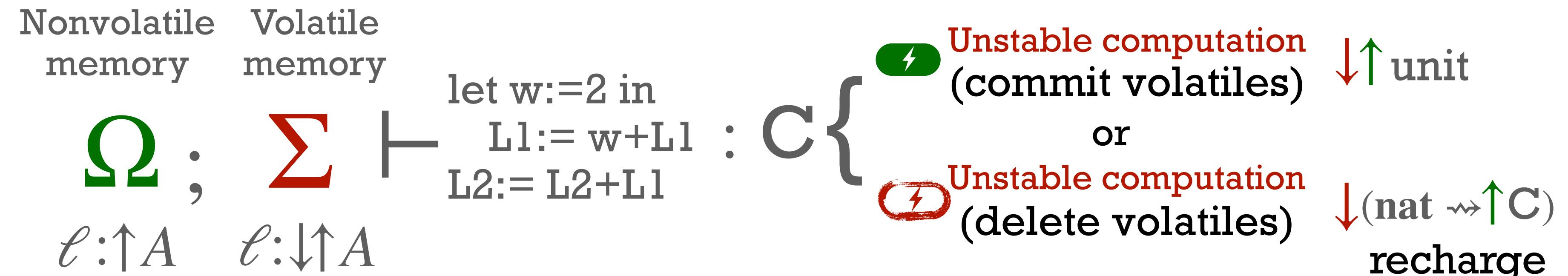
Different judgments judge different things

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Stable judgments:



Unstable judgments:



Different judgments judge different things

Stable types	$\tau_s := \uparrow \tau_u \mid \text{nat} \rightsquigarrow \tau_s$
Unstable types	$\tau_u := T \mid \downarrow \tau_s \mid \tau_u \vee \tau_u \mid v_t$
Basic types	$T := A \mid \text{unit}$
Store types	$A := \text{int} \mid \text{bool}$

Stable judgments:

Nonvolatile memory

Checkpoi

```
let w:=2 in  
  L1:= w+L  
  L2:= L2+L1
```

A green upward-pointing arrow icon.

Unstable judgments:

Nonvolatile memory

Volatile memor

Ω

$$\ell : \uparrow A \quad \ell : \downarrow \uparrow A$$

```
let w:=2 in  
  L1:= w+  
  L2:= L2+L
```

$$C = \downarrow \uparrow \text{unit} \vee \downarrow (\text{nat} \rightsquigarrow \uparrow C)$$

Unstable computation (commit volatiles)

2

Unstable computation (delete volatiles)

↓ ↑ unit

\downarrow (nat \rightsquigarrow \uparrow C)
recharge

Background: adjoint logic

Background: adjoint logic

- Combine modes of truth

(Benton 1994),

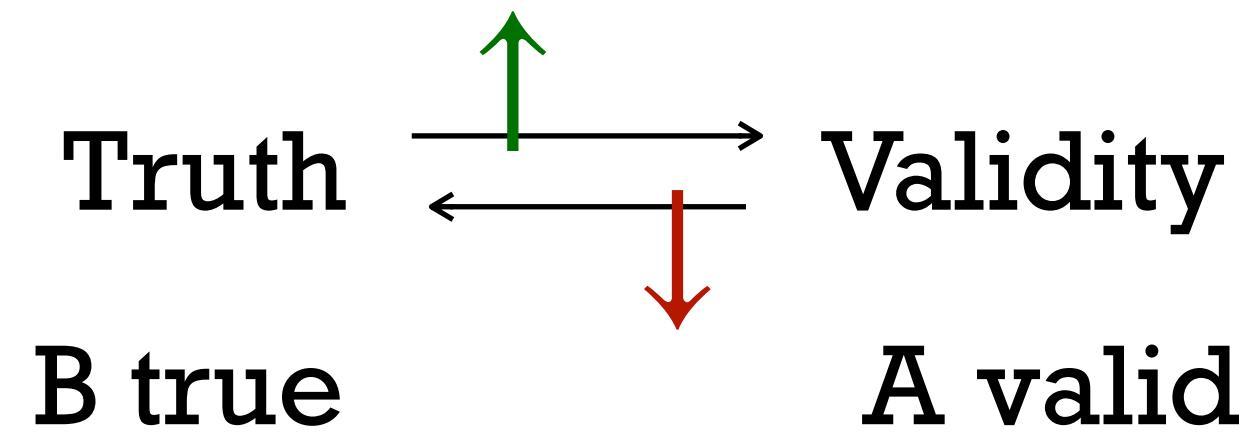
(Pfenning and Davies 1999),

(Reed 2009)

(Pruiksma et al 2020)

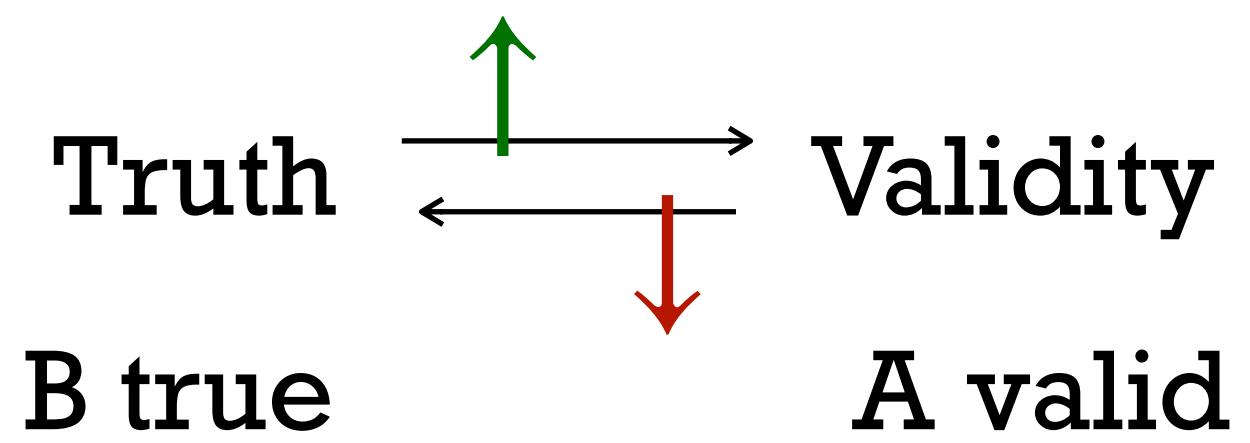
Background: adjoint logic

- Combine modes of truth
*(Benton 1994),
(Pfenning and Davies 1999),
(Reed 2009)
(Pruiksma et al 2020)*



Background: adjoint logic

- Combine modes of truth
*(Benton 1994),
(Pfenning and Davies 1999),
(Reed 2009)
(Pruiksma et al 2020)*

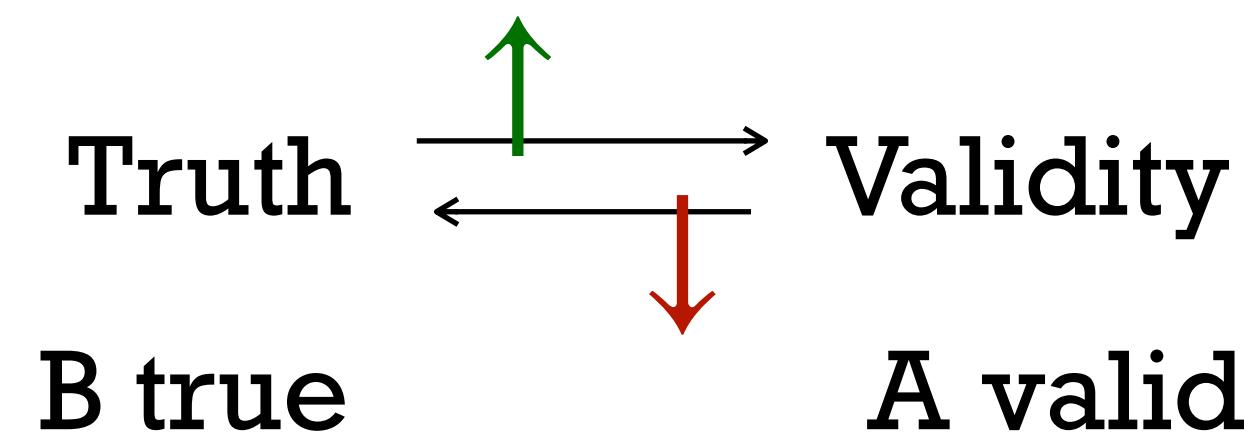


Independence principle:

Validity cannot depend on truth

Background: adjoint logic

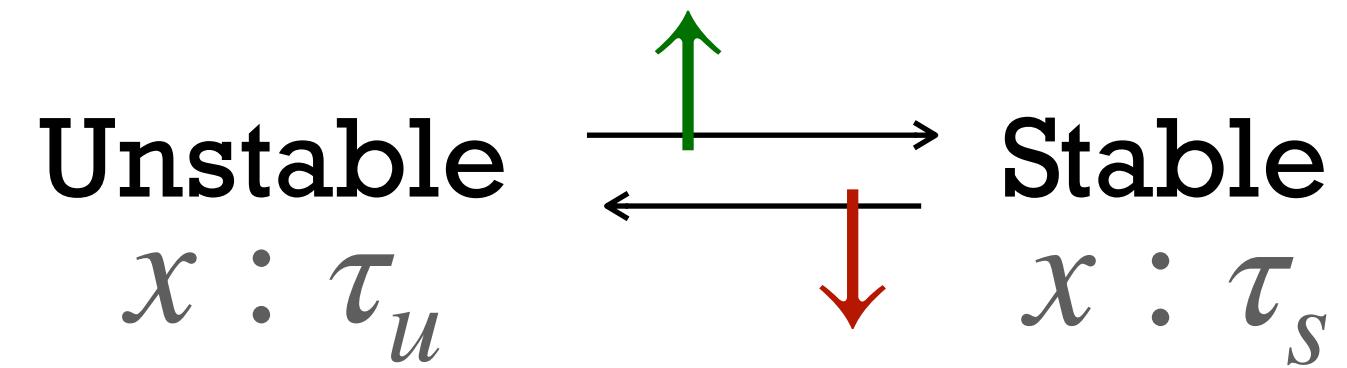
- Combine modes of truth
*(Benton 1994),
(Pfenning and Davies 1999),
(Reed 2009)
(Pruiksma et al 2020)*



Independence principle:

Validity cannot depend on truth

- Combine stable and unstable values



Stable values cannot depend on unstable values

Different judgments judge different things

Different judgments judge different things

Stable judgments:

$$\Omega \vdash \tau_S$$
$$x : \tau_S$$

Different judgments judge different things

Stable judgments:

$$\Omega \vdash \tau_s$$
$$x:\tau_s$$

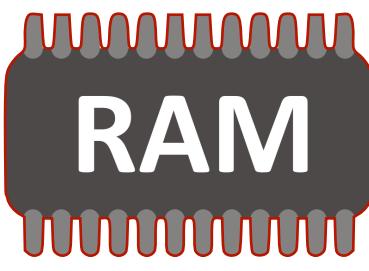
Unstable judgments:

$$\Omega ; \Sigma \vdash \tau_u$$
$$x:\tau_s \quad x:\tau_u$$

Type system based on adjoint logic rules



Nonvolatile memory
Stable values $\uparrow \text{Int}$

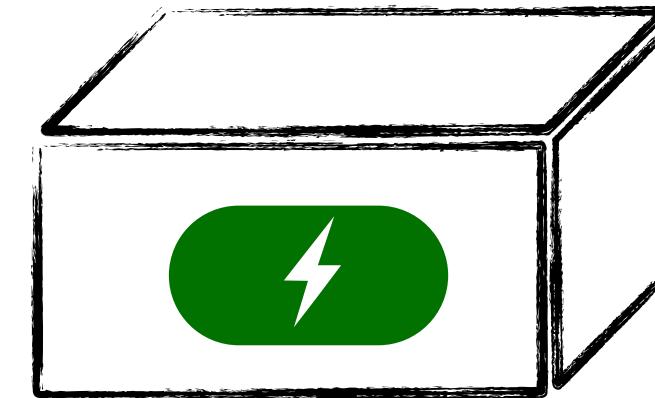


Volatile memory
Unstable values $\downarrow \uparrow \text{Int}$

ℓ_1	ℓ_2
0	0

pc → Checkpoint

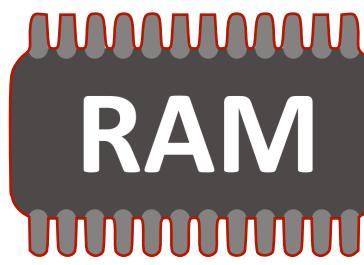
```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```



Type system based on adjoint logic rules



Nonvolatile memory
Stable values $\uparrow \text{Int}$

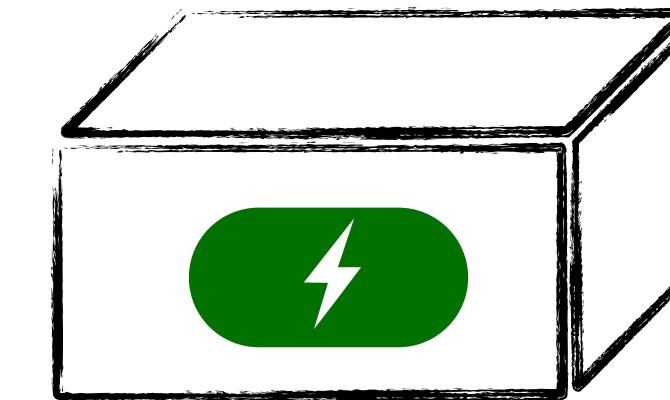


Volatile memory
Unstable values $\downarrow \uparrow \text{Int}$

ℓ_1	ℓ_2
0	0

pc → Checkpoint

```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```



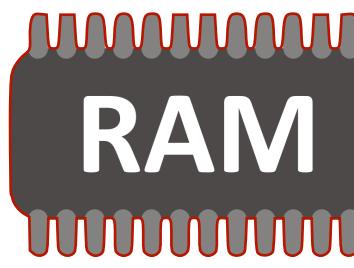
Check point

$\Omega \vdash$ let w:=2 in
 L1:= w+L1
 L2:= L2+L1 : $\uparrow C_{\text{Unit}}$

Type system based on adjoint logic rules



Nonvolatile memory
Stable values $\uparrow \text{Int}$



Volatile memory
Unstable values $\downarrow \uparrow \text{Int}$

Typing rule:

$$\Omega \vdash \boxed{\text{Check point}} : \uparrow C_{\text{Unit}}$$

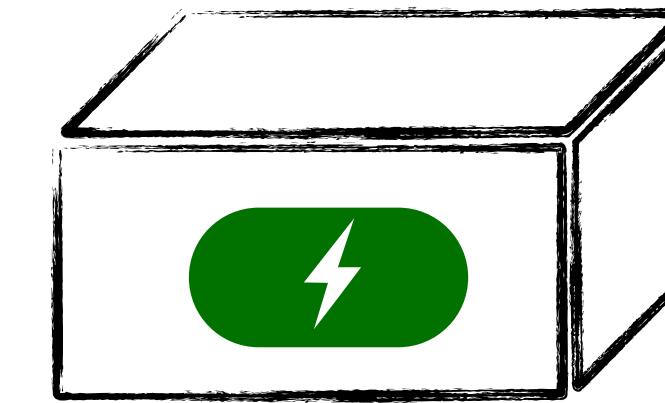
Check point

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

ℓ_1	ℓ_2
0	0

pc → Checkpoint

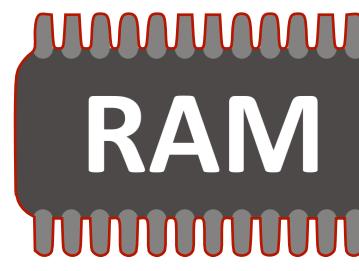
```
let w:=2 in
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  L2:= L2+L1
```



Type system based on adjoint logic rules

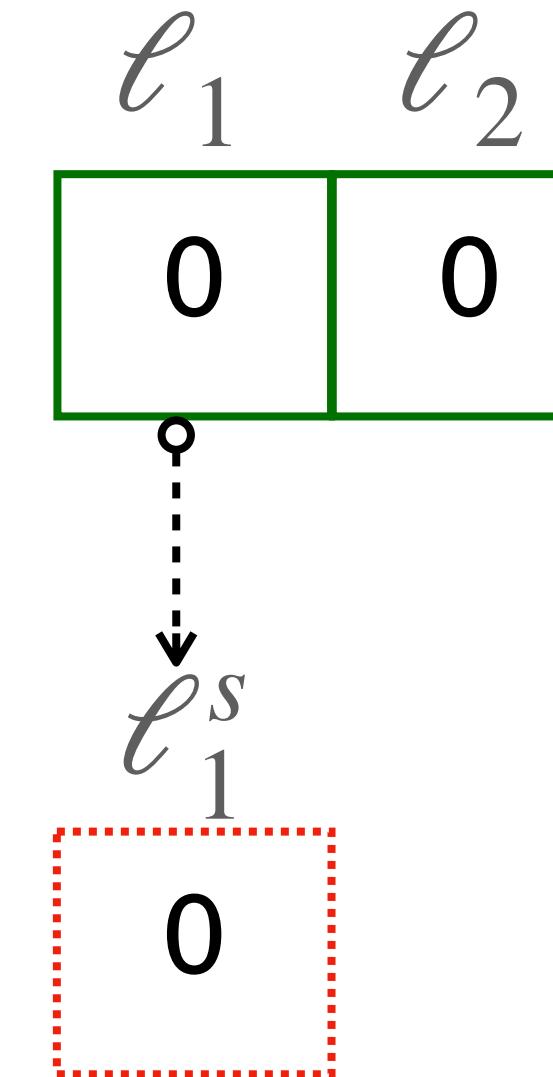


Nonvolatile memory
Stable values $\uparrow \text{Int}$



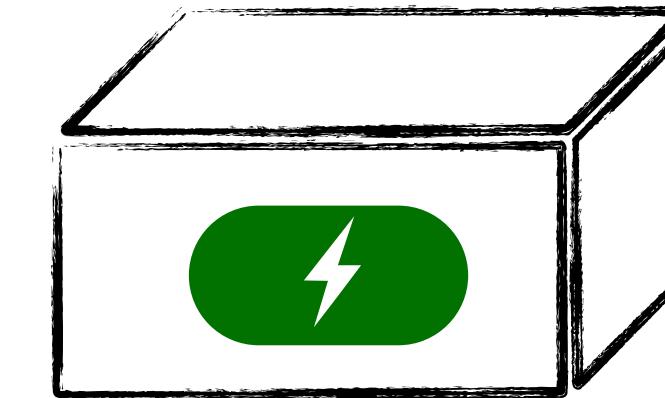
Volatile memory
Unstable values $\downarrow \uparrow \text{Int}$

Typing rule:



pc → Checkpoint

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```

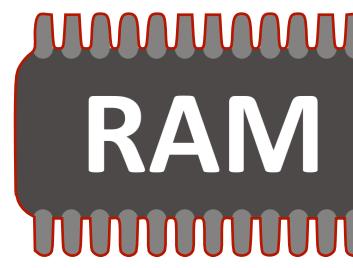


$\Omega \vdash$ Check point
let w:=2 in
 L1:= w+L1
 L2:= L2+L1 : $\uparrow C_{\text{Unit}}$

Type system based on adjoint logic rules



Nonvolatile memory
Stable values $\uparrow \text{Int}$



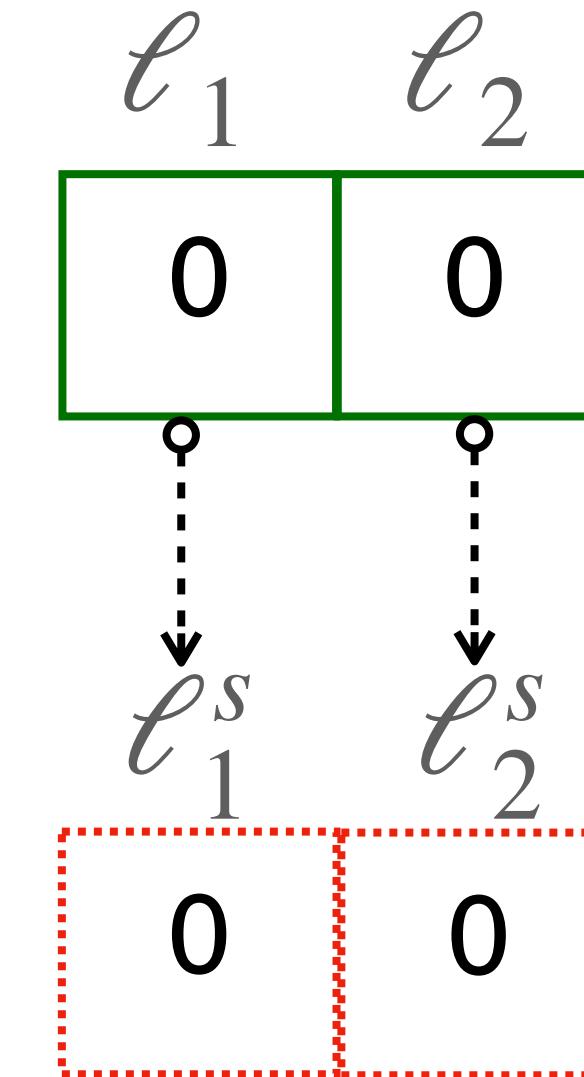
Volatile memory
Unstable values $\downarrow \uparrow \text{Int}$

Typing rule:

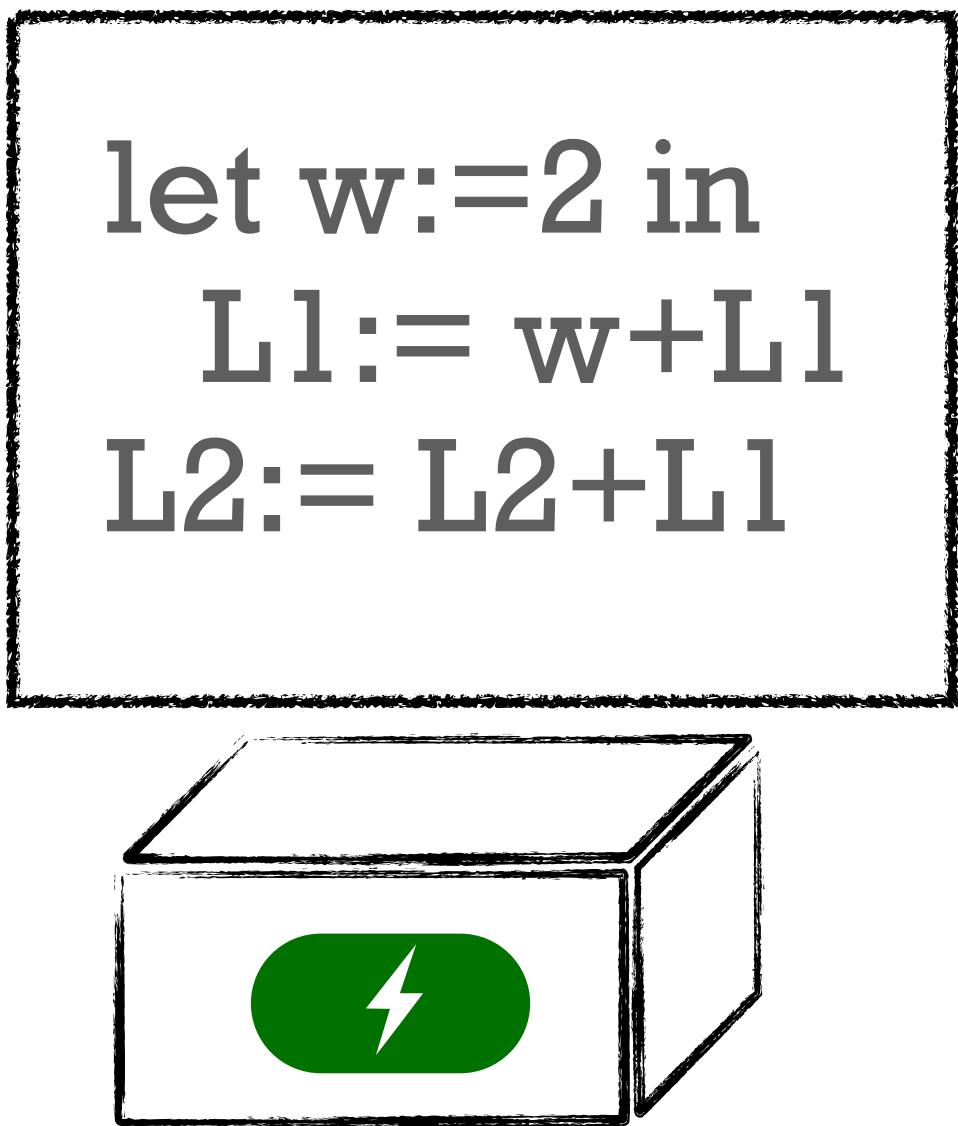
$$\Omega \vdash \boxed{\text{Check point}} : \uparrow C_{\text{Unit}}$$

Check point

```
let w:=2 in
  L1:= w+L1
  L2:= L2+L1
```



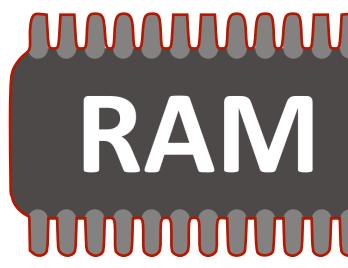
pc → Checkpoint



Type system based on adjoint logic rules

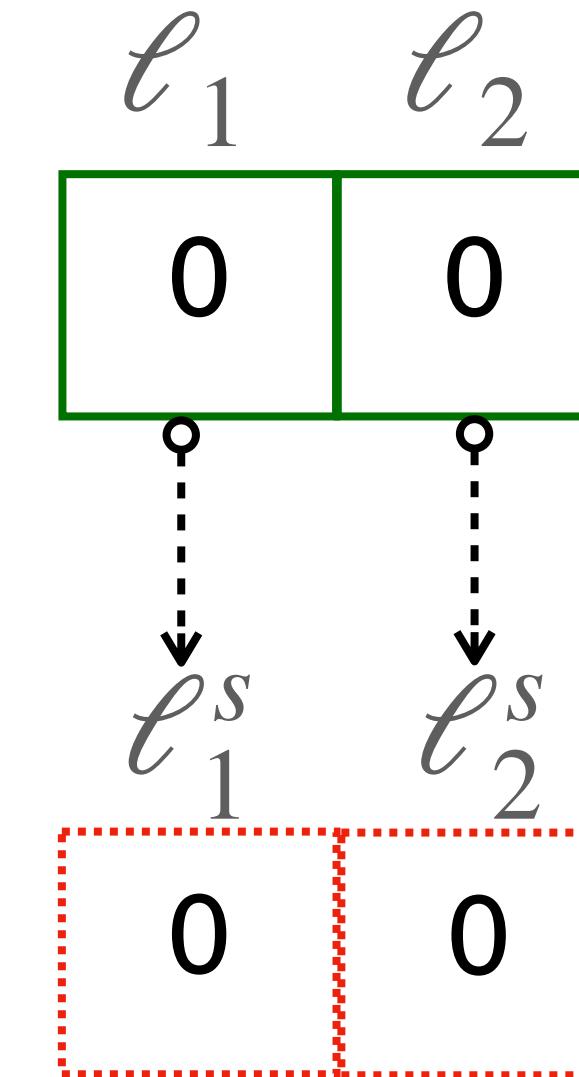


Nonvolatile memory
Stable values $\uparrow \text{Int}$



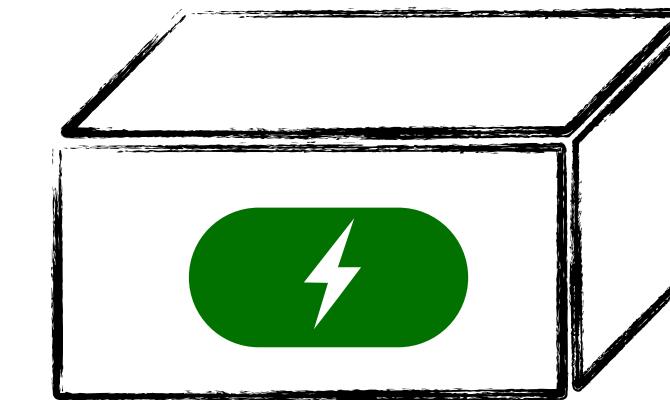
Volatile memory
Unstable values $\downarrow \uparrow \text{Int}$

Typing rule:



pc → Checkpoint

```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```



Ckpt

$\Omega \vdash$ Check point

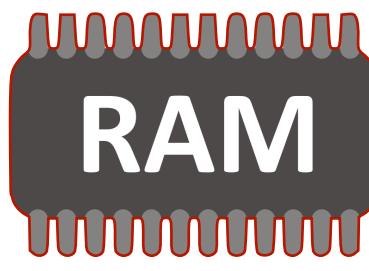
```
let w:=2 in  
  L1:= w+L1  
  L2:= L2+L1
```

 : $\uparrow C_{\text{Unit}}$

Type system based on adjoint logic rules



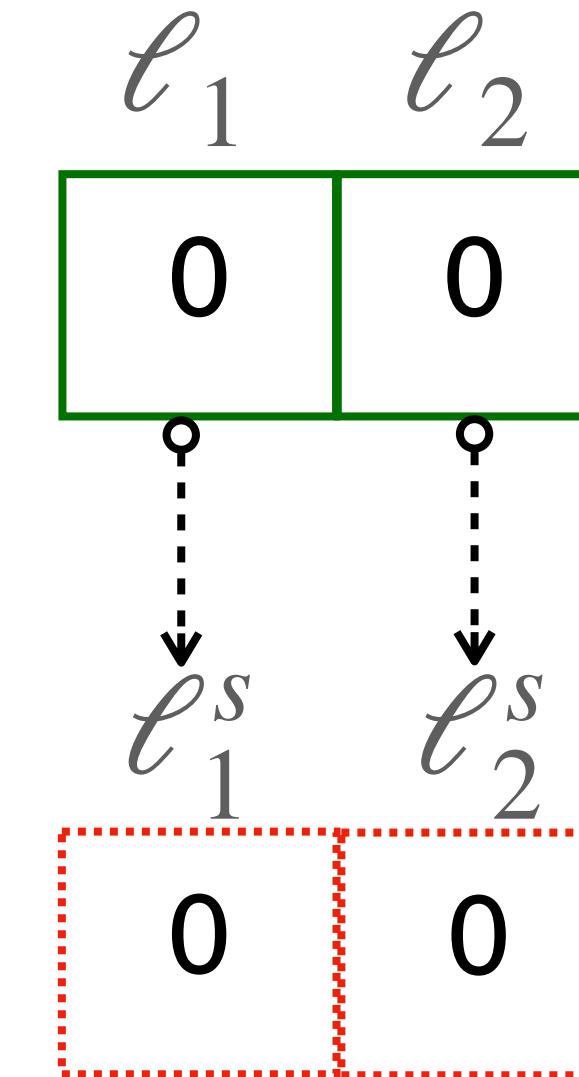
Nonvolatile memory
Stable values $\uparrow \text{Int}$



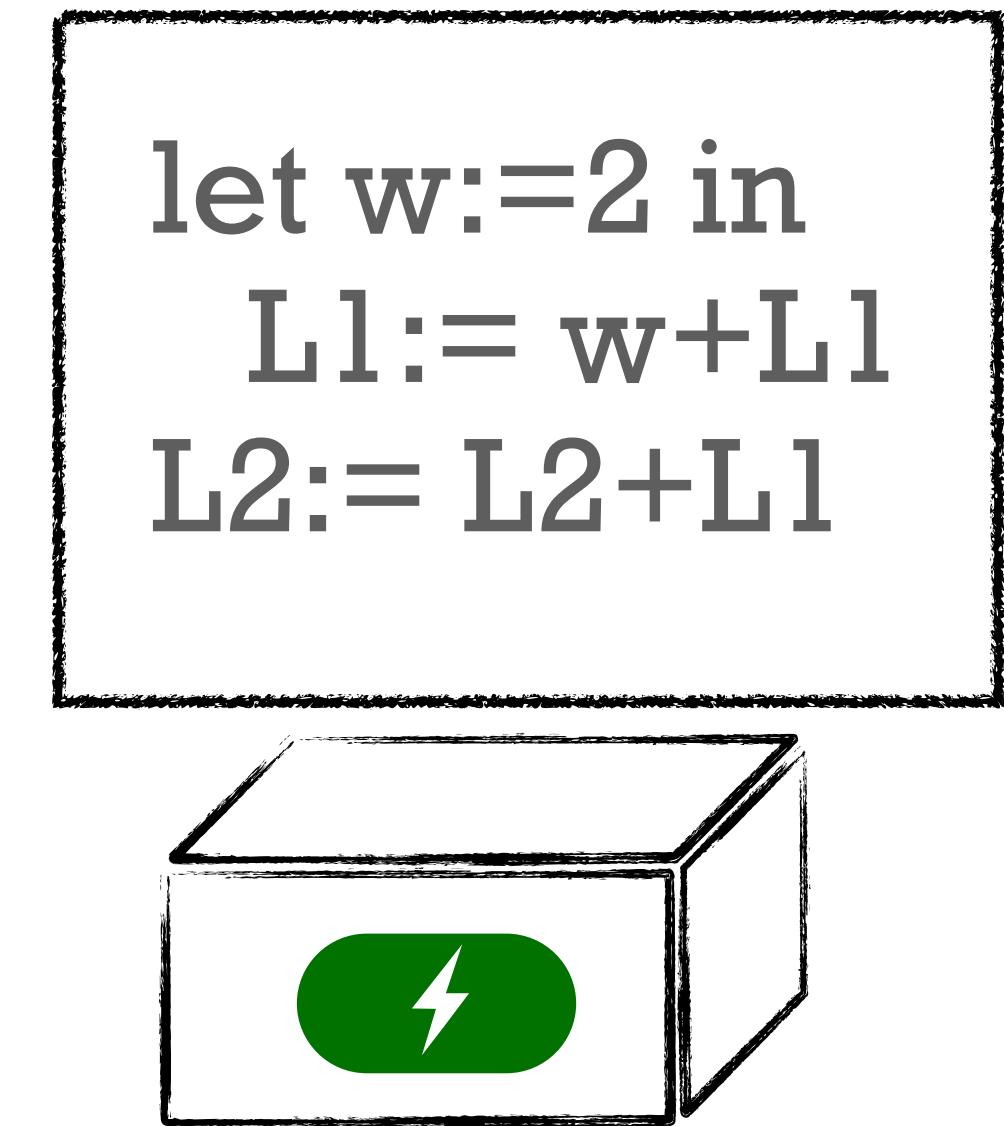
Volatile memory
Unstable values $\downarrow \uparrow \text{Int}$

Typing rule:

$$\bullet \quad \Omega ; \Sigma \vdash \frac{\text{let } w := 2 \text{ in} \\ L1 := w + L1 \\ L2 := L2 + L1}{\text{CUnit}} : \text{CUnit}$$



pc → Checkpoint

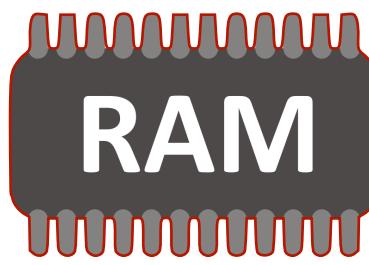


$$\Omega \vdash \frac{\text{Check point}}{\text{let } w := 2 \text{ in} \\ L1 := w + L1 \\ L2 := L2 + L1} : \uparrow \text{CUnit}$$

Type system based on adjoint logic rules



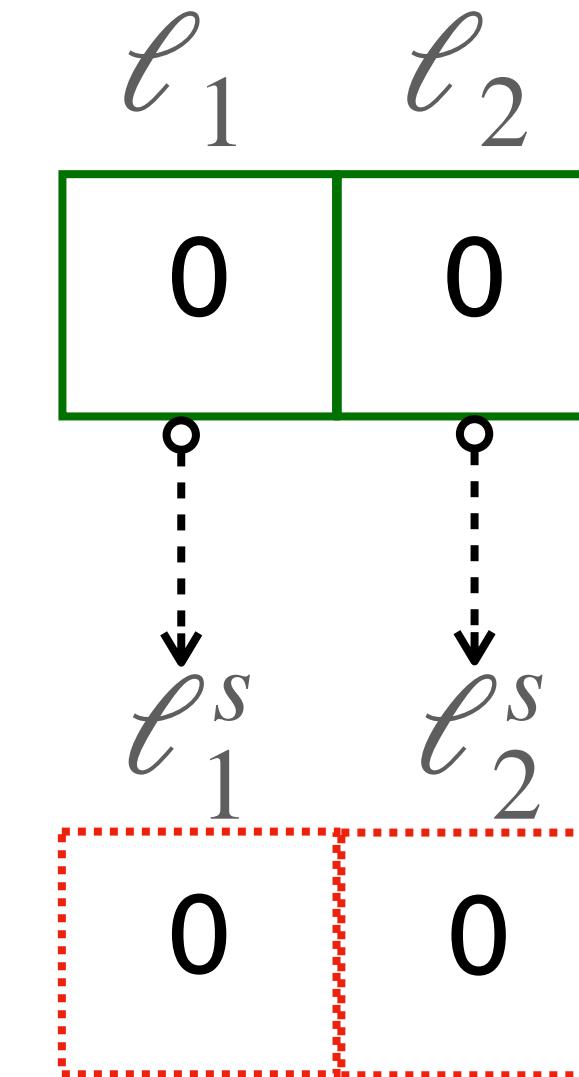
Nonvolatile memory
Stable values $\uparrow \text{Int}$



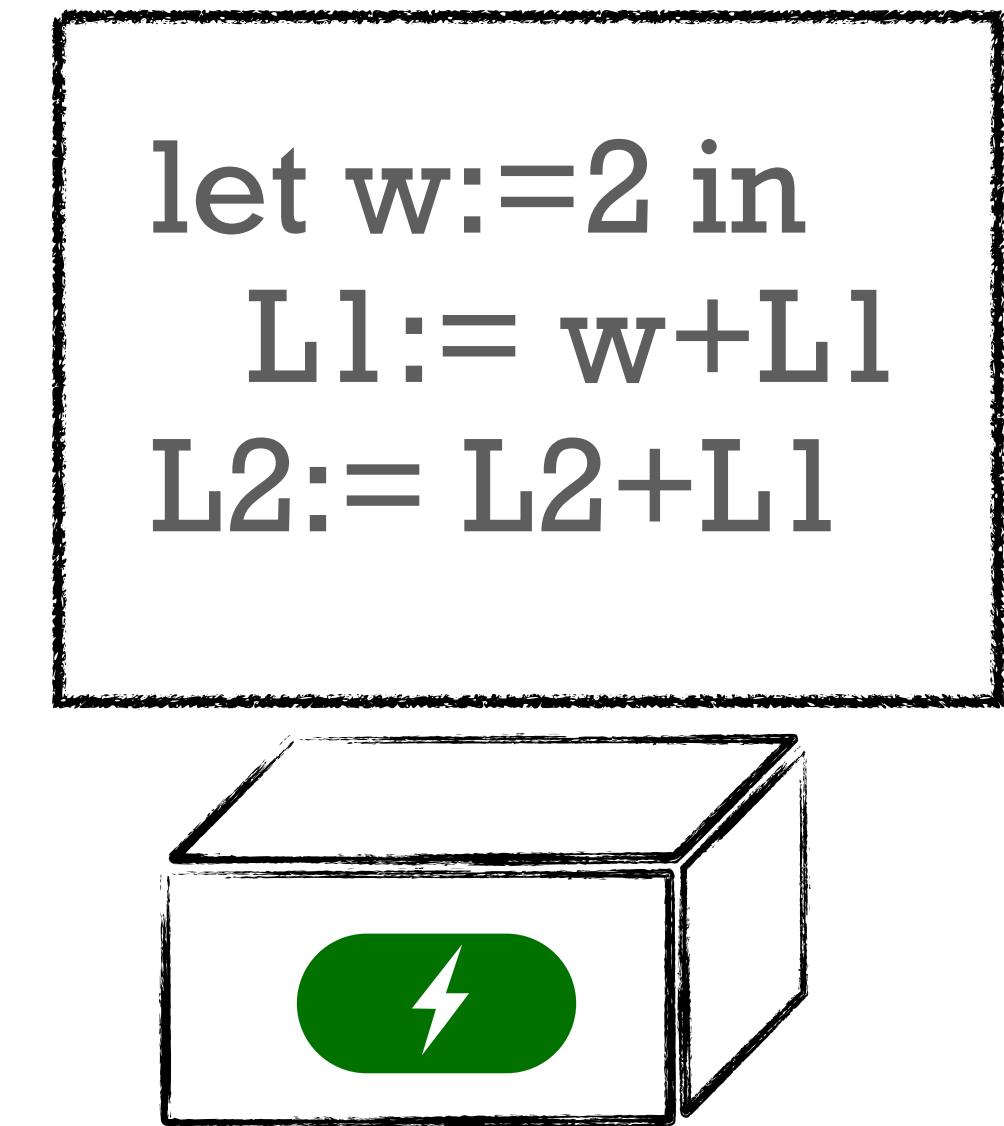
Volatile memory
Unstable values $\downarrow \uparrow \text{Int}$

Typing rule:

$$\bullet \quad \Omega ; \Sigma \vdash \frac{\text{let } w := 2 \text{ in} \\ L1 := w + L1 \\ L2 := L2 + L1}{\text{CUnit}} : \text{CUnit}$$



pc \rightarrow Checkpoint



Adjoint logic:

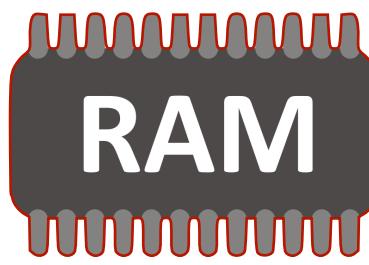
$$\Omega \vdash \frac{\text{Check point}}{\text{let } w := 2 \text{ in} \\ L1 := w + L1 \\ L2 := L2 + L1} : \uparrow \text{CUnit}$$

Type system based on adjoint logic rules



Nonvolatile memory

Stable values $\uparrow \text{Int}$

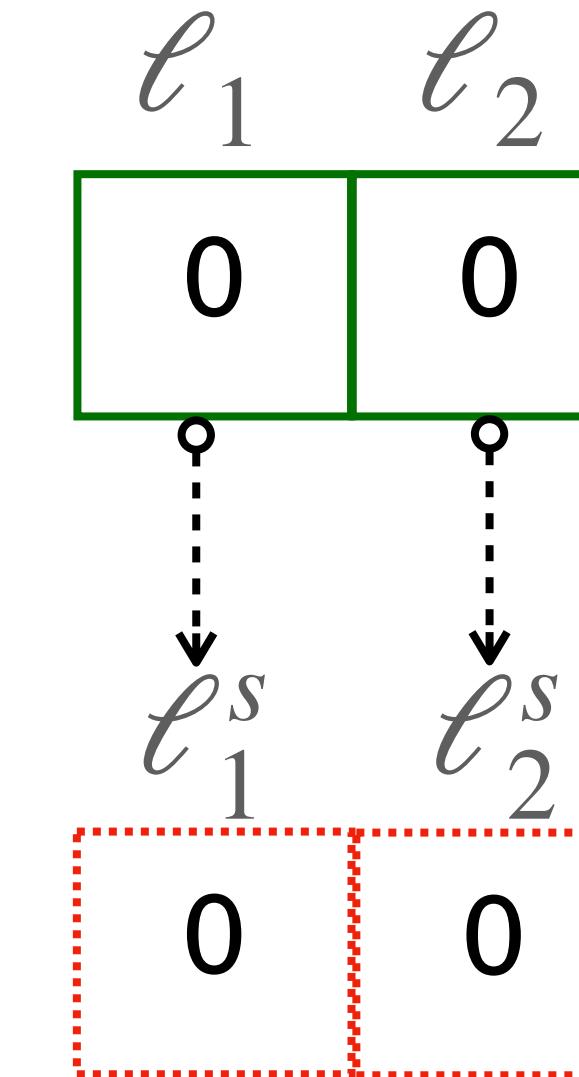


Volatile memory

Unstable values $\downarrow \uparrow \text{Int}$

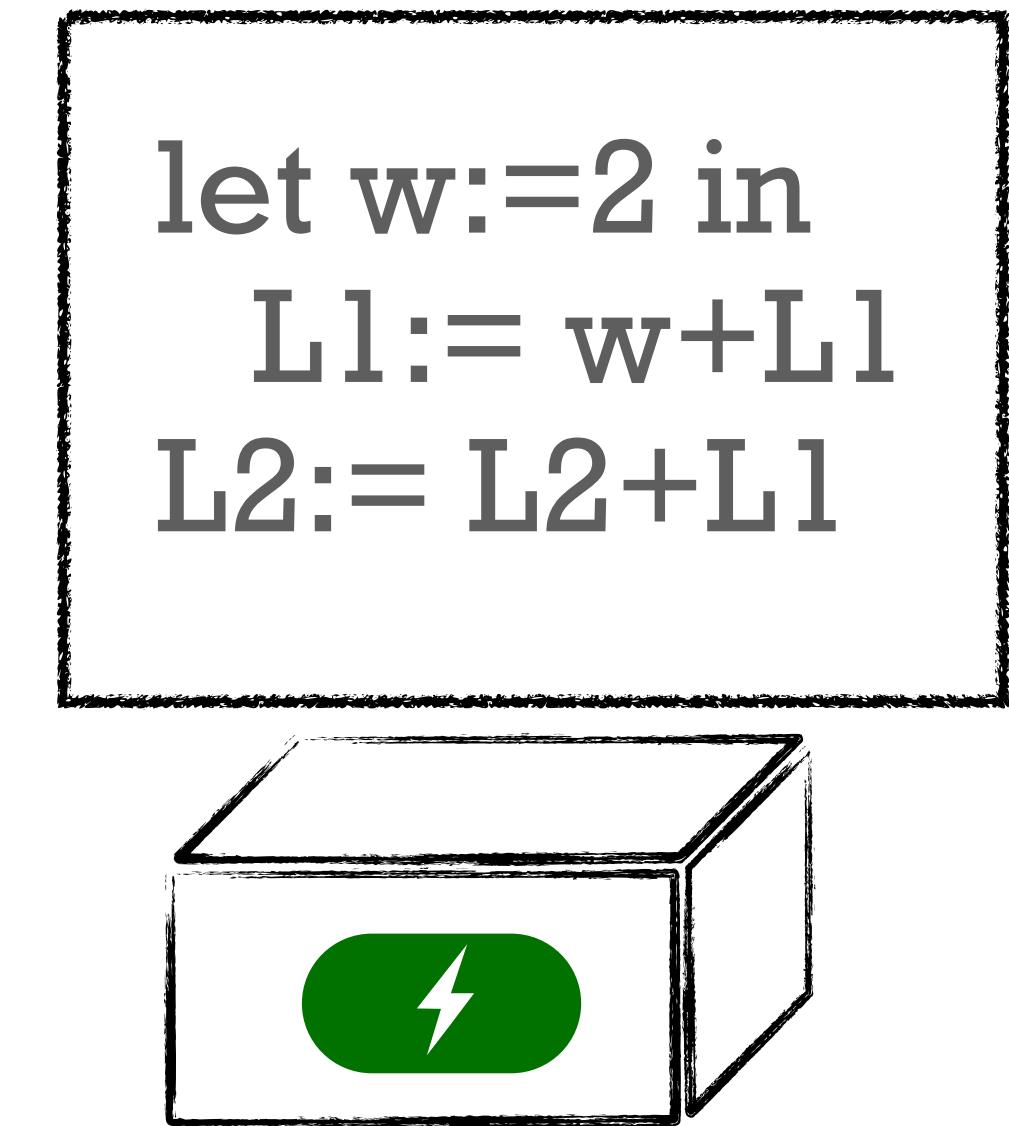
Typing rule:

$$\bullet \quad \Omega ; \Sigma \vdash \frac{\text{let } w := 2 \text{ in} \\ L1 := w + L1 \\ L2 := L2 + L1}{\text{CUnit}} : \text{CUnit}$$



$$\Omega \vdash \frac{\text{Check point} \\ \boxed{\text{let } w := 2 \text{ in} \\ L1 := w + L1 \\ L2 := L2 + L1}}{\text{CUnit}} : \uparrow \text{CUnit}$$

pc → Checkpoint



Adjoint logic:

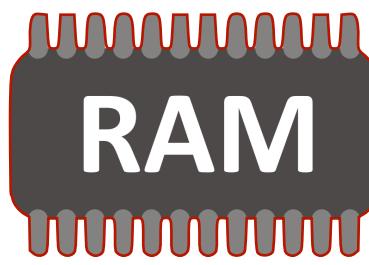
$$\frac{\Omega ; \bullet \vdash \tau}{\Omega \vdash \uparrow \tau} \uparrow R$$

Type system based on adjoint logic rules



Nonvolatile memory

Stable values $\uparrow \text{Int}$

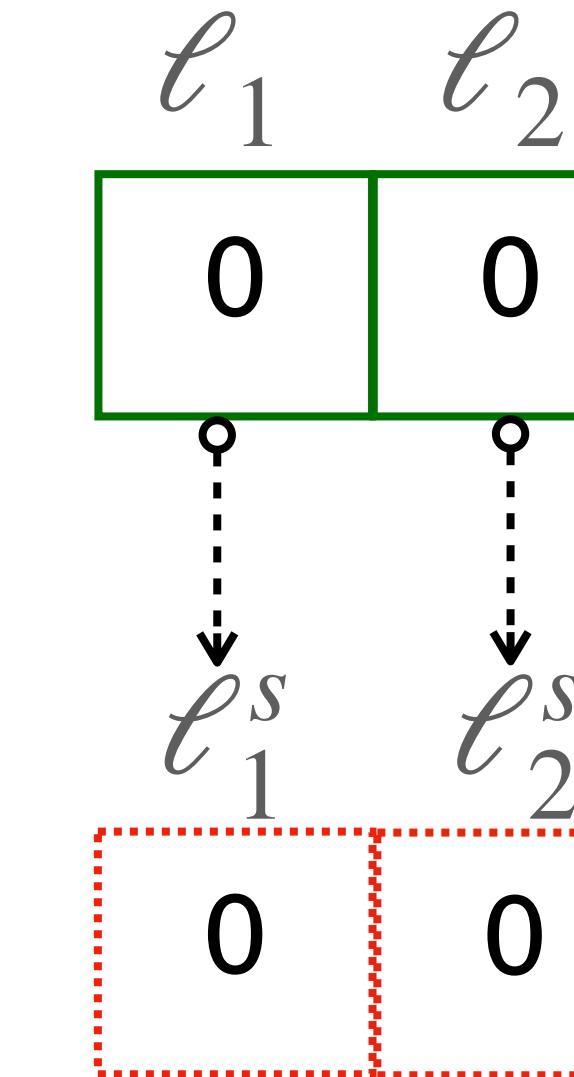


Volatile memory

Unstable values $\downarrow \uparrow \text{Int}$

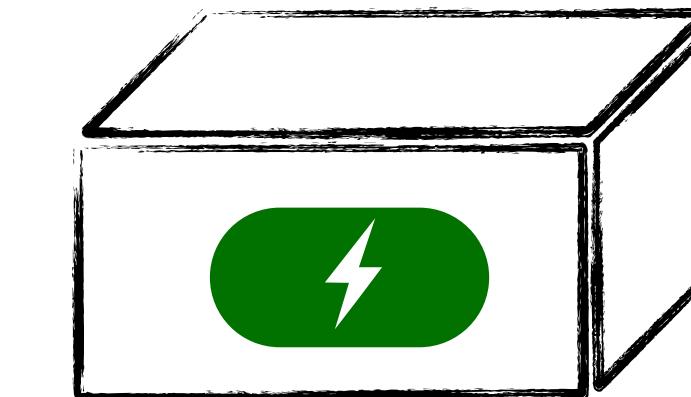
Typing rule:

$$\bullet \quad \Omega ; \Sigma \vdash \frac{\text{let } w := 2 \text{ in} \\ L1 := w + L1 \\ L2 := L2 + L1}{\tau} : C_{\text{Unit}}$$



pc → Checkpoint

```
let w:=2 in
L1:= w+L1
L2:= L2+L1
```



Adjoint logic:

$$\frac{\Omega, \uparrow A ; \downarrow \uparrow A, \Sigma \vdash \tau}{\Omega, \uparrow A ; \Sigma \vdash \tau} \xrightarrow{\text{L}^*}$$

Ckpt

Check point

$$\Omega \vdash \frac{\text{let } w := 2 \text{ in} \\ L1 := w + L1 \\ L2 := L2 + L1}{\tau} : \uparrow C_{\text{Unit}}$$

$$\Omega, \uparrow A ; \Sigma \vdash \tau$$

$$\frac{\Omega ; \bullet \vdash \tau}{\Omega \vdash \uparrow \tau} \xrightarrow{\text{R}}$$

$$\Omega \vdash \uparrow \tau$$

Outline

- A type system based on adjoint modalities and independence principle
 - Correctness as a logical relation
 - Conclusion

Correctness of programs

Every intermittent execution of the program can be simulated by a continuous execution of it.

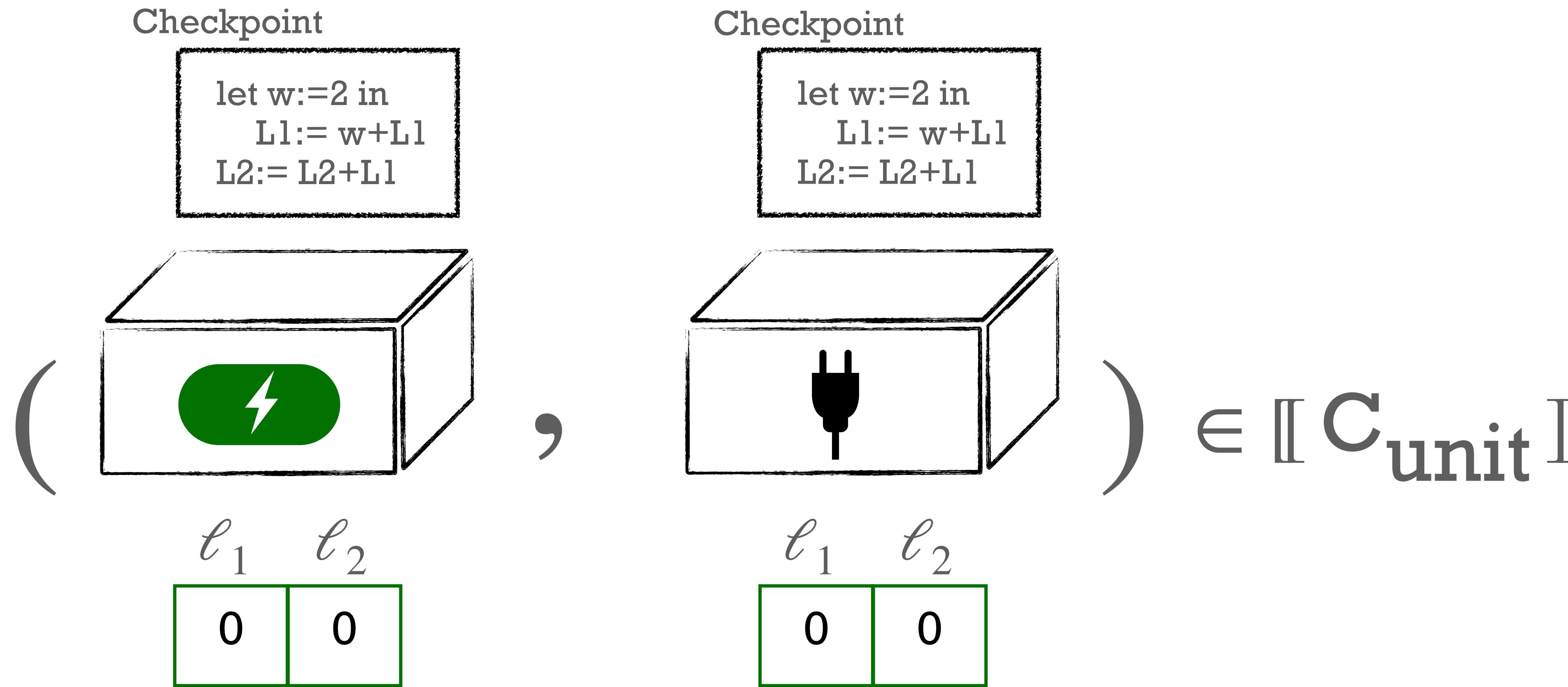
Correctness of programs

Every intermittent execution of the program can be simulated by a continuous execution of it.

We define correctness based on the meaning of crash types annotated with \uparrow and \downarrow .

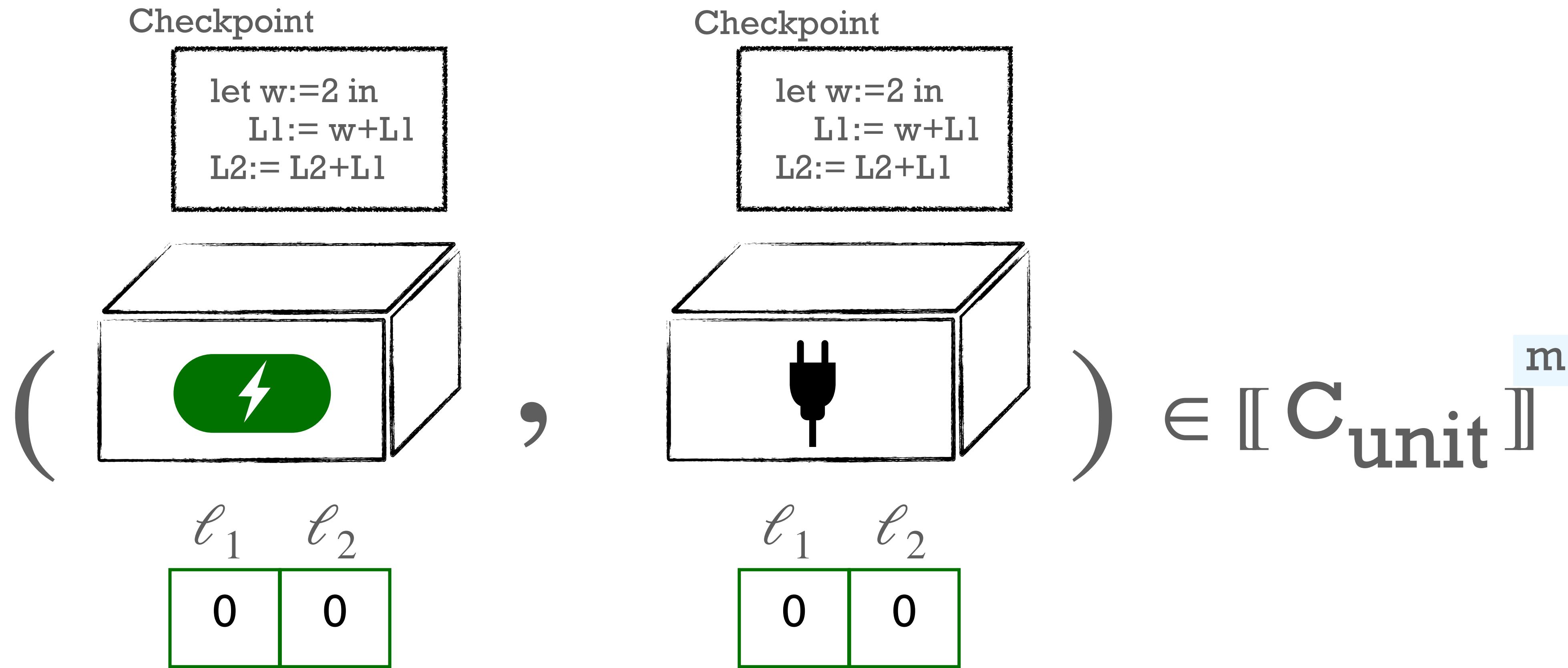
Logical relation for correctness

An intermittent execution of a well-typed program can be simulated by its continuous execution.



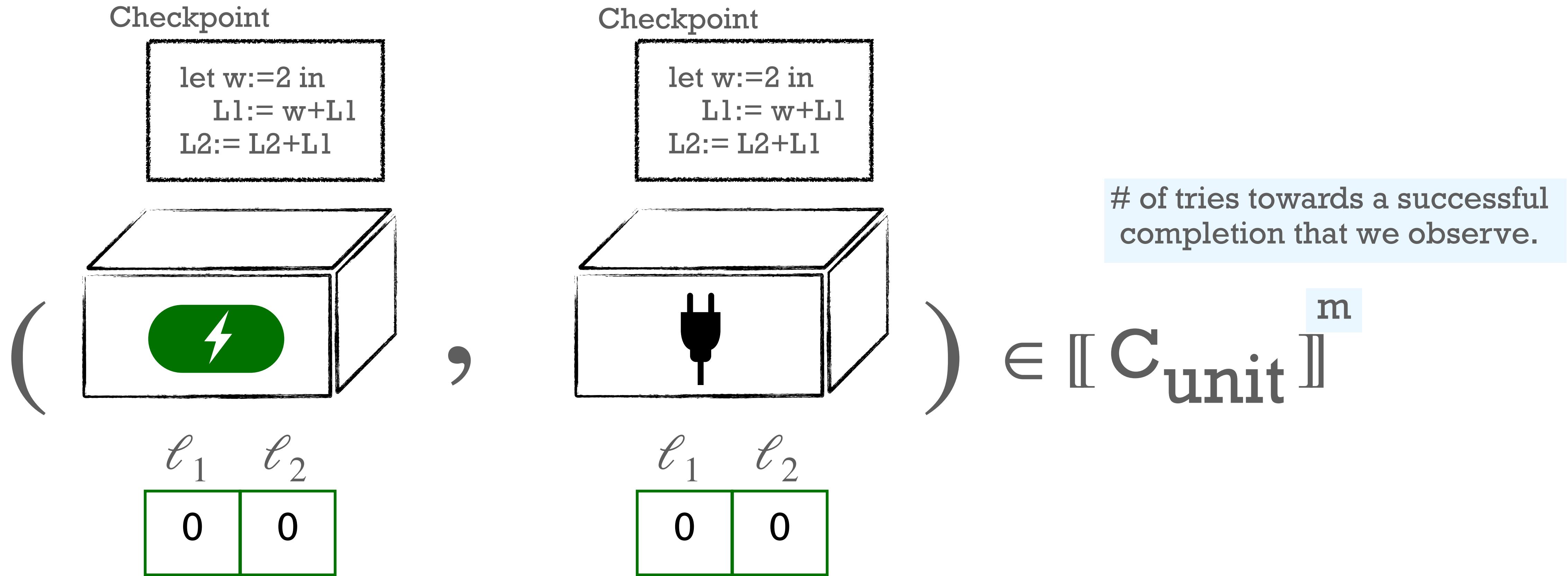
Logical relation for correctness

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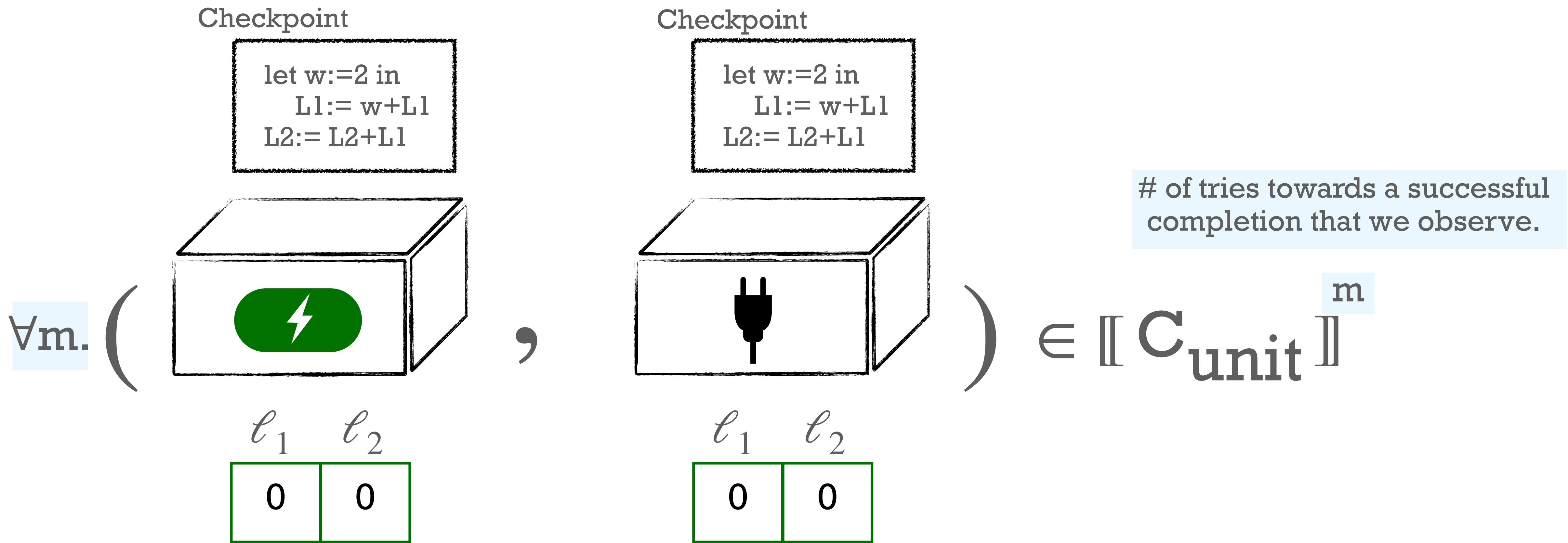
Logical relation for correctness

An intermittent execution of a well-typed program can be simulated by its continuous execution.

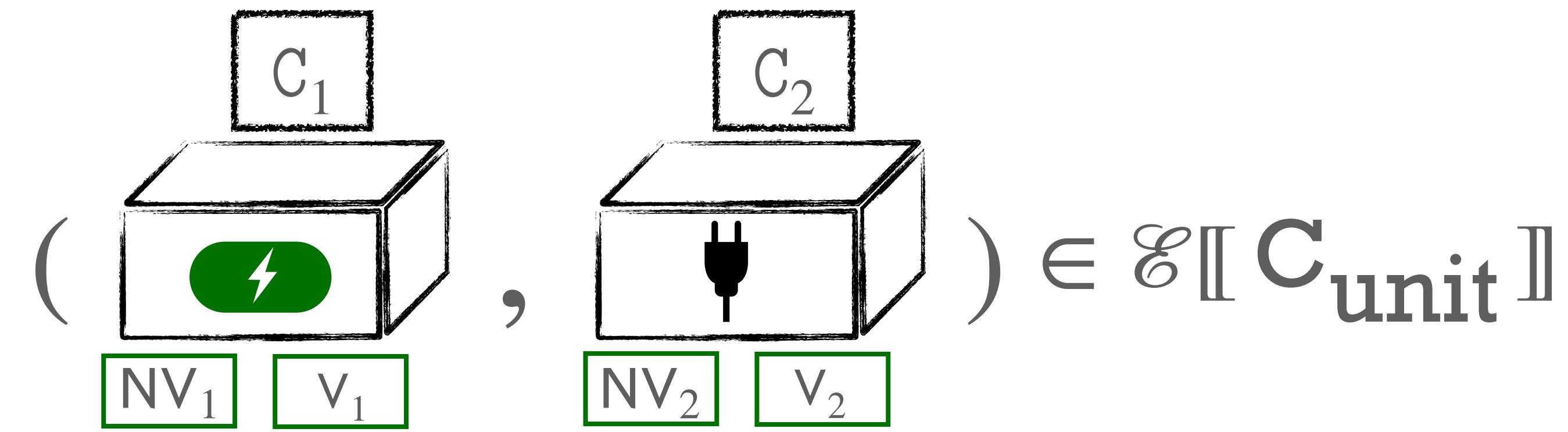


Logical relation for correctness

An intermittent execution of a well-typed program can be simulated by its continuous execution.



Term relation



Term relation - base case : we observe nothing

$$(\text{C}_1, \text{C}_2) \in \mathcal{E}[\mathbf{C}_{\text{unit}}]^0$$

always true!

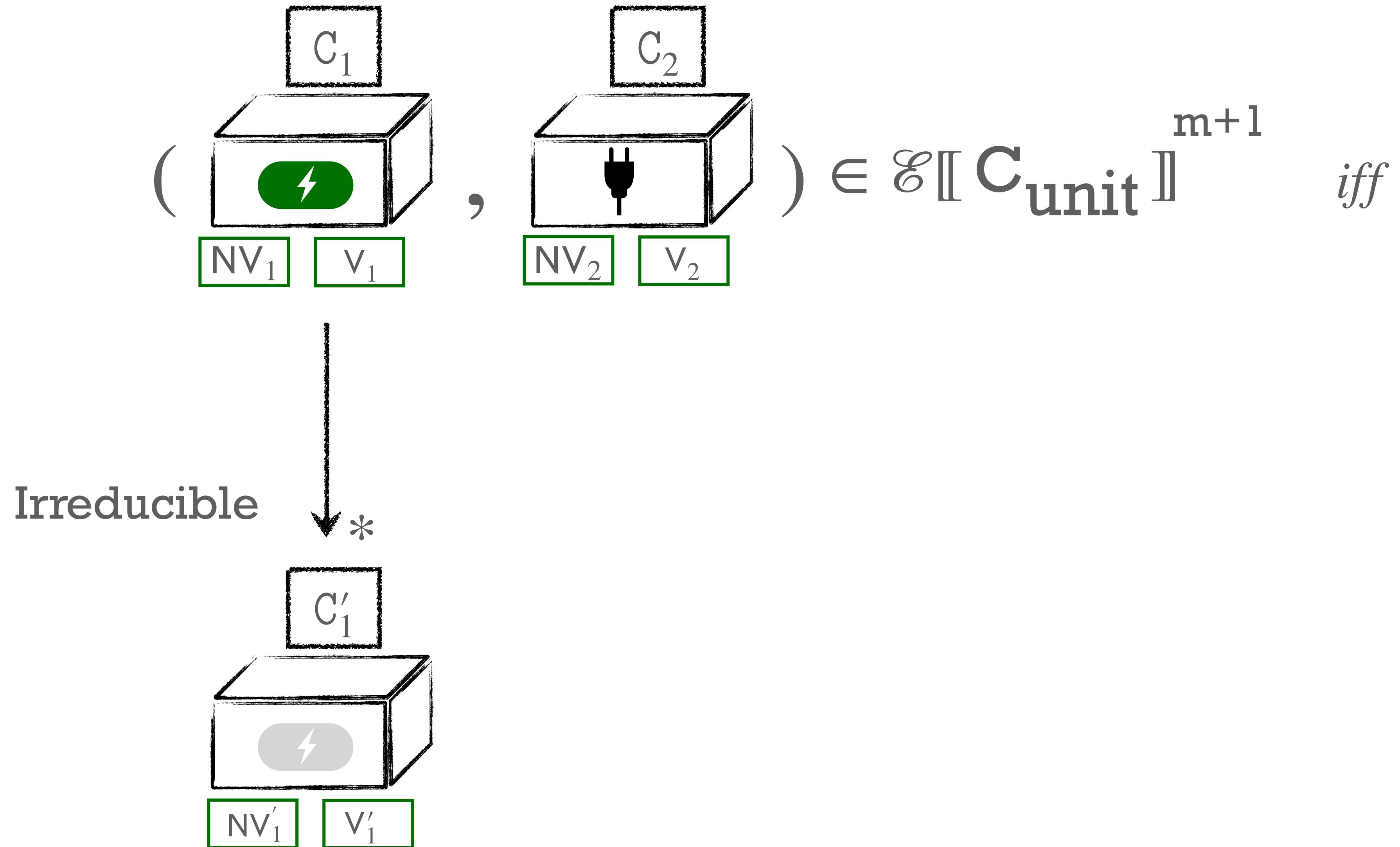
Term relation - inductive case

$$(\text{C}_1, \text{C}_2) \in \mathcal{E}[\mathbf{C}_{\text{unit}}]^{m+1} \text{ iff}$$

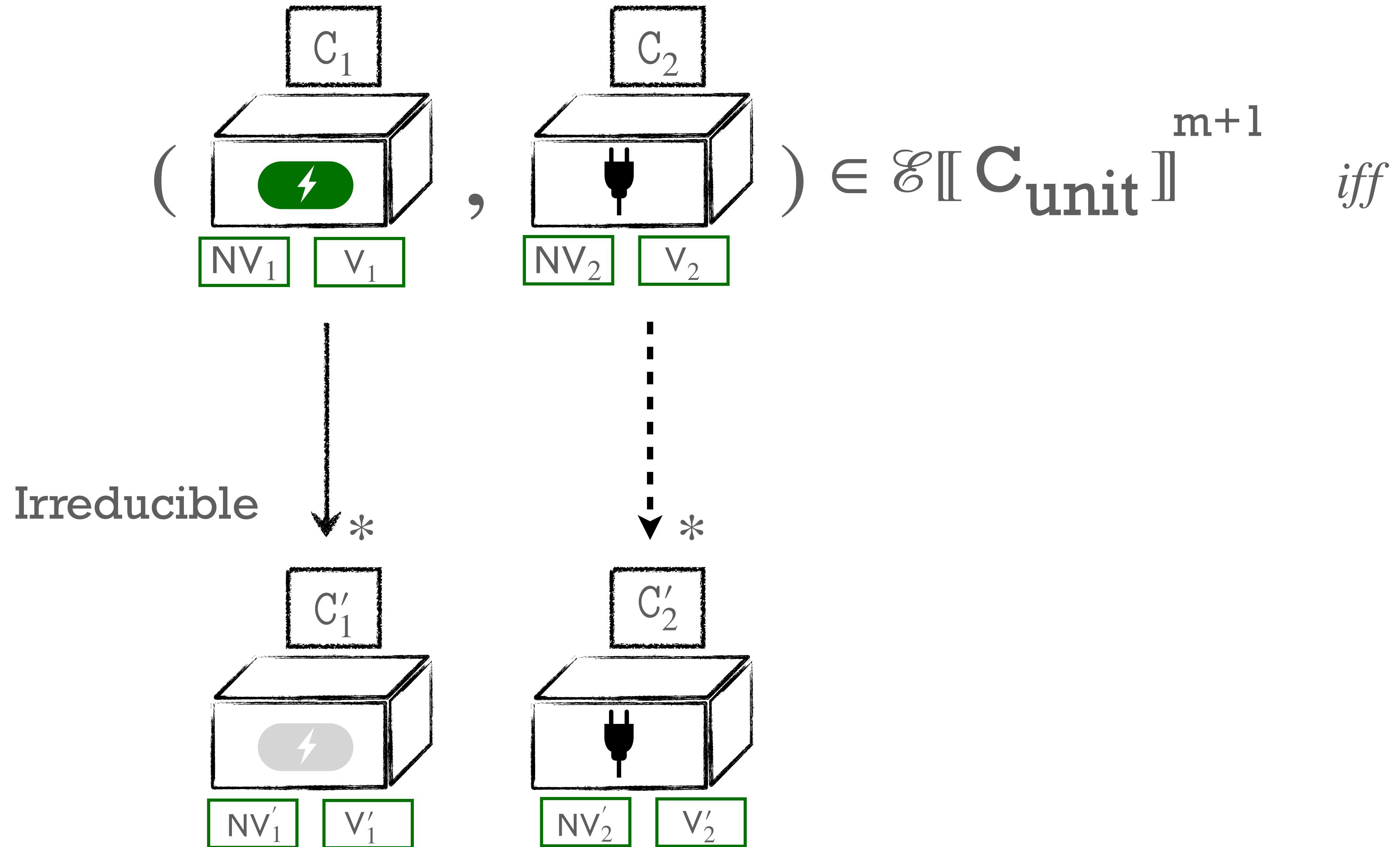
Diagram illustrating the term relation for two components C_1 and C_2 :

- C_1 is represented by a rectangular box containing a green oval with a lightning bolt icon. Below the box are two green-bordered boxes labeled NV_1 and V_1 .
- C_2 is represented by a rectangular box containing a black plug icon. Below the box are two green-bordered boxes labeled NV_2 and V_2 .

Term relation - inductive case



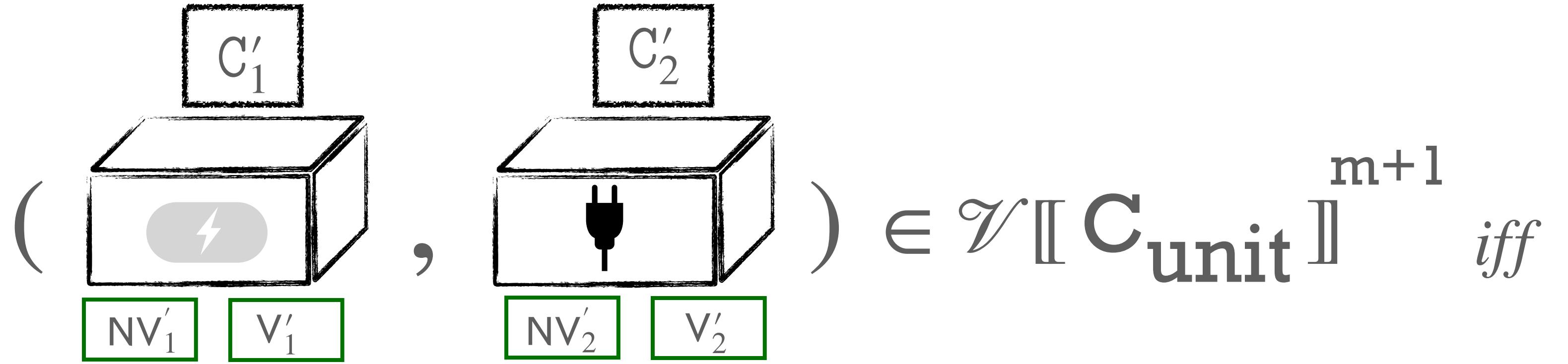
Term relation - inductive case



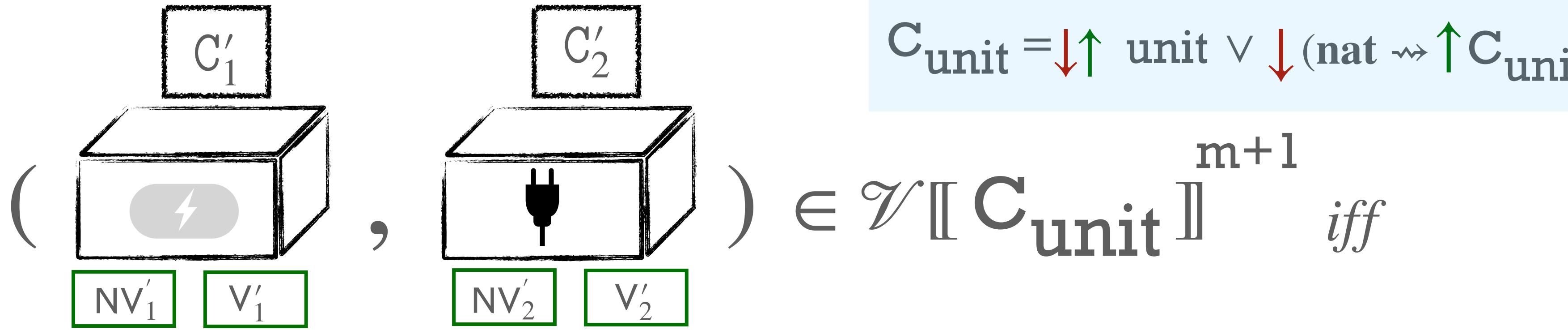
Term relation - inductive case

$(C_1, NV_1, V_1, C_2, NV_2, V_2) \in \mathcal{E}[\text{C}_{\text{unit}}]^{m+1}$ iff
 $(C'_1, NV'_1, V'_1, C'_2, NV'_2, V'_2) \in \mathcal{V}[\text{C}_{\text{unit}}]^{m+1}$

Value relation

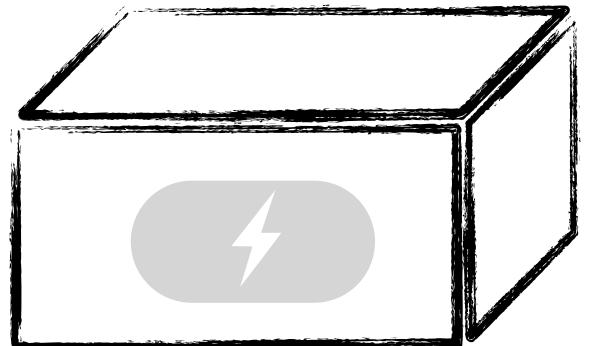


Value relation



Value relation

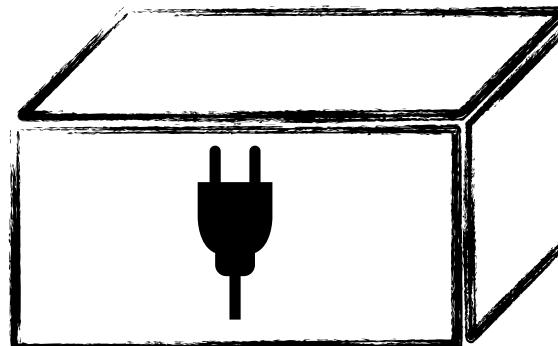
C'_1



NV'_1

V'_1

C'_2



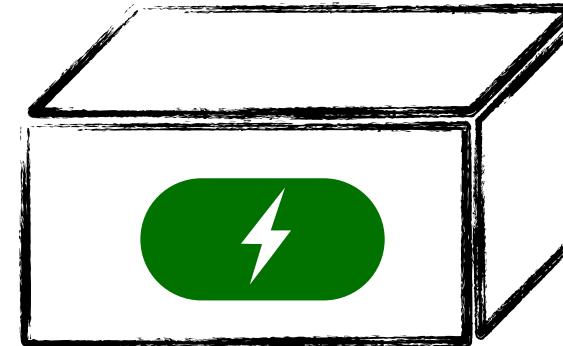
NV'_2

V'_2

$$C_{\text{unit}} = \downarrow \uparrow \text{unit} \vee \downarrow (\text{nat} \rightsquigarrow \uparrow C_{\text{unit}})$$

$\left(\quad , \quad \right) \in \mathcal{V}[\![C_{\text{unit}}]\!]^{m+1}$ iff

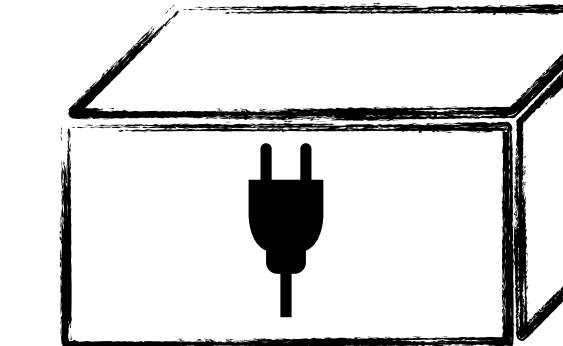
C'_1



NV'_1

V'_1

C'_2



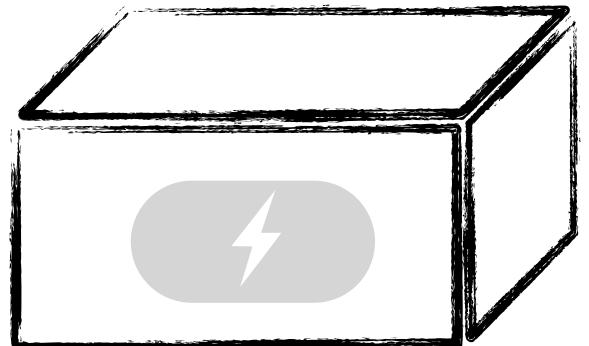
NV'_2

V'_2

$\left(\quad , \quad \right) \in \mathcal{V}[\![\downarrow \uparrow \text{unit}]\!]^m$

Value relation

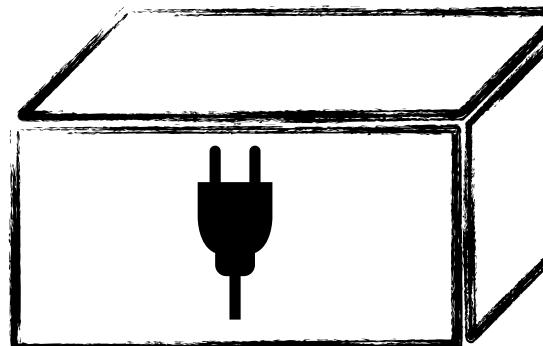
C'_1



NV'_1

V'_1

C'_2

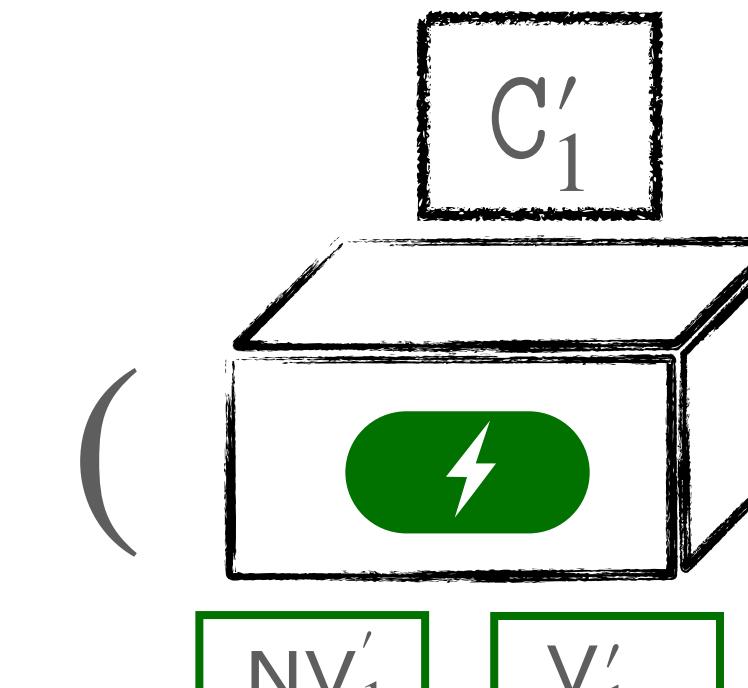


NV'_2

V'_2

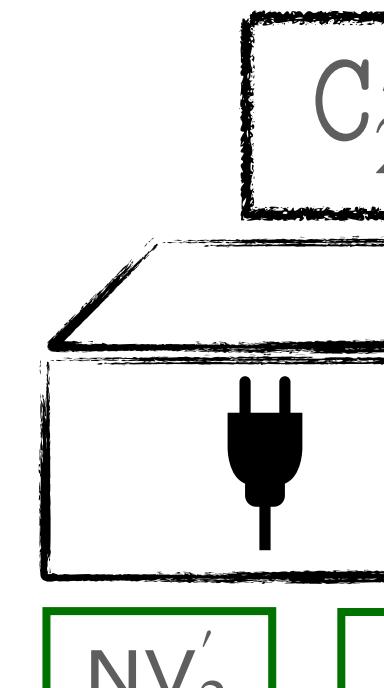
$$C_{\text{unit}} = \downarrow \uparrow \text{unit} \vee \downarrow (\text{nat} \rightsquigarrow \uparrow C_{\text{unit}})$$

$\in \mathcal{V}[\![C_{\text{unit}}]\!]^{m+1}$ iff



NV'_1

V'_1



NV'_2

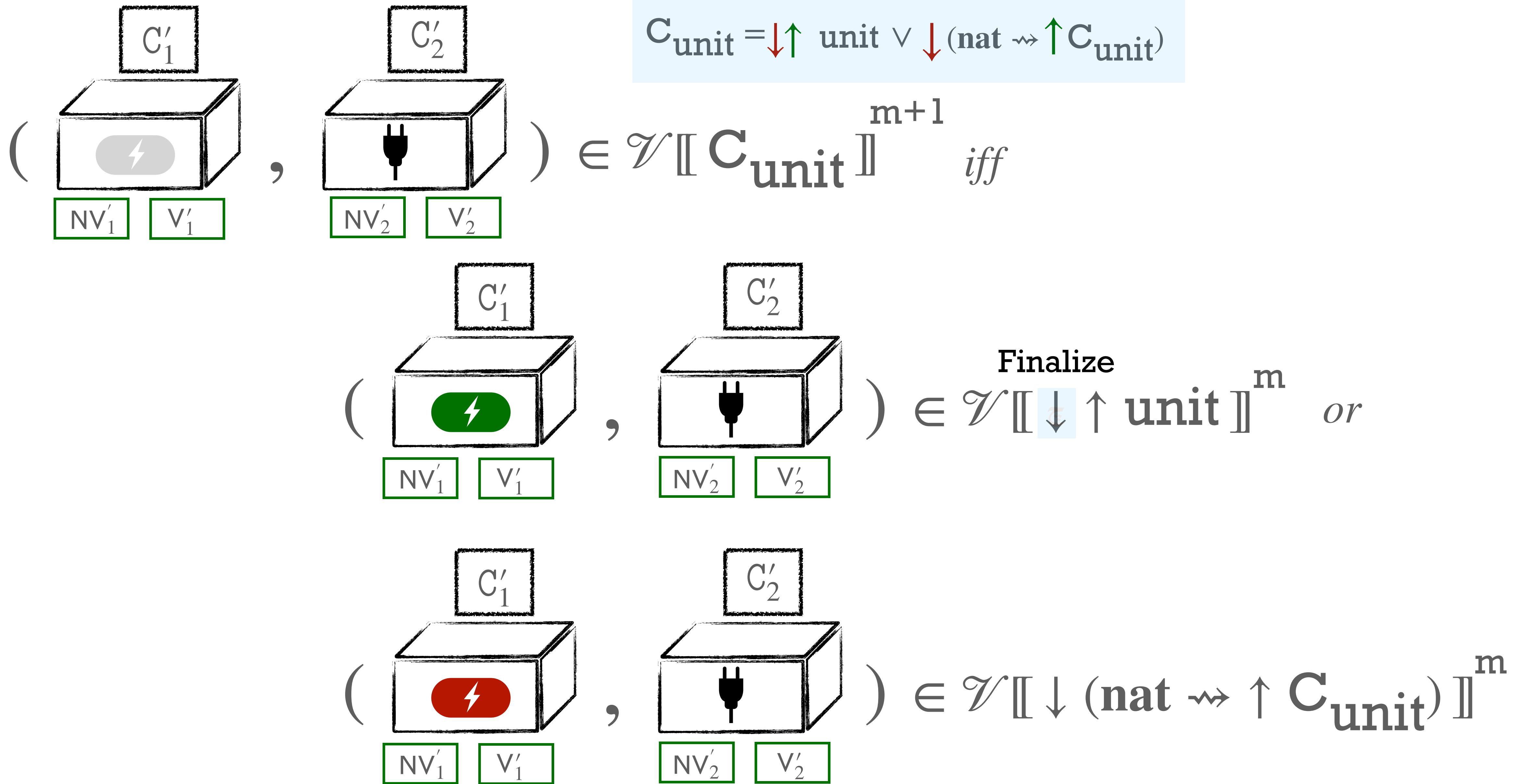
V'_2

$\in \mathcal{V}[\![\downarrow \uparrow \text{unit}]\!]^m$ or

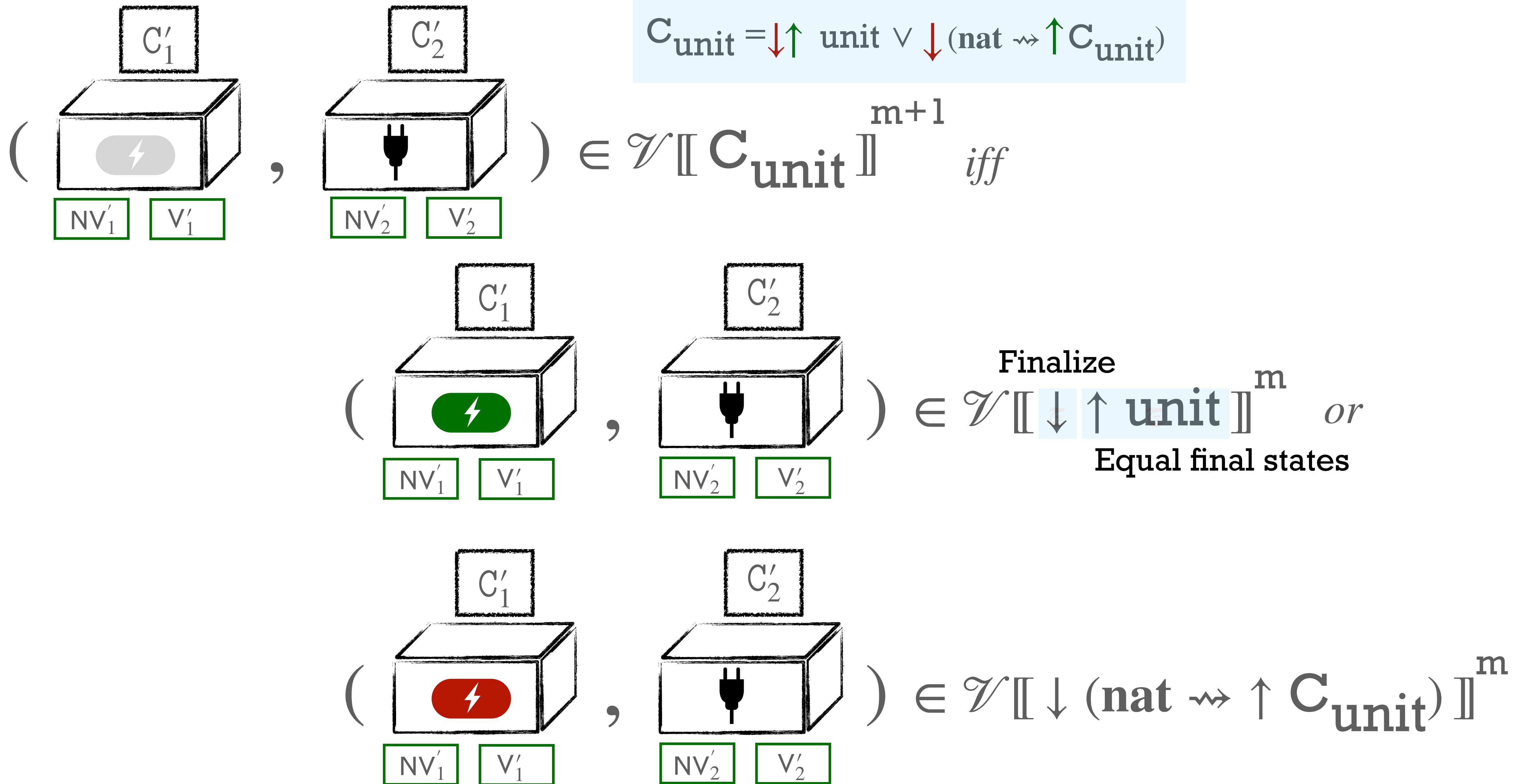
Value relation

- $(\begin{array}{c} C'_1 \\ \hline \text{---} \\ \text{---} \end{array}, \begin{array}{c} C'_2 \\ \hline \text{---} \\ \text{---} \end{array}) \in \mathcal{V}[\![C_{\text{unit}}]\!]^{m+1}$ iff
 $C_{\text{unit}} = \downarrow \uparrow \text{unit} \vee \downarrow (\text{nat} \rightsquigarrow \uparrow C_{\text{unit}})$
- $(\begin{array}{c} C'_1 \\ \hline \text{---} \\ \text{---} \end{array}, \begin{array}{c} C'_2 \\ \hline \text{---} \\ \text{---} \end{array}) \in \mathcal{V}[\![\downarrow \uparrow \text{unit}]\!]^m$ or
- $(\begin{array}{c} C'_1 \\ \hline \text{---} \\ \text{---} \end{array}, \begin{array}{c} C'_2 \\ \hline \text{---} \\ \text{---} \end{array}) \in \mathcal{V}[\![\downarrow (\text{nat} \rightsquigarrow \uparrow C_{\text{unit}})]!]^m$

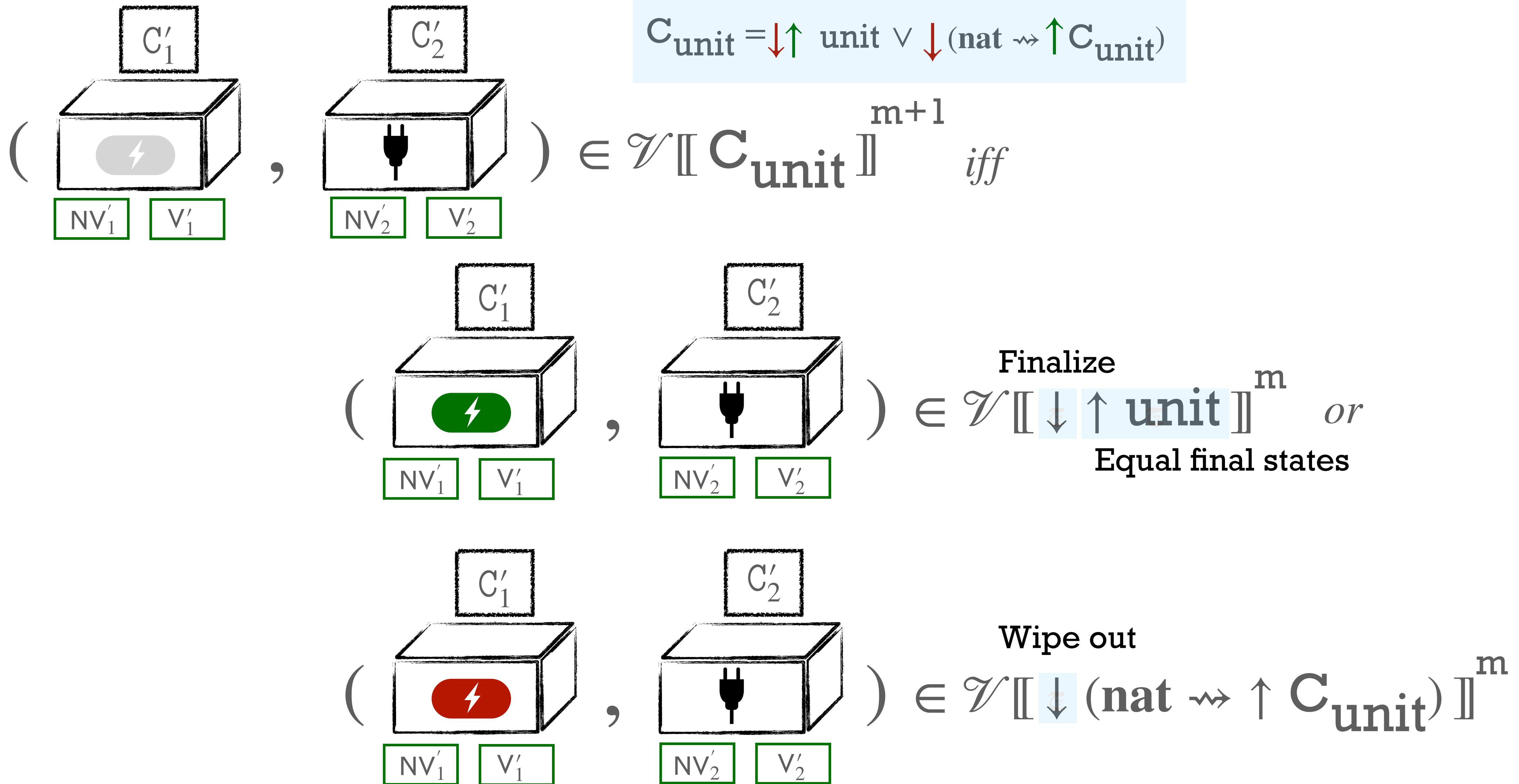
Value relation



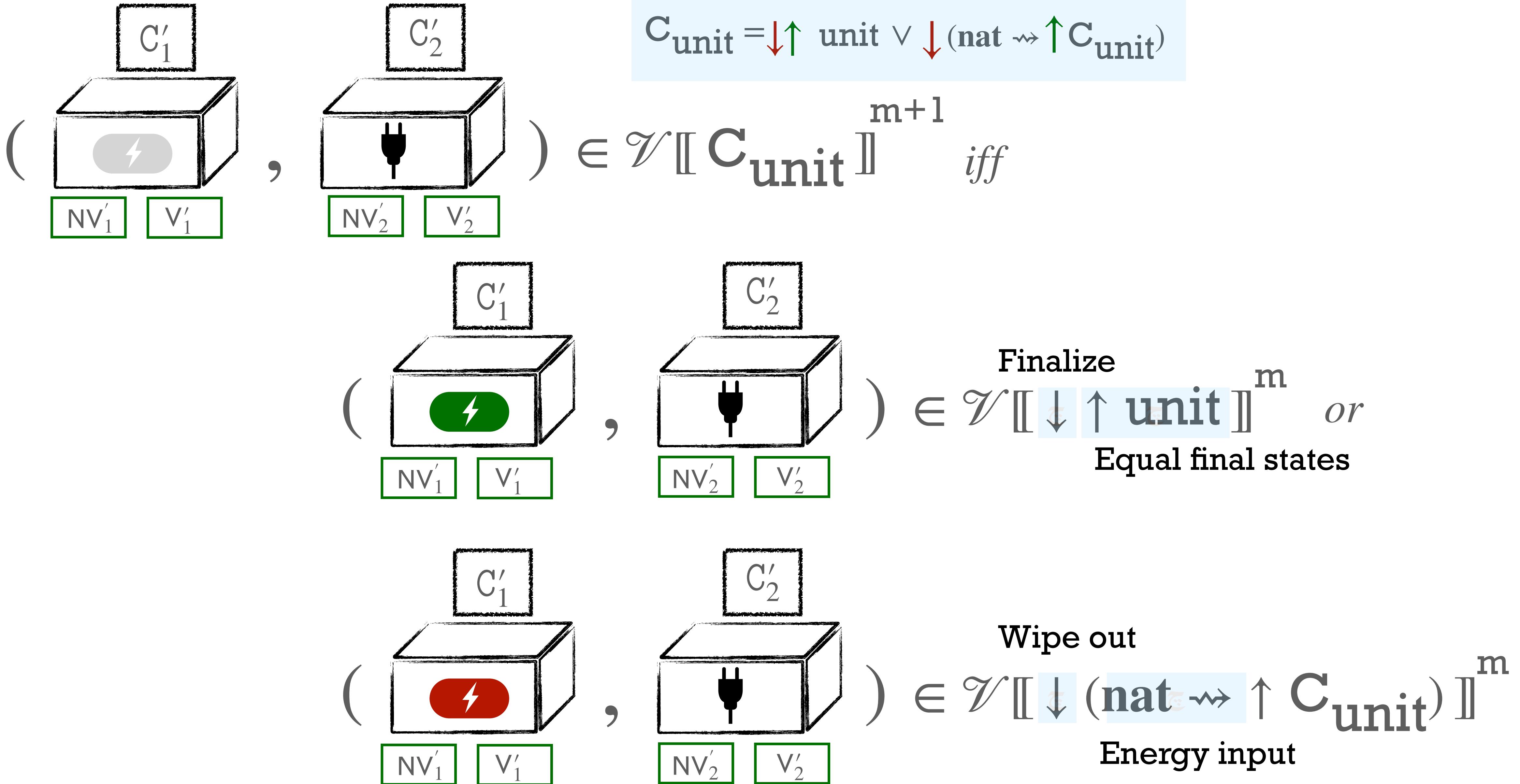
Value relation



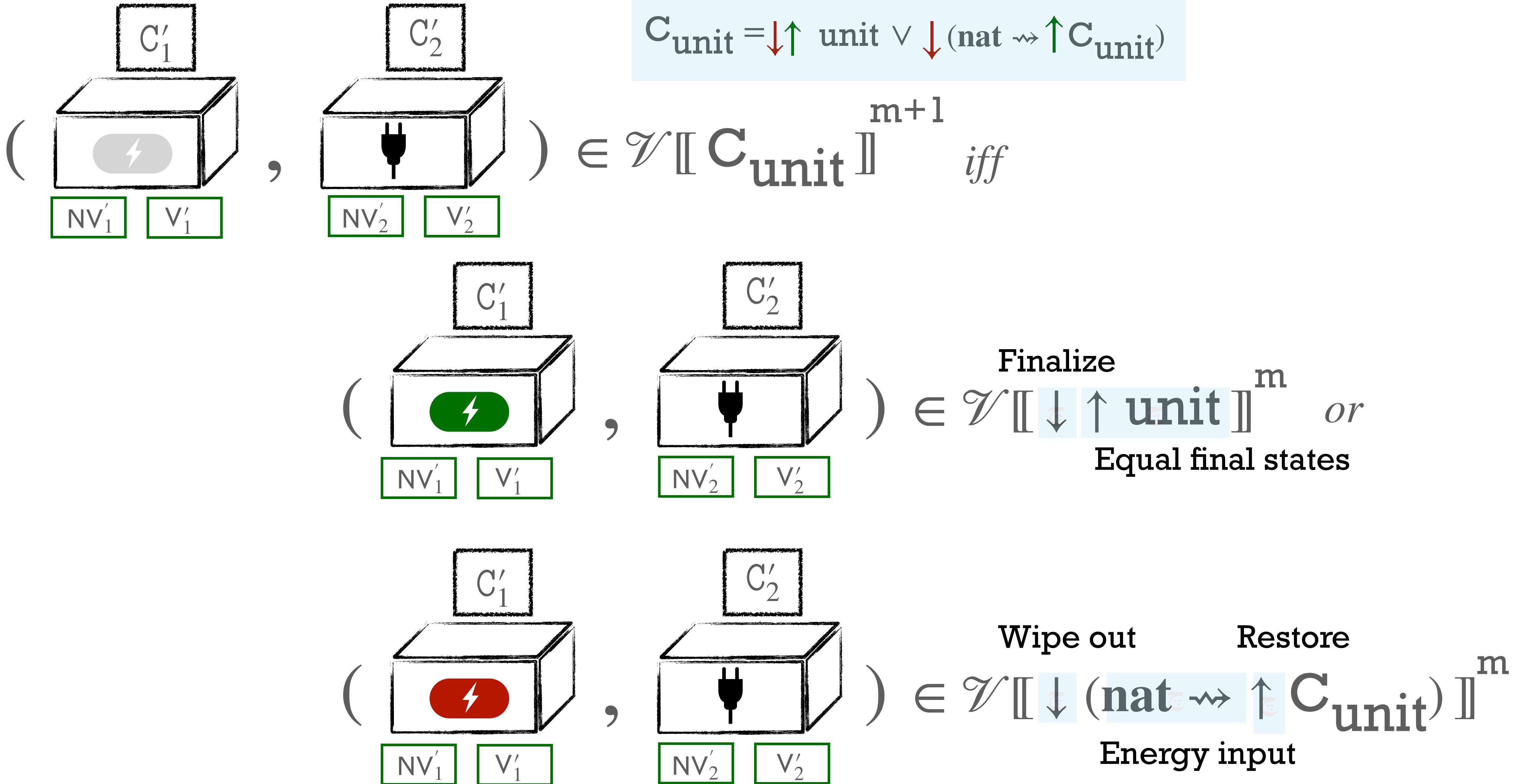
Value relation



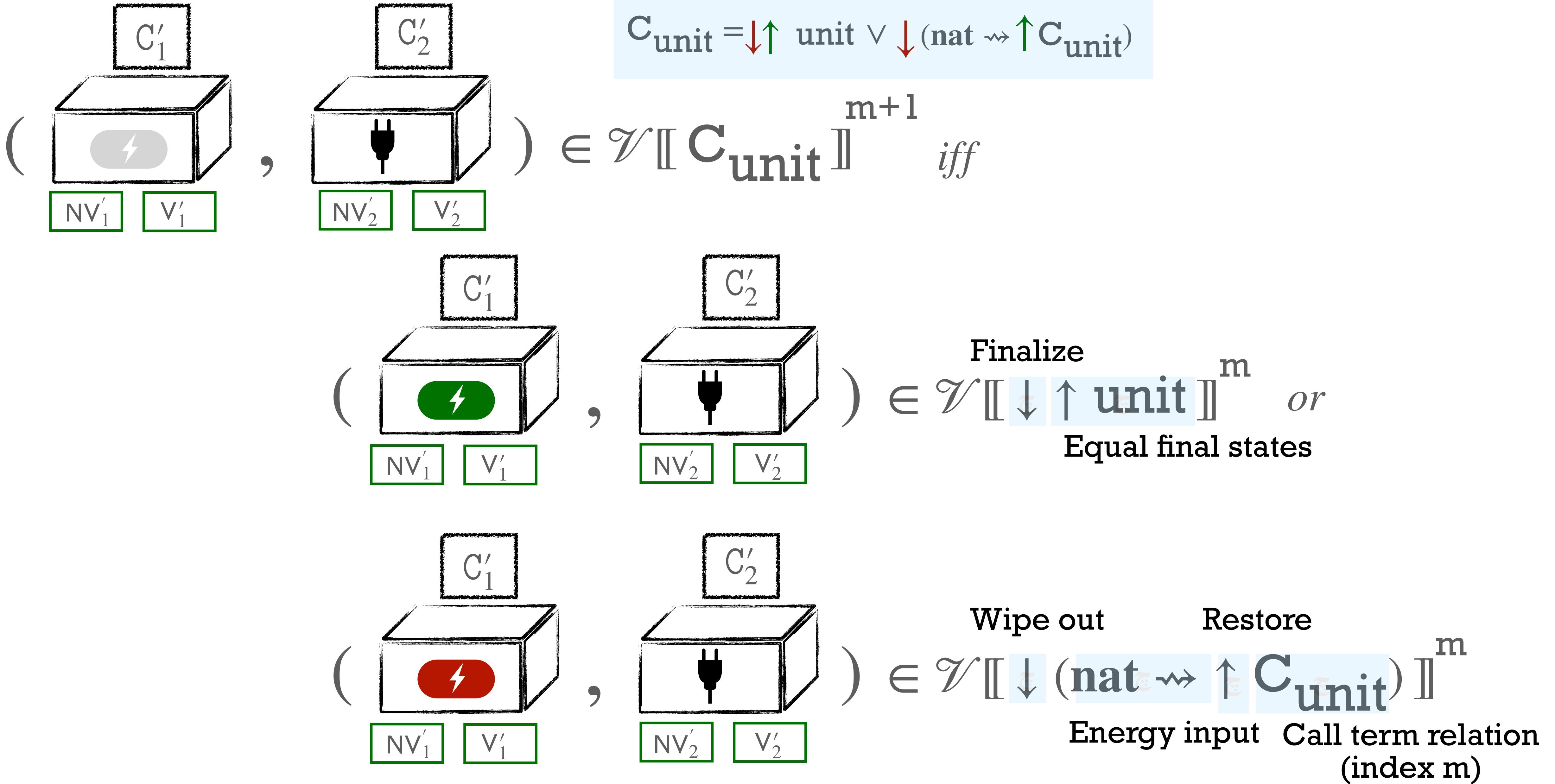
Value relation



Value relation



Value relation



Main results

- Fundamental theorem: Syntactically well-typed programs are semantically well-typed.
- Adequacy theorem: Semantically well-typed programs are correct.

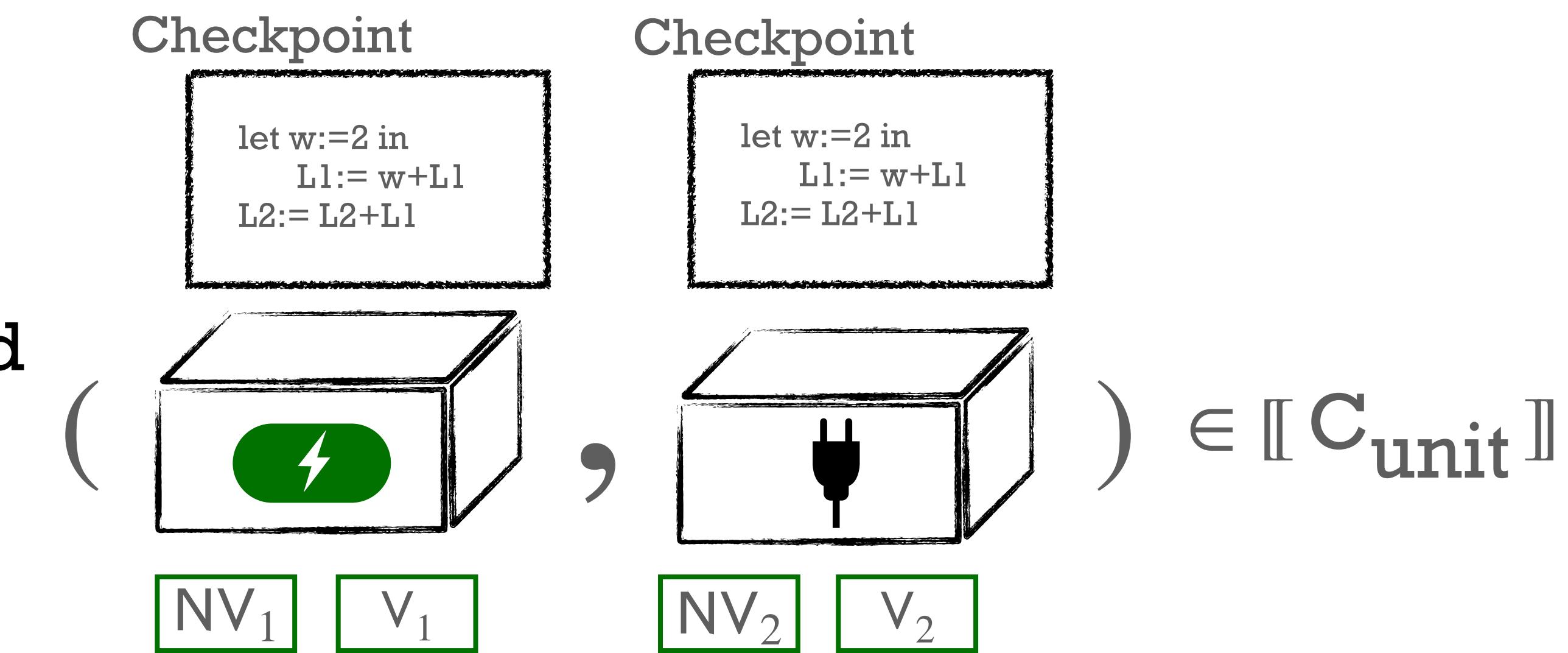
Every intermittent execution of well-typed programs can be simulated by a continuous execution of them.

Summary

- A logical interpretation of intermittent execution.
- Crash types to specify how stable and unstable portions interact.
- A core calculus for Crash types.
- A logical relation for correctness of intermittent executions.

Future work

- Not all variables need to be checkpointed
- Shared memory concurrency



Extras

$\text{Md} \mid b \geq 0 : \text{nat} \mid \Omega \mid \Sigma \Vdash c_1 \leq c_2 : \mathbf{C}_{\text{unit}}$

iff $\forall n, m \geq 0. \forall \gamma, \mathbf{NV}, \mathbf{V}. s.t. \mathbf{NV} \mid \mathbf{V} \Vdash \gamma :: \Omega \mid \Sigma.$

$(\gamma \mid \text{Md} \mid n \mid \mathbf{NV} \mid \mathbf{V} \mid c_1, \gamma \mid \text{Md} \mid \infty \mid \mathbf{NV} \mid \mathbf{V} \mid c_2) \in \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^m$

Term Relation

$$\begin{aligned} \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} = & \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) s.t. \\ & \exists. (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1) s.t. \\ & \gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1 \xrightarrow{*_{irred}} \gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1 \wedge \\ & \exists. (\gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) s.t. \\ & \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2 \xrightarrow{*} \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2 \wedge \\ & (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1, \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) \in \mathcal{V}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1}\} \end{aligned}$$

$\mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^0 = \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2)\}$

$$\text{Md} \mid b \geq 0 : \text{nat} \mid \Omega \mid \Sigma \Vdash c_1 \leq c_2 : \mathbf{C}_{\text{unit}}$$

iff $\forall n, m \geq 0. \forall \gamma, \mathbf{NV}, \mathbf{V}. s.t. \mathbf{NV} \mid \mathbf{V} \Vdash \gamma :: \Omega \mid \Sigma.$

$$(\gamma \mid \text{Md} \mid n \mid \mathbf{NV} \mid \mathbf{V} \mid c_1, \gamma \mid \text{Md} \mid \infty \mid \mathbf{NV} \mid \mathbf{V} \mid c_2) \in \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^m$$

Term Relation

$$\begin{aligned} \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} = & \{ (\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) s.t. \\ & \exists. (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1) s.t. \\ & \gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1 \xrightarrow{*_{irred}} \gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1 \wedge \\ & \exists. (\gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) s.t. \\ & \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2 \xrightarrow{*} \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2 \wedge \\ & (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1, \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) \in \mathcal{V}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} \} \end{aligned}$$

$$\mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^0 = \{ (\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \}$$

$$\text{Md} \mid b \geq 0 : \text{nat} \mid \Omega \mid \Sigma \Vdash c_1 \leq c_2 : \mathbf{C}_{\text{unit}}$$

iff $\forall n, m \geq 0. \forall \gamma, \mathbf{NV}, \mathbf{V}. s.t. \mathbf{NV} \mid \mathbf{V} \Vdash \gamma :: \Omega \mid \Sigma.$

$$(\gamma \mid \text{Md} \mid n \mid \mathbf{NV} \mid \mathbf{V} \mid c_1, \gamma \mid \text{Md} \mid \infty \mid \mathbf{NV} \mid \mathbf{V} \mid c_2) \in \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^m$$

Term Relation

$$\begin{aligned} \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} = & \{ (\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) s.t. \\ & \exists. (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1) s.t. \\ & \gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1 \xrightarrow{*_{irred}} \gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1 \wedge \\ & \exists. (\gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) s.t. \\ & \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2 \xrightarrow{*} \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2 \wedge \\ & (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1, \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) \in \mathcal{V}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} \} \end{aligned}$$

$$\mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^0 = \{ (\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \}$$

$$\text{Md} \mid b \geq 0 : \text{nat} \mid \Omega \mid \Sigma \Vdash c_1 \leq c_2 : \mathbf{C}_{\text{unit}}$$

iff $\forall n, m \geq 0. \forall \gamma, \mathbf{NV}, \mathbf{V}. s.t. \mathbf{NV} \mid \mathbf{V} \Vdash \gamma :: \Omega \mid \Sigma.$

$$(\gamma \mid \text{Md} \mid n \mid \mathbf{NV} \mid \mathbf{V} \mid c_1, \gamma \mid \text{Md} \mid \infty \mid \mathbf{NV} \mid \mathbf{V} \mid c_2) \in \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^m$$

Term Relation

$$\begin{aligned} \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} &= \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) s.t. \\ &\quad \exists. (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1) s.t. \\ &\quad \gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1 \xrightarrow{*_{irred}} \gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1 \wedge \\ &\quad \exists. (\gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) s.t. \\ &\quad \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2 \xrightarrow{*} \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2 \wedge \\ &\quad (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1, \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) \in \mathcal{V}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1}\} \end{aligned}$$

$$\mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^0 = \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2)\}$$

$$\text{Md} \mid b \geq 0 : \text{nat} \mid \Omega \mid \Sigma \Vdash c_1 \leq c_2 : \mathbf{C}_{\text{unit}}$$

iff $\forall n, m \geq 0. \forall \gamma, \mathbf{NV}, \mathbf{V}. s.t. \mathbf{NV} \mid \mathbf{V} \Vdash \gamma :: \Omega \mid \Sigma.$

$$(\gamma \mid \text{Md} \mid n \mid \mathbf{NV} \mid \mathbf{V} \mid c_1, \gamma \mid \text{Md} \mid \infty \mid \mathbf{NV} \mid \mathbf{V} \mid c_2) \in \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^m$$

Term Relation

$$\begin{aligned} \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} &= \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) s.t. \\ &\quad \exists. (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1) s.t. \\ &\quad \gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1 \xrightarrow{*_{irred}} \gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1 \wedge \\ &\quad \exists. (\gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) s.t. \\ &\quad \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2 \xrightarrow{*} \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2 \wedge \\ &\quad (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1, \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) \in \mathcal{V}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1}\} \end{aligned}$$

$$\mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^0 = \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2)\}$$

$$\text{Md} \mid b \geq 0 : \text{nat} \mid \Omega \mid \Sigma \Vdash c_1 \leq c_2 : \mathbf{C}_{\text{unit}}$$

iff $\forall n, m \geq 0. \forall \gamma, \mathbf{NV}, \mathbf{V}. s.t. \mathbf{NV} \mid \mathbf{V} \Vdash \gamma :: \Omega \mid \Sigma.$

$$(\gamma \mid \text{Md} \mid n \mid \mathbf{NV} \mid \mathbf{V} \mid c_1, \gamma \mid \text{Md} \mid \infty \mid \mathbf{NV} \mid \mathbf{V} \mid c_2) \in \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^m$$

Term Relation

$$\begin{aligned} \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} &= \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) s.t. \\ &\quad \exists. (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1) s.t. \\ &\quad \gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1 \xrightarrow{*_{irred}} \gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1 \wedge \\ &\quad \exists. (\gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) s.t. \\ &\quad \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2 \xrightarrow{*} \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2 \wedge \\ &\quad (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1, \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) \in \mathcal{V}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1}\} \\ \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^0 &= \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2)\} \end{aligned}$$

Value Relation

$$\mathcal{V}[\uparrow \text{unit}]^m = \{(\gamma | \text{Md} | n_1 | \text{NV}_1 | \text{skip}, \gamma | \text{Md} | \infty | \text{NV}_2 | \text{skip}) \text{ s.t. } \text{NV}_1 = \text{NV}_2\}$$

$$\begin{aligned} \mathcal{V}[\downarrow \uparrow \text{unit}]^m &= \{(\gamma_1 | \text{Md} | n_1 | \text{NV}_1 | \text{V}_1 | \text{skip}, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | \text{skip}) \text{ s.t. } \\ &\quad \text{Commit}(\gamma_i | \text{Md} | \text{NV}_i | \text{V}_i) = \gamma'_i | \text{NV}'_i \wedge \\ &\quad (\gamma'_1 | \text{Md} | n_1 | \text{NV}'_1 | \text{skip}, \gamma_2 | \text{Md} | \infty | \text{NV}'_2 | \text{skip}) \in \mathcal{V}[\uparrow \text{unit}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\uparrow \mathbf{C}_{\text{unit}}]^m &= \{(\gamma_1 | \text{Md} | n | \text{NV}_1 | \uparrow \kappa, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \text{ s.t. } \\ &\quad \text{restore}(\gamma_1, \text{Md}, \text{NV}_1, \kappa) = \text{NV}_0 | \text{V}_0 | c_0 \wedge \\ &\quad (\gamma_1 | \text{Md} | n | \text{NV}_0 | \text{V}_0 | c_0, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \in \mathcal{E}[\mathbf{C}_{\text{unit}}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}}]^m &= \{(\gamma_1 | \text{Md} | \cdot | \text{NV}_1 | \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \text{ s.t. } \\ &\quad \forall n > 0. (\gamma_1 | \text{Md} | n | \text{NV}_1 | \uparrow \kappa, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \in \mathcal{V}[\uparrow \mathbf{C}_{\text{unit}}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\downarrow (\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}})]^m &= \{(\gamma_1 | \text{Md} | \cdot | \text{NV}_1 | \text{V}_1 | \downarrow \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \text{s.t. } \text{PwOff}(\gamma_1, \text{Md}, \text{NV}_1, \text{V}_1) = \gamma'_1 | \text{V}'_1 \wedge \\ &\quad (\gamma'_1 | \text{Md} | \cdot | \text{V}', \text{NV}_1 | \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \in \mathcal{V}[\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\mathbf{C}_{\text{unit}}]^{m+1} &= \{(\gamma_1 | \text{Md} | n_1 | \text{NV}_1 | \text{V}_1 | c_1, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \text{s.t. either } \\ &\quad n_1 = 0 \wedge (\gamma_1 | \text{Md} | \cdot | \text{NV}_1 | \text{V}_1 | \downarrow \varepsilon \# \text{in}(n_1 > 0, \uparrow c_1), \\ &\quad \quad \quad \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \in \mathcal{V}[\downarrow (\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}})]^m, \text{ or } \\ &\quad n_1 > 0 \wedge (\gamma_1 | \text{Md} | n_1 | \text{NV}_1 | \text{V}_1 | c_1, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \quad \quad \in \mathcal{V}[\downarrow \uparrow \text{unit}]^m\} \end{aligned}$$

Value Relation

$$\mathcal{V}[\uparrow \text{unit}]^m = \{(\gamma | \text{Md} | n_1 | \text{NV}_1 | \text{skip}, \gamma | \text{Md} | \infty | \text{NV}_2 | \text{skip}) \text{ s.t. } \text{NV}_1 = \text{NV}_2\}$$

$$\begin{aligned} \mathcal{V}[\downarrow \uparrow \text{unit}]^m &= \{(\gamma_1 | \text{Md} | n_1 | \text{NV}_1 | \text{V}_1 | \text{skip}, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | \text{skip}) \text{ s.t. } \\ &\quad \text{Commit}(\gamma_i | \text{Md} | \text{NV}_i | \text{V}_i) = \gamma'_i | \text{NV}'_i \wedge \\ &\quad (\gamma'_1 | \text{Md} | n_1 | \text{NV}'_1 | \text{skip}, \gamma_2 | \text{Md} | \infty | \text{NV}'_2 | \text{skip}) \in \mathcal{V}[\uparrow \text{unit}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\uparrow \mathbf{C}_{\text{unit}}]^m &= \{(\gamma_1 | \text{Md} | n | \text{NV}_1 | \uparrow \kappa, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \text{ s.t. } \\ &\quad \text{restore}(\gamma_1, \text{Md}, \text{NV}_1, \kappa) = \text{NV}_0 | \text{V}_0 | c_0 \wedge \\ &\quad (\gamma_1 | \text{Md} | n | \text{NV}_0 | \text{V}_0 | c_0, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \in \mathcal{E}[\mathbf{C}_{\text{unit}}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}}]^m &= \{(\gamma_1 | \text{Md} | \cdot | \text{NV}_1 | \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \text{ s.t. } \\ &\quad \forall n > 0. (\gamma_1 | \text{Md} | n | \text{NV}_1 | \uparrow \kappa, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \in \mathcal{V}[\uparrow \mathbf{C}_{\text{unit}}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\downarrow (\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}})]^m &= \{(\gamma_1 | \text{Md} | \cdot | \text{NV}_1 | \text{V}_1 | \downarrow \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \text{s.t. } \text{PwOff}(\gamma_1, \text{Md}, \text{NV}_1, \text{V}_1) = \gamma'_1 | \text{V}'_1 \wedge \\ &\quad (\gamma'_1 | \text{Md} | \cdot | \text{V}', \text{NV}_1 | \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \in \mathcal{V}[\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\mathbf{C}_{\text{unit}}]^{m+1} &= \{(\gamma_1 | \text{Md} | n_1 | \text{NV}_1 | \text{V}_1 | c_1, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \text{s.t. either} \\ &\quad n_1 = 0 \wedge (\gamma_1 | \text{Md} | \cdot | \text{NV}_1 | \text{V}_1 | \downarrow \varepsilon \# \text{in}(n_1 > 0, \uparrow c_1), \\ &\quad \quad \quad \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \in \mathcal{V}[\downarrow (\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}})]^m, \text{ or} \\ &\quad n_1 > 0 \wedge (\gamma_1 | \text{Md} | n_1 | \text{NV}_1 | \text{V}_1 | c_1, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \quad \quad \in \mathcal{V}[\downarrow \uparrow \text{unit}]^m\} \end{aligned}$$

Value Relation

$$\mathcal{V}[\uparrow \text{unit}]^m = \{(\gamma | \text{Md} | n_1 | \text{NV}_1 | \text{skip}, \gamma | \text{Md} | \infty | \text{NV}_2 | \text{skip}) \text{ s.t. } \text{NV}_1 = \text{NV}_2\}$$

$$\begin{aligned} \mathcal{V}[\downarrow \uparrow \text{unit}]^m &= \{(\gamma_1 | \text{Md} | n_1 | \text{NV}_1 | \text{V}_1 | \text{skip}, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | \text{skip}) \text{ s.t. } \\ &\quad \text{Commit}(\gamma_i | \text{Md} | \text{NV}_i | \text{V}_i) = \gamma'_i | \text{NV}'_i \wedge \\ &\quad (\gamma'_1 | \text{Md} | n_1 | \text{NV}'_1 | \text{skip}, \gamma_2 | \text{Md} | \infty | \text{NV}'_2 | \text{skip}) \in \mathcal{V}[\uparrow \text{unit}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\uparrow C_{\text{unit}}]^m &= \{(\gamma_1 | \text{Md} | n | \text{NV}_1 | \uparrow \kappa, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \text{ s.t. } \\ &\quad \text{restore}(\gamma_1, \text{Md}, \text{NV}_1, \kappa) = \text{NV}_0 | \text{V}_0 | c_0 \wedge \\ &\quad (\gamma_1 | \text{Md} | n | \text{NV}_0 | \text{V}_0 | c_0, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \in \mathcal{E}[C_{\text{unit}}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\text{nat} \rightsquigarrow \uparrow C_{\text{unit}}]^m &= \{(\gamma_1 | \text{Md} | \cdot | \text{NV}_1 | \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \text{ s.t. } \\ &\quad \forall n > 0. (\gamma_1 | \text{Md} | n | \text{NV}_1 | \uparrow \kappa, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \in \mathcal{V}[\uparrow C_{\text{unit}}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[\downarrow (\text{nat} \rightsquigarrow \uparrow C_{\text{unit}})]^m &= \{(\gamma_1 | \text{Md} | \cdot | \text{NV}_1 | \text{V}_1 | \downarrow \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \text{s.t. } \text{PwOff}(\gamma_1, \text{Md}, \text{NV}_1, \text{V}_1) = \gamma'_1 | \text{V}' \wedge \\ &\quad (\gamma'_1 | \text{Md} | \cdot | \text{V}', \text{NV}_1 | \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \in \mathcal{V}[\text{nat} \rightsquigarrow \uparrow C_{\text{unit}}]^m\} \end{aligned}$$

$$\begin{aligned} \mathcal{V}[C_{\text{unit}}]^{m+1} &= \{(\gamma_1 | \text{Md} | n_1 | \text{NV}_1 | \text{V}_1 | c_1, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \text{s.t. either } \\ &\quad n_1 = 0 \wedge (\gamma_1 | \text{Md} | \cdot | \text{NV}_1 | \text{V}_1 | \downarrow \varepsilon \# \text{in}(n_1 > 0, \uparrow c_1), \\ &\quad \quad \quad \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \in \mathcal{V}[\downarrow (\text{nat} \rightsquigarrow \uparrow C_{\text{unit}})]^m, \text{ or } \\ &\quad n_1 > 0 \wedge (\gamma_1 | \text{Md} | n_1 | \text{NV}_1 | \text{V}_1 | c_1, \gamma_2 | \text{Md} | \infty | \text{NV}_2 | \text{V}_2 | c_2) \\ &\quad \quad \quad \in \mathcal{V}[\downarrow \uparrow \text{unit}]^m\} \end{aligned}$$

$$\begin{aligned}
& \text{Md} \mid b \geq 0 : \text{nat} \mid \Omega \mid \Sigma \Vdash c_1 \leq c_2 : \mathbf{C}_{\text{unit}} \\
& \text{iff } \forall n, m \geq 0. \forall \gamma, \mathbf{NV}, \mathbf{V}. \text{s.t. } \mathbf{NV} \mid \mathbf{V} \Vdash \gamma :: \Omega \mid \Sigma. \\
& \quad (\gamma \mid \text{Md} \mid n \mid \mathbf{NV} \mid \mathbf{V} \mid c_1, \gamma \mid \text{Md} \mid \infty \mid \mathbf{NV} \mid \mathbf{V} \mid c_2) \in \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^m
\end{aligned}$$

Term Relation

$$\begin{aligned}
\mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} &= \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \text{ s.t.} \\
&\quad \exists. (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1) \text{ s.t.} \\
&\quad \gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1 \xrightarrow{*_{\text{irred}}} \gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1 \wedge \\
&\quad \exists. (\gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) \text{ s.t.} \\
&\quad \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2 \xrightarrow{*} \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2 \wedge \\
&\quad (\gamma'_1 \mid \text{Md}' \mid n'_1 \mid \mathbf{NV}'_1 \mid \mathbf{V}'_1 \mid c'_1, \gamma'_2 \mid \text{Md}' \mid \infty \mid \mathbf{NV}'_2 \mid \mathbf{V}'_2 \mid c'_2) \in \mathcal{V}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1}\} \\
\mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^0 &= \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2)\}
\end{aligned}$$

Value Relation

$$\begin{aligned}
\mathcal{V}[\![\uparrow \text{unit}]\!]^m &= \{(\gamma \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \text{skip}, \gamma \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \text{skip}) \text{ s.t. } \mathbf{NV}_1 = \mathbf{NV}_2\} \\
\mathcal{V}[\![\downarrow \uparrow \text{unit}]\!]^m &= \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid \text{skip}, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid \text{skip}) \text{ s.t.} \\
&\quad \text{Commit}(\gamma_i \mid \text{Md} \mid \mathbf{NV}_i \mid \mathbf{V}_i) = \gamma'_i \mid \mathbf{NV}'_i \wedge \\
&\quad (\gamma'_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}'_1 \mid \text{skip}, \gamma'_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}'_2 \mid \text{skip}) \in \mathcal{V}[\![\uparrow \text{unit}]\!]^m\} \\
\mathcal{V}[\![\uparrow \mathbf{C}_{\text{unit}}]\!]^m &= \{(\gamma_1 \mid \text{Md} \mid n \mid \mathbf{NV}_1 \mid \uparrow \kappa, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \text{ s.t.} \\
&\quad \text{restore}(\gamma_1, \text{Md}, \mathbf{NV}_1, \kappa) = \mathbf{NV}_0 \mid \mathbf{V}_0 \mid c_0 \wedge \\
&\quad (\gamma_1 \mid \text{Md} \mid n \mid \mathbf{NV}_0 \mid \mathbf{V}_0 \mid c_0, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \in \mathcal{E}[\![\mathbf{C}_{\text{unit}}]\!]^m\} \\
\mathcal{V}[\![\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}}]\!]^m &= \{(\gamma_1 \mid \text{Md} \mid \cdot \mid \mathbf{NV}_1 \mid \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \text{ s.t.} \\
&\quad \forall n > 0. (\gamma_1 \mid \text{Md} \mid n \mid \mathbf{NV}_1 \mid \uparrow \kappa, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \in \mathcal{V}[\![\uparrow \mathbf{C}_{\text{unit}}]\!]^m\} \\
\mathcal{V}[\![\downarrow (\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}})]\!]^m &= \{(\gamma_1 \mid \text{Md} \mid \cdot \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid \downarrow \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \\
&\quad \text{s.t. } \text{PwOff}(\gamma_1, \text{Md}, \mathbf{NV}_1, \mathbf{V}_1) = \gamma'_1 \mid \mathbf{V}' \wedge \\
&\quad (\gamma'_1 \mid \text{Md} \mid \cdot \mid \mathbf{V}', \mathbf{NV}_1 \mid \varepsilon \# \text{in}(n > 0, \uparrow \kappa), \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \\
&\quad \in \mathcal{V}[\![\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}}]\!]^m\} \\
\mathcal{V}[\![\mathbf{C}_{\text{unit}}]\!]^{m+1} &= \{(\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \\
&\quad \text{s.t. either} \\
&\quad n_1 = 0 \wedge (\gamma_1 \mid \text{Md} \mid \cdot \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid \downarrow \varepsilon \# \text{in}(n_1 > 0, \uparrow c_1), \\
&\quad \quad \quad \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \in \mathcal{V}[\![\downarrow (\text{nat} \rightsquigarrow \uparrow \mathbf{C}_{\text{unit}})]\!]^m, \text{ or} \\
&\quad n_1 > 0 \wedge (\gamma_1 \mid \text{Md} \mid n_1 \mid \mathbf{NV}_1 \mid \mathbf{V}_1 \mid c_1, \gamma_2 \mid \text{Md} \mid \infty \mid \mathbf{NV}_2 \mid \mathbf{V}_2 \mid c_2) \\
&\quad \quad \quad \in \mathcal{V}[\![\downarrow \uparrow \text{unit}]\!]^m\}
\end{aligned}$$

Semantic typing

$$\frac{\text{jit} \mid b \geq 0 : \mathbf{nat} \mid \Omega; \cdot \Vdash c \leq c : \mathbf{C}_{\mathbf{unit}} \quad b : \mathbf{nat} \mid \Omega \Vdash p : \mathbf{\uparrow C}_{\mathbf{unit}}}{b : \mathbf{nat} \mid \Omega \Vdash c; p : \mathbf{\uparrow C}_{\mathbf{unit}}} \text{ (P-SEQ-SEMANTIC)}$$

$$\frac{\Omega_0 \mid \Sigma_0 = \mathbf{InitWorld}_t(\Omega; \rho) \quad \mathbf{aID}(c_0) \mid b \geq 0 : \mathbf{nat} \mid \Omega_0; \Sigma_0 \Vdash c_0 \leq c_0 : \mathbf{C}_{\mathbf{unit}} \quad b : \mathbf{nat} \mid \Omega \Vdash p : \mathbf{\uparrow C}_{\mathbf{unit}}}{b : \mathbf{nat} \mid \Omega \Vdash \mathbf{Ckpt}[\mathbf{aID}, \rho](c_0); p : \mathbf{\uparrow C}_{\mathbf{unit}}} \text{ (P-CKPT-SEMANTIC)}$$